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# IDT<sup>®</sup> Tsi381 Design Notes

80E2000\_ER001\_02

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## About this Document

This document describes design notes for the Tsi381.

### Revision History

#### 80E2000\_DN001\_02, Formal, September 2009

This document was rebranded as IDT. It does not include any technical changes.

#### 80E2000\_DN001\_01, Formal, February 2008

This document contains design note information that used to reside in the *Tsi381 Device Errata*. No new information was introduced in this document.

## Design Notes

Design Note	Applicability	
	Tsi381-66IL	Tsi381-66LV
"Redefinition of TEST_SPARE[2] pin"	✓	✓

### Redefinition of TEST\_SPARE[2] pin

The TEST\_SPARE[2] pin has been redefined to provide users with additional flexibility for parking the PCI bus when it is idle. When TEST\_SPARE[2] is tied low, the PCI bus arbiter will park the bus on the last master. When TEST\_SPARE[2] is tied high or is left as a no connect, the PCI bus arbiter will park the bus on the Tsi381.



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