

# RL78/L12 Group

# Renesas Promotional Board Demo User Manual

RENESAS MCU RL78 Family / L12 Series

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The product generates, uses, and can radiate radio frequency energy and may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment causes harmful interference to radio or television reception, which can be determined by turning the equipment off or on, you are encouraged to try to correct the interference by one or more of the following measures;

- · ensure attached cables do not lie across the equipment
- · reorient the receiving antenna
- · increase the distance between the equipment and the receiver
- · connect the equipment into an outlet on a circuit different from that which the receiver is connected
- · power down the equipment when not in use
- consult the dealer or an experienced radio/TV technician for help NOTE: It is recommended that wherever possible shielded interface cables are used.

The product is potentially susceptible to certain EMC phenomena. To mitigate against them it is recommended that the following measures be undertaken;

- The user is advised that mobile phones should not be used within 10m of the product when in use.
- The user is advised to take ESD precautions when handling the equipment.

The Renesas Promotional Board does not represent an ideal reference design for an end product and does not fulfil the regulatory standards for an end product.

# How to Use This Manual

#### (1) Purpose and Target Readers

This manual is designed to provide the user with an understanding of the hardware functions and electrical characteristics of the MCU. It is intended for users designing application systems incorporating the MCU. A basic knowledge of electric circuits, logical circuits, and MCUs is necessary in order to use this manual. The manual comprises an overview of the product; descriptions of the CPU, system control functions, peripheral functions, and electrical characteristics; and usage notes.

Particular attention should be paid to the precautionary notes when using the manual. These notes occur within the body of the text, at the end of each section, and in the Usage Notes section.

The revision history summarizes the locations of revisions and additions. It does not list all revisions. Refer to the text of the manual for details.

The following documents apply to the RL78/L12 Group. Make sure to refer to the latest versions of these documents. The newest versions of the documents listed may be obtained from the Renesas Electronics Web site.

Document Type	Description	Document Title	Document No.
Data Sheet	Hardware overview and electrical characteristics	RL78/L12 Group Datasheet	R01DS0157EJ
User's manual for Hardware	Hardware specifications (pin assignments, memory maps, peripheral function specifications, electrical characteristics, timing charts) and operation description. Note: Refer to the application notes for details on using peripheral functions.	RL78/L12 User's Manual for Hardware	R01UH0330EJ
User's manual for Promotional Board	Description of how to set-up the board, debugging using e2studio and connecting to the GUI.	RL78/L12 Promotional Board User's Manual	R20UT2494EG
User's manual for Software	Description of CPU instruction set	RL78 Series User's Manual for Software	R01US0015EJ
Quick Start Guide	Provides simple instructions to setup the RPB and run the sample code.	Quick Start Guide	R20UT2495EG

#### (2) List of Abbreviations and Acronyms

Abbreviation	Full Form
bps	bits per second
CRC	Cyclic Redundancy Check
DMAC	Direct Memory Access Controller
IDE	Integrated Development Environment
I/O	Input/Output
NC	Non-Connect
RFP	Renesas Flash Programmer
RTC	Real Time Clock
SAU	Serial Array Unit
SFR	Special Function Register
TAU	Timer Array Unit
UART	Universal Asynchronous Receiver/Transmitter

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# Chapter 1 Introduction

The YRPBRL78L12 is a promotional board for the new Renesas RL78 microcontroller family. It supports On-Board debugging, flash programming, and is pre-programmed to work with the GUI provided on the DVD to demonstrate the low power capabilities of the Renesas RL78 MCU.

## 1.1 Main features of YRPBRL78L12

- Easy to use device demonstration capabilities YRPBRL78L12 contains elements to easily demonstrate simple I/O-functions (i.e. LED output, I/O lines, UART serial interface) together with the key functions such as Real Time Clock (RTC), ADC, DMAC, ELC, Timers and safety functions.
- Power supply via USB interface YRPBRL78L12 is powered via USB interface. No separate power supply is needed although a connector is provided to supply an external power supply if needed.
- Renesas Flash Programmer V2.00.01, Flash programming software

Windows based Flash programming software allows the user to select and download application programs to the YRPBRL78L12 LCD board for evaluation purposes.

Caution:

Applilet3 is a device driver code generator software tool used to generate device driver code for initializing and using on-chip peripherals. Please note that the Renesas Applilet3 for RL78/L12 V1.00.00 software is at a preliminary version an can be downloaded from the Renesas website. We recommend not upgrading these software tools in order to guarantee operation with the board. Documentation is not supplied for preliminary versions, but can be downloaded from the Renesas website once final versions are released.

Any of the software supplied with this product can be updated from the Renesas Tools web site. However please be aware that while every effort is made to maintain compatibility between revisions, it is not guaranteed that new software versions will be backward compatible with previous releases of the YRPBRL78L12 LCD board.

- Various input / output signals available, such as
  - $\circ~$  Virtual UART interface, via the  $\mu PD78F0730~78K0~8\mathchar`-bit$  microcontroller with on-board USB interface
  - o 3 user LEDs are connected to the RL78/L12 ports for visualisation.
- Full documentation is included for all of the released Renesas software tools and the RL78/L12 device.

*YRPBRL78L12* is not intended for code development. Renesas does not allow and does not support in any way any attempt to use *YRPBRL78L12* in a commercial or technical product.



## **1.2 System requirements**

HOST PC	A PC supporting Windows XP, Windows Vista or 7 is required for the GUI and development tools installation. A Pentium processor with at least 1 GHz CPU performance, with at least 256 Mbytes of RAM, allowing you to fully utilize and take advantage of the product features. 350 Mbytes of free disk space and an additional 10 Mbytes of free disk space on the Windows system drive.
	A web browser
Display Properties	Display with 1024x768 minimum resolution. Normal size (96Dpi) Display Dpi settings.
Host interface	USB interface that enables communication based on USB (Ver1.1 or later)

#### **1.3 Package contents**

- YRPBRL78L12
- USB Type A / Mini-B cable
- Potentiometer shaft to adjust the potentiometer voltage
- DVD containing all the software, tools and documentation needed to quickly start evaluating the product.

If any part is missing or seems to be damaged, please contact the dealer from whom you received your *YRPBRL78L12* promotional board.



# Chapter 2 YRPBRL78L12 System Configuration

The YRPBRL78L12 system configuration is given in the diagram below:



Figure 1: YRPBRL78L12 System Configuration

## 2.1 YRPBRL78L12

*YRPBRL78L12* is a promotional board for evaluating the new Renesas RL78/L12 family of devices. The device used is the RL78/L12 (R5F10RLCAFB). The board can be directly connected to the host system via a USB port. The host system allows execution of the Demonstration program on the RL78/L12 device in combination with the supplied demonstration GUI.

As default, the RL78/L12 is using the 24 MHz internal high-speed oscillator, but the YRPBRL78L12 LCD board is provided with a connection for an external 20 MHz oscillator (not fitted) if the user wishes to use a different clock frequency than that provided by the internal high speed oscillator. An external Seiko Instruments low power 32.768 KHz resonator is provided for the sub-clock.

## 2.2 Host Computer

The USB host interface enables communication to the YRPBRL78L12 LCD board. The  $\mu$ PD78F0730 78K0 8-bit microcontroller with on-chip USB interface and the Renesas Electronics virtual UART driver allows application software to access the USB device in the same way as it would access a standard RS232 interface. The Renesas Electronics virtual UART driver appears to the windows system as an extra Com Port, in addition to any existing hardware Com Ports.

# 2.3 Power Supply via USB interface

YRPBRL78L12 is powered by USB interface therefore no separate power supply is required.



# Chapter 3 YRPBRL78L12 Components

The *YRPBRL78L12 LCD board* is equipped with a USB connector and with several jumpers in order to be connected to host computers, E1 emulator or any external target hardware.



Very Low Power Seiko 32KHz Resonator



Figure 2: YRPBRL78L12 LCD board Components

Some of the *YRPBRL78L12* components are available for user application hardware and software. Please read the user's manual of the RL78/L12 device carefully to get information about the electrical specification of the available I/O ports before you connect any external signal to the *YRPBRL78L12 LCD board*.

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### 3.1 Configuration Switches SW4 and SW5

Switches SW4 and SW5 control the different operating modes of the YRPBRL78L12 LCD board.

#### 3.1.1 GUI Demonstration Mode / Virtual UART Mode

The default operation of the board is set for use with the GUI using the virtual UART. Jumpers should be set as shown in the table below

Jumpers	Configuration
SW4	2
SW5	2

Table 1: Switch configuration for GUI Demonstration Mode

#### 3.1.2 On-Board Debug (OCD) and Flash Programming Modes

The *YRPBRL78L12* promotional board supports on-board debug mode achieved by a dedicated monitor running on the RL78/L12 device. By using the e<sup>2</sup>studio

IDE, flash programming and standard debug functions i.e. code execution,

single stepping, software breakpoints, memory manipulation etc. are supported.

Configure switches SW4 and SW5 as following to use the on-board debug or Flash programming mode:

Jumpers	Configuration
SW4	1
SW5	1

Table 2: Switch Configuration for On-Board Debug / Flash Programming Mode



Figure 3: YRPBRL78L12 On-chip Debugger Jumper Settings

For details on debugging in e<sup>2</sup>studio please refer to **Chapter 8 – Debugging in e<sup>2</sup>studio** of this document.



## 3.2 Power LED

The Power LED is activated if power is supplied to the *YRPBRL78L12 LCD board*.

#### 3.3 User's LEDs

The user LEDs connected and controlled by the RL78/L12.

LED	Port Pin
LED0	P130
LED1	P21
LED2	P125

Table 3: LEDs Available for User's Control

## 3.4 External Power Supply CN6

External power can be supplied by connecting a regulated 5V DC to the connector CN6 (not assembled).

The operation of the board is as follows:

- USB power only

Or

- External supply only (Board can only be operated as stand-alone only, no USB power connection).

When external power supply (EXT\_5V) is used, make sure that you follow these guidelines:

- EXT\_5V can only be used if the board is not connected to the PC

- USB connection is not permitted (if both USB and external power supplies are used, then it can damage the board).

Please refer to the schematic and the assembly drawing of the board to locate the above components.

CN6	Function
1	EXT_5V (+)
2	GND (-)

**Table 4: External Power Supply, Connector CN6** 

Note: Pin number 1 is marked with a square on the assembly drawing

## 3.5 Current Measurement CN5

CN5 connects the power supply from the USB/external/battery to the  $V_{DD}$  and  $EV_{DD}$  pins of the RL78/L12 microcontroller. The jumper can be replaced by the two leads of a multi-meter to measure the current consumed by the RL78 device. Connect a multi-meter on CN5. To use this function please make sure that the multi-meter is connected before supplying power to the board. The current can be measured for all the different low power modes by using the demonstration GUI supplied to enter all different power and standby modes.

# 3.6 E1 Emulator Connector CN4

Connector CN4 (not fitted) is provided to allow debugging and programming of the RL78/L12 microcontroller using an E1 emulator. The Renesas Flash Programmer V2.00.01 software can be used with the E1 emulator in order to flash the RL78/L12 device with a program file such .mot or .hex. The board power supply can also be provided through this connector.

A 14-way (2 x 7) standard pitch connector needs to be mounted and connected as shown below if the E1 is to be used.



Figure 4: E1 Emulator Connection

## 3.7 Mini-B USB Interface Connector USB

The mini-B USB connector allows connecting e<sup>2</sup>studio to the *YRPBRL78L12 LCD board* in order to debug or program application software to the RL78/L12 device. The board power supply is also provided by this connector.

Additionally connector USB connects UART2 of the RL78/L12 device to the host system.



Figure 5: Connector USB, Mini USB B Type Connector Pin Configuration

USB Connector USB	Signal Name
1	V <sub>BUS</sub>
2	D-
3	D+
4	NC
5	GND

Table 5: Pin Configuration of mini-B USB Connector USB

For connection with the host machine, YRPBRL78L12 can be plugged directly into an available USB port.

## 3.8 Seiko Instruments Inc. Low Power 32 KHz Resonator

The *YRPBRL78L12 LCD board* is fitted with an external 32 KHz resonator (X2) characterized specifically for the RL78/L12 device supplied by Seiko Instruments Inc.

This is a specially designed low power resonator designed to support the very low power consumption of the RL78/L12 family.

For more technical details on this device and contact details for Seiko Instruments, please refer to chapter 13.

## 3.9 RL78/L12 Memory Map

The RL78/L12 memory layout is shown in the table below.

	0xFFFFF 0xFFF00	SFR Area 256 bytes	Free for user application
	0xFFEDF 0xFF900	Internal RAM 1.5KB	software
	0xF7FFF 0xF2000	Mirror 24 KB	
s area	0xF1FFF 0xF1000	Data flash memory 2 KB	
Address area	0xF0FFF 0xF0800	Access prohibited area	
	0xF07FF 0xF0000	2nd SFR 2 KB	Free for user application software
	0xEFFFF 0x08000	Access prohibited area	
	0x07FFF 0x00000	32 KB code flash memory	Free for user application Software*

Table 6: RL78/L12 Memory Map

\* Note: In debug mode the end address for the user application software is 0x07DFF.



# Chapter 4 Getting Started

The default setting of the *YRPBRL78L12 LCD board* is set for demonstration use with the GUI. The DVD included and quick start guide will take the user through the installation procedure

The Quick Start Guide will automatically open after the installation process to guide the user through the initial setup and connection to the PC and GUI.

Please note as the communication interface between the host computer and the *YRPBRL78L12 LCD board* is the USB port interface, the software must be installed properly **before** the board is connected to the PC.

#### 4.1 Installed Contents

The installer will copy and install the contents of the DVD on the host PC.

The contents copied and installed from the DVD will appear on the "Start à All Programs" menu.

Senesas Electronics Tools	
STREAM YRPBRL78L12	
R YRPBRL78L12-Demo	GUI Software
🖼 Manuals	Shortcuts to Documents
🖼 Renesas Electronics e2studio	
e <sup>2</sup> Renesas e2studio	e <sup>2</sup> studio IDE

Table 7: Contents Installed on Start à All Programs



# Chapter 5 Hardware Installation

Before connecting the *YRPBRL78L12 LCD board* to the host PC via USB cable, ensure all the software is correctly installed first (see chapter 6).

If a multi-meter is available, remove the jumper shorting CN5 and connect the multi-meter on CN5 as indicated below.



Figure 6: Connecting an ammeter



# Chapter 6 Software Installation

The *YRPBRL78L12* package comes with several software demonstration packages:

- e<sup>2</sup>studio IDE, including the GNURL78 compiler, assembler, linker, library generator, Renesas GDB Server and E1 emulator drivers.
- Projects built for use with the Renesas GDB Server.
- o GUI to be run with the main demonstration programs.
- Source code for the demonstration and debugger programs.

The  $e^2$ studio work environment must be installed on your PC. For detailed installation hints, refer to the following chapters and to the corresponding  $e^2$ studio documentation.

#### 6.1 Sample Programs Installation

The DVD installer will automatically install the sample debugger programs and the demonstration source. The debugging project and demo source code project can be found in the following location:

C:\Workspace\RPB\RL78L12\Sample Projects

#### 6.1.1 Confirmation of USB Driver Installation

After installing the USB driver, check that the driver has been installed correctly, according to the procedure below.

For Windows 7 and Vista:

Open "Device Manager" by clicking:

[Start] à [Control Panel] à [Hardware and Sound] à [Device Manager]

For Windows XP (Control Panel Classic View):

[Start] à [Control Panel] à [Performance and Maintenance] à [System] à [Hardware] à [Device Manager]

When the *YRPBRL78L12 LCD board* is connected to the host PC, the "Renesas Electronics Starter Kit Virtual UART" (without "?" mark) should be present under the section "Ports (COM & LPT)".



The screen below shows that the COM port number is "COM9".

Changing the USB port connection will result in the COM port number changing as well. Refer to Chapter 10 for notes on manually changing the COM port number.



Figure 7: Device Manager

"Hardware Modification Scan" should not be executed whist communicating with the target device. Please refer to Chapter 9 Troubleshoot, if the driver fails to install correctly.



# Chapter 7 Renesas Flash Programmer

This chapter explains the basic operations of the Renesas Flash Programmer RFP V2.00.01 GUI for programming the *YRPBRL78L12 LCD board*. This chapter covers how to start the system, execute the EP command (Erase, Program), and program the target RL78/L12 device.

The Renesas Flash programmer is not installed by default. The executable file needed to install the programmer can be found in the Renesas Flash Programmer folder, in the root folder of the DVD. The executable filename is:

Renesas\_Flash\_Programmer\_Package\_V20001\_free.exe

The filename will not appear with the .exe extension if file extension displaying is turned off.

The conditions of the series of operations described in this chapter are as follows:

Hardware Configuration of YRPBRL78L12

Board	:	YRPBRL78L12
CPU	:	RL78/L12
Target Device	:	R5F10RLC
Voltage Level	:	5V

A binary file can be generated by compiling the 'GUI Demo Source Code' project in e<sup>2</sup>studio (see chapter 8).

<1> Installing the RFP V2.00.01 GUI

The following guides assume the Renesas Flash Programmer V2.00.01 GUI software has been installed on the host machine you are using:

Connect a USB cable between the YRPBRL78L12 and the host PC (see figure below).



Figure 8: YRPBRL78L12 to Host PC Connection Diagram



Start the Renesas Flash Programmer V2.00 GUI from the start menu:

[Start]à [All Programs]à [Renesas Electronics Utilities]à [Programming Tools]à [Renesas Flash Programmer V2.00]

(5.1) Select [Create new workspace] and click button Next.

⑦ Full mode	
	Next Cancel
	Full mode

Figure 9: « RFP Welcome » Dialog Box

(5.2) from the menu select [Microcontroller] **à** [RL78]. A list off all supported devices will appear in the target Microcontroller. Select [R5F10RLC].

(5.3) give the workspace and the project a name. The RFP V2.00.01 will, by default, save the workspace to the following location:

C:\Documents and Settings\User\_Name\Local Settings\Application Data\Renesas Flash Programmer\V2.00.01. The User\_Name being your logging username to the host PC. Click the Next button.

Create new workspace			x
<u>M</u> icrocontroller: <u>F</u> ilter: Using Target Microcontroler:	RL78	•	
Group	Device Name	Information	-
BL78/L12	B5F10BJA		
RL78/L12	R5F10RJC		
RL78/L12	R5F10RLA		
RL78/L12	R5F10RLC		
RL78/L13	R5F10WLA		
RL78/L13	R5F10WLC		
RL78/L13	R5F10WLD		
RL78/L13	R5F10WLE		
RL78/L13	R5F10WLF		
RL78/L13	R5F10WLG		
BL 78/L13	R5F1/fw/MA		Ŧ
Workspace Name:			_
Project Name:			
Folder:	C:\Users\ifuraM\AppData\Loca	al\Renesas Flash Programme Browse.	
		<u>N</u> ext <u>C</u> ancel	

**Figure 10: Device Selection** 

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(5.4) From the "Tool" list box, select the communication port that matches the host machine being used. Click button Next.

🖌 Renesas Flash Programmer (Unsupported Freeware Version) 💷 📼 🖾
File Tool Microcontroller Help
Micro controller:
Communication Interface
Tool: E1 E20 COM1 COM3
Back Next Cancel
Ciear Output Panel

Figure 11: Port Selection

(5.5) The Power Supply dialog box appears. Click button Next.

le	Tool Microcontroller Help
I	Micro controller:
F	Power Supply
	Power supply from the emulator
	Power target from the emulator.
l	Supply voltage: O 3.3V O 5.0V
1	Additional Settings
	Wide Voltage Mode
Т	Input Voltage: 5.00 [V]
	Back Next Cancel
L	

Figure 12: « Information Settings » Dialog Box

(5.5) The following information settings window will appear with all the default parameters already set by the GUI. Click button Complete

<u>File T</u> ool <u>M</u> icrocontroller <u>H</u>	elp	
ect Settings		
Basic Settings Other Settings		
Device Information File		
File name	R5F10RLCpr5	
File version	V1.00	
🗆 Target		
Interface	UART-ch0	
Communication transfer rate	115,200bps	
Supply oscillator	Target	
Frequency [MHz]	Internal-OSC	
Multiply rate	1.00	
Supply power	5.00V	
🗆 Using Tool		
Tool name.	COM9	
Tool name. The tool name of flash programmer is	indicated.	
<u>M</u> odify		
-		Complete <u>C</u> ancel

Figure 13: « Information Settings » Dialog Box

<6> Selecting a user program

In the RFP main window click [Browse]. Select a program file to be written to the target device. In this case the YRPBRL78L12\_LCD\_Demo\_SW.mot file is used, however RFP supports .hex, .mot, .s and .rec binary file types. This can be found in the following project folder:

C:\Workspace\...\SampleProjects\GUI Demo Source Code\YRPBRL78L12\_LCD\_Demo\_SW\HardwareDebug

Note: The projects found in the location C:\Workspace\...\SampleProjects should not be modified.

Renesas Flash Programm	ner (Unsupported Freeware Version)			٢
File Tool Microcon	troller Help			
Microcontroller:	R5F1 ORLC			
User/Data area:			Browse	
User Boot area:			Browse	
Command:	Auto pro cedure(E.P)			
	Start			
This software is the no charge free version. This is unsupported.				
		Cle	ar Output Panel	

Figure 14: Selecting a user program

<7> Auto procedure (EP) command execution

Click the button Start.

When the Start button is Clicked, Blank Check ® Erase ® Program are executed sequentially for the RL78/L12 device. The result of the operation is displayed at the end.

Microcontroller:	R5 F1 ORLC	
User/Data area:	YRPBRL78L12_LCD_Demo_SW.mot	Browse
User Boot area:		Browse
Command:	Autoprocedure(E.P)	
Program Data flash: 10%		^
	PASS	
10% 20% 30% 50%		
60% 70%		
80%		
100% PASS		

Figure 15: Flash Programming

#### <8> Terminating the GUI

Select [File] à [Exit] to terminate the GUI software. All settings executed are saved, so that those settings can be reused when the RFP GUI is restarted.

#### <10> Restarting the GUI

When the Renesas Flash Programmer V2.00.01 GUI is restarted, simply reopen the latest saved workspace.



# Chapter 8 Debugging in e<sup>2</sup>studio

<1> Create Workspace

The two projects provided can be debugged in e<sup>2</sup>studio using the on-board debugging feature via a USB cable connected to CN2 (USB connector). Ensure to configure the board as shown in Table 2 (Section 3.1.2). Debugging can also be achieved using an E1 (not supplied) connected to CN4 (not fitted).

The project located in the folder 'Debugging Project' does not require a demo GUI and allows the user to debug the YRPBRL78L12 hardware. The project located in the folder 'GUI Demo Source Code' cannot be fully debugged without connecting to the demo GUI. Connecting to the GUI is not possible when debugging using the on-board debug feature. The steps provided in this section use the Debugging Project to help the user to familiarise with the e<sup>2</sup>studio debugging environment.

Start e<sup>2</sup>studio by selecting it from the Start Menu:

Start > All Programs > Renesas Electronics e2studio > Renesas e2studio

The first dialog that will appear will be the Workspace Launcher. Click 'Browse' and select a suitable location to store your workspace, using the 'Create New Folder' option as necessary. Click 'OK'.



Click 'Yes' when presented with the 'Administrator Privilege' dialog.



The e<sup>2</sup>studio welcome splash screen will appear. Click the 'Go to the workbench' arrow button on the far right.





#### <2> Import Project

Once the e<sup>2</sup>studio environment has initialised, right click in the project explorer window and click 'Import...'.



The Import dialog will now show. Expand the General folder icon, and select "Existing Projects into Workspace", then click 'Next'.

e2 Import	
Select Create new projects from an archive file or directory.	Ľ
Select an import source:	
type filter text	
<ul> <li>✓ General</li> <li>Marchive File     <li>✓ Existing Projects into Workspace     <li>File System     <li>HEW Project     <li>Preferences     <li>C/C++     <li>CVS     <li>Run/Debug     <li>Team     </li> </li></li></li></li></li></li></li></li></ul>	
A Back Next >	<u>Einish</u> Cancel

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Select "Select archive file:"

The import dialog will now allow you to specify the project to import.

e <sup>2</sup> Import		
Import Projects Select a directory to search for existing Eclipse projects.		
Select root directory:		Browse Browse
Projects:		Select All Deselect All Refresh
Copy projects into workspace Working sets Add project to working sets Working sets:	•	Select
(?) (Back Next > Fi	nish	Cancel

Click 'Browse'.

The import dialog will open a folder browser to allow you to specify the project to import. Locate the master copy of the *YRPBRL78L12* Debugging Project from:

C:\Workspace\RPB\RL78L12\Sample Projects

Select the "YRPBRL78L12\_e2studio\_Debugging\_Project.zip" file.

Click "Open".

Click "Finish" on the import dialog to complete the import procedure.

#### <3> Configure Debugger Connection

Once the project has been imported, click the arrow to the right of the bug symbol to open the debugger configuration menu, and then select 'Debug Configurations'.



The 'Debug Configurations' dialog will appear. Click the small arrow next to 'Renesas GDB Hardware Debugging' option to reveal the pre-configured 'YRPBRL78L12\_e2studio\_Debugging\_Project' configuration.



Click on the 'YRPBRL78L12\_e2studio\_Debugging\_Project' configuration to view the configurations.

Click the 'Debugger' tab, check the settings match those shown in the image below. Make sure the 'Debug Hardware' drop down menu is set to 'E1 (RL78)', and the target device to 'R5F10RLC'. The rest of the settings are as follows:

Main Clock Frequency[MHz]	Using Internal Clock		
Sub Clock Frequency[kHz]	32.768		
Monitor Clock	System		
Communication Method	1 Line Type (TOOL0)		
Power Target From The Emulator	Yes		
Supply Voltage	5.0V		



2 Debug Configurations			Х
Create, manage, and run configurations Erase Flash on Start is Enabled. Please Disable this option after sucessfu	I connection.	T.	ř.
Ymail       Image: Second Secon	GDB Settings Connection Settings Debug To Clock Main Clock Frequency[MHz] Sub Clock Frequency[MHz] Monitor Clock		
<ul> <li>Renesas GDB Hardware Attach</li> <li>Renesas GDB Hardware Debugging</li> <li>RPBRL78L12_e2studio_Debugging_Project</li> <li>Renesas GDB Simulator Debugging</li> </ul>	Connection with Target Board Communication Method Power Target From The Emulator (MAX Supply Voltage Flash Current Security ID (HEX) Permit Flash Programming Use Wride Voltage Mode Erase Flash ROM When Starting	LLine Type (TOOL0) - Yes - 5.0V - 00000000000000000 Yes - Yes - Yes - Yes -	Е
Filter matched 12 of 12 items		Apply Revert Debug Close	•

<4> Build and Download the Sample Code

Once the debugger configuration has been verified, click 'Close' and select 'Cancel' if prompted to save the settings.

Click on the small arrow next to the 'Manage Configurations' button (sundial) to show the current build configuration. Ensure that 'HardwareDebug' is selected.

<mark>e2</mark> C/0	C++ -	e2studio								
<u>F</u> ile	<u>E</u> dit	<u>S</u> ource	Refactor	<u>N</u> avigate	Se <u>a</u> rch	<u>P</u> roject	<u>R</u> un	<u>W</u> indow	<u>H</u> elp	
	-	R &		2 🖉	<u>c</u>	🚳 👻 🖸	- 0	) - I 🔨	- 📎	- 🎄 - 🜔 - 💁 -
P 🗗	roject	Explorer	X		Ē	\$₽ ~ -			<ul> <li>✓</li> </ul>	1 HardwareDebug
Þ	🗳 yrf	BRL78L12	2_e2studio_	Debugging	_Project					2 Release

Click on the small arrow next to the 'Build Configuration' button (hammer) to show the build configuration menu. Ensure that 'HardwareDebug' is selected. Clicking this will start the project build process.

File Edit Source Refactor Navigate Search Project Run Windo	ow Help
📑 • 🕞 🕞 💼 😖 🥖 🤠 • 😂 • 😋 •	<u>≪</u> • ⊛ • 🔅 • 🜔 • 💁 •
Project Explorer 🛛 📄 🔄 🖓 🖓 🖓	✓ 1 HardwareDebug
> 😂 YRPBRL78L12_e2studio_Debugging_Project	2 Release

Once the sample code has been built, click the bug icon to download the code to the target device and begin debugging. e<sup>2</sup>studio may show a security alert dialog, select the option that allows e<sup>2</sup>-server-gdb access. Another dialog may be shown asking to change to the 'Renesas Debugging Perspective'. Select the 'Remember my decision' tick-box to prevent this message appearing each time e<sup>2</sup>studio connects to the target device – then click 'Yes'.



<5> Running and Debugging the Sample Code

The target device should now be programmed, ready for debugging. The debug command buttons will appear.



Open the Show View dialog from the menu bar:

Window > Show View > Other

Double-click on 'Debug' to expand the folder view.

Select 'Disassembly'.

Click 'OK'.

The program counter's position is indicated by the circled arrow and the instruction is also highlighted as shown below:



Click on the Project Explorer tab. Click on the arrow to the left of the YRPBRL78L12\_e2studio\_Debugging\_Project folder to expand the folder.





Expand the src folder to reveal the list of source files. Double click the hardware\_setup.c file to open it. Alternatively, right click on the file and select 'Open'.

📴 Outline 陷 Project Explorer 🛛	💷 Disassembly	<b>□</b> 🔄 ▽ 🗆 🗆				
a 😤 YRPBRL78L12_e2studio_Debugging_Project						
Binaries						
Archives						
Includes						
🔈 🗁 HardwareDebug						
Release						
🔺 🗁 src						
👂 📂 include						
🔺 🗁 source						
> 🗁 startup						
hardware_setun.c						
Interrupt_har	New	•				
🕨 🕞 Icd_panel.c	Onen					
▷ 1 r_cg_adc_use	Open					
⊳ 💼 r_cg_adc.c	Open With	•				

Insert a break point on the R\_Systeminit() function call inside the HardwareSetup function, by double clicking in the blue section on the left hand side of the source code window.



Note:

For the HardwareSetup function to be executed, a macro named 'RELEASE' needs to be defined. This macro has been defined in the 'reset\_program.asm' file.

Click the 'Restart' button, circled on the left, to restart the target device and code execution. The 'Resume' button, circled on the right, can also be used to resume code execution.





Program execution will stop at the break point. The main window will show the program counter indicator on the line of code with the break point.

469	) / * * * * * * * * * * * * * * * * * *
47	* Function Name: HardwareSetup
48	* Description : This function calls function R_Systeminit to initialise all peripherals
49	* Arguments : None
50	* Return Value : None
51	***************************************
526	void HardwareSetup(void)
53	ť
54	/* Disable global interrupts */
55	asm("DI");
56	
57	/* Configure all peripherals */
2058	R_Systeminit();
59	
60	/* Enable global interrupts */
61	asm("EI");
62	}

Press the 'Resume' button, to run the code. It will run up to the break point, and then halt. The position of the program counter is indicated by the highlight line.

Press the 'Step In' button, to step inside the function.



The file containing the function will open, and the program counter will move to the first line of code in the function.

```
* Function Name: R Systeminit
 * Description : This function initializes
 * Arguments : None
 * Return Value : None

void R_Systeminit (void)

 {
    /* Configure port pin assignments */
   PIOR = 0x00U;
    /* Configure the LCD */
    R_LCD_Create();
    /* Configure system clocks */
    R_CGC_Create();
    /* Configure ADC */
    R ADC Create();
```



#### <6> Advanced Debug Features

The panel in the top left of the screen can be used for debugging. Click the 'Variables' tab to view all local variables currently in scope. Click 'Breakpoints' to view and edit software breakpoints, and click 'IO Registers' to view the device SFR registers.

(×)= Variables 🛛	e Breakpoints ଜୁଟ୍ଲ Expression	ons 🟙 Registers 🔎 Eventpoints 🔲 IO Registers 🗧	
		🖾 🏘 🖂 🖉 💥 🖗	5
Name	Туре	Value	
			-
			•

To view global variables, click 'Window' from the top menu bar and then click 'Show View' and select 'Expressions'. This will add the 'Expressions' panel to the list of tabs. Click the plus icon, and type in the name of the global variable you want to watch.

🕪= Variables 🤷 Breakpoint	s ଙ୍କି Expression	s 🕴 👭 Registers 🥌 Eventpo	pints 📘 IO Registers	
			約 🍕 📄 🔮 🗶 🎙	ž 7
Expression	Туре	Value		~
\varTheta Add new expression				
				-
•		4		Þ.

To view memory areas directly, click 'Window' from the top menu bar and then click 'Show View' and select 'Memory'. The memory window will appear docked in the bottom section of e<sup>2</sup>studio. Click the plus button to add a new memory view.

📮 Console	🧟 Tasks	Problems	Executables	Realtii
Monitors				) × 🛬

Enter either a memory address location in hex or the name of a variable.

Enter address or expression to monitor:           0xFE900	e <sup>2</sup> Moni	tor Memory		X
0xFE900			ssion to monit	or:
	0xFE90	00		-

Click <OK>.

You can scroll the memory view up and down, to see adjacent memory locations.

🔄 Console 🧔 Tasks 🔝 Problems 🚺 Executables 🖏 Real-	time Memory 🕅	Real-time Char	t 👒 Trace 🚺	Memory 🛛	_
Monitors 🕂 🐈 💥	0xFE900 : 0xFE9	00 <hex> 😒</hex>	🕂 New Renderi	ngs	
♦ 0xFE900	Address	0 - 3	4 - 7	8 – B	C - F
	000FE900	F2FEC307	E95ECD00	CEF79E72	7D32F806
	000FE910	00000000	01000000	00000000	00000000

Note:

The projects provided with the YRPBRL78L12 were partly created using APIs generated by Applilet; a tool used to generate source and headers files containing APIs. Source files and APIs generated by Applilet have names starting with 'r\_' and 'R\_', respectively.

The version of Applilet used in the projects provide is 'Applilet3 for RL78L12 V.1.00.00' and can be downloaded from the following website;

http://www2.renesas.eu/products/micro/download/?oc=AP3-RL78L12-EE



# Chapter 9 GUI Description

The RL78/L12 demonstration GUI is Windows software application that can be used to demonstrate the key operation of the 16-bit microcontroller RL78/L12 mounted on the YRPBRL78L12 LCD board. The demonstrations allow the user to check the on-board RTC, DMAC, CRC, the memory contents (RAM), switch between the different standby modes and to check associated power consumptions, and to use the self-test functions implemented in the RL78/L12 device.

Please note that the YRPBRL78L12 needs to be running the program supplied

"YRPBRL78L12\_LCD\_Demo\_SW.mot" within the "GUI Demo Source Code" directory copied to the host PC during installation (This is programmed as default in manufacture).

Before connecting the YRPBRL78L12 LCD board, check switches SW4 and SW5 are configured correctly as stated in Table 1 (GUI Demonstration Mode).

After plugging the board to a free USB port make a note of the COM port assigned to the device from the device manager.

#### 9.1 Running the RL78/L12 Demo

<9> Execute application

Verify that SW4 and SW5 switches on the *YRPBRL78L12 LCD board* are set to Virtual UART operation mode with the following settings:

Switch	Configuration
SW4	2
SW5	2

Table 8: SW4 and SW5 Configurations for Virtual UART Operation Mode



Figure 16: SW4 and SW5 Configurations for Virtual UART Operation Mode

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From the menu "All Programs" locate the folder "Renesas Electronics Tools **à** YRPBRL78L12" within that folder select "YRPBRL78L12-Demo" to run the RL78/L12 demo as shown below. It is also possible to run the demo by double clicking on the GUI demo shortcut icon on the desktop.

When the Demo GUI is started, the COM port to which the YRPBRL78L12 LCD board is connected should be automatically detected and selected in the 'Connection' section of the "Home" tab. The GUI software should also connect to the YRPBRL78L12 LCD board and start automatically.

If for any reason this does not happen, please select the appropriate port number from the drop down list, and then press the "Connect" button.

Renesas Electronics: RL78/L12 Low Power Demonstrator	
Home Mome Data Logger 🐼 Real Time Cle	ock 🗼 Memory Demo 📧 Low Power 🔶 Self Test
- Connection	RL78 The True Low Power MCU Platform
Port: Renesas Starter Kit Virtual UART (COM7)	Low Power
[Disconnect]	
Firmware version: 1.0	Scalable RL78
LCD Demo	
<u></u>	Efficient
RTC Time: 2013/01/21 12:19:27	● ▶

Figure 17: COM Setup


# 9.2 GUI Demo Sections

The following screenshot shows the GUI demo followed by the names and short explanations of the different sections.

Renesas Electronics: RL78/L12 Low Power Demonstrator			
🛲 Home 🔣 Data Logger 🔕 Real Time Cl	lock 🚀 Memory Demo 📰 Low Power 🔶 Self Test		
Connection Port: Renesas Starter Kit Virtual UART (COM7)  Disconnect Firmware version: 1.0  LCD Demo	RL78 The True Low Power MCU Platform Low Power Scalable		
<u>Start</u>	Efficient		
RTC Time: 2013/01/21 12:19:27			

Figure 18: The RL78/L12 Demo GUI

#### <1> Information Bar

The connection status between the YRPBRL78L12 and the GUI is shown on this area. When connection is established, the bar is used to display the RTC time and date.

LCD Demo runs a simulation of an air conditioning system. A dialog will appear to indicate operation of the system after clicking "Start". The potentiometer on the *YRPBRL78L12* LCD board is used to vary the temperature between from 0 °C to +40 °C. With the temperature from 10 °C and below 30 °C, the system is in good working condition. When the temperature falls below 10 °C, the heat symbol is turned on indicating the need to increase the temperature. The word 'COLD' is also displayed. When the temperature reaches 30 °C or above, the cool symbol is turned off, indicating the need to decrease the temperature. 'HOT' can also be seen on the screen. Click "Stop" on the dialog to exit the simulation.

#### <2> Data Logger

This section displays the external voltage (measured from the potentiometer on the board) and the temperature reported by the internal temperature sensor of the RL78/L12. The measurements are performed by the 10-bit ADC of the RL78/L12.

The graphs can be re-initialized by clicking the "Clear" buttons on the right hand side of the window. Note that the graphs are re-scaled automatically when the "Clear" buttons are pressed, so a more detailed look of the reported values can be made.

#### <2> Real Time Clock

This section displays the date and time reported by the on-board RTC. The RL78/L12 RTC registers can be synchronised with the PC system date and time by clicking the "Sync" button. The user can also set and enable the interval interrupt, used to toggle LED1, and the alarm functions.

#### <3> Memory Demo

This section displays the data 64-byte RAM content written to by the DMAC and then read from the RL78/L12 RAM variable memory. An 8 byte user area can also be written and read by the user. RAM content is not lost even when the RL78/L12 enters the lowest power mode (STOP mode) providing power is not removed from the board.

#### <4> Low Power – Low Power Mode Selection:

Three different standby modes are implemented in the RL78/L12 device, HALT mode, STOP mode and the new SNOOZE mode.

Buttons are used to switch the operational mode of the RL78/L12 to the standby mode selected. The button clicked will set the RL78 into the selected low power mode and change caption to "Release". The RL78 will remain in the selected low power mode, until the Halt release button is clicked to set the RL78/L12 to the active mode. In Stop mode, a switch press on the YRPBRL78L12 LCD board is required. The Snooze mode requires the user to adjust the external potentiometer to exit the standby mode.

Detailed explanations about each of the low power modes are provided in the section 9.6 Low Power Demonstration, please refer to it.

#### <5> Self-Test Function:

This section demonstrates the different self-test functions provided within the RL78/L12. The self-test functions are switch test, RAM and SFR guard, and system clock measurement. The ELC test is also included within this tab. The interrupt from one timer is used as the clock count source for another timer.

It is important to note that tooltips are present on every buttons to guide the user through the experience of the RL78/L12 GUI Demo, and provide useful information for a good and quick understanding of the different features and peripherals demonstrated. Place the mouse cursor over a button to view the tooltip.

Connection
Port:
Renesas Starter Kit Virtual UART (COM19) 🗾 🗸
Connect
Click to connect/disconnect the RL78 to/from the PC

Figure 19: Tooltip Example



# 9.3 ADC Demonstration

This section of the GUI makes use of the ADC to measure and display the external voltage and the temperature using the RL78/L12 internal temperature sensor. The sensor is internal to the RL78/L12 device therefore the measured temperature might not reflect the room temperature. The internal reference voltage (TYP: 1.45V) is also displayed on the right hand side of the GUI window.

For all ADC data requests by the GUI, the internal reference voltage is measured and then used as a calculation scaling point for the external voltage (Potentiometer) and internal temperature calculations as shown in the example below:





Figure 20: External Voltage & Temperature Measurements

The graphs keep a history of all the reported values from the RL78/L12. In order to clear the history and get clear readings please click the "Clear" buttons. The graphs then automatically re-scale to the displayed data values. To see the external voltage changes turn the potentiometer using the potentiometer shaft included. To increase the voltage, turn the potentiometer in a clockwise direction, to decrease the value turn the potentiometer anticlockwise. The temperature can be changed by simply touching the MCU with a finger or blowing air over the RL78 device.

### 9.4 Real Time Clock Demonstration

This section of the GUI demonstrates the capability of the RL78/L12 to implement full calendar function using the internal separate registers for seconds, minutes, hours, days, days of the week, months and years. For full explanation of the on-board RTC please consult the user manual of the device.

The *YRPBRL78L12* LCD promotional board sends the value of the RTC registers to the GUI every second and the values are displayed on the GUI on the right hand side of the lower window. When the board is first connected and powered, the RTC will start with a default value. When the RTC time is synchronised with the host PC by clicking on the "Sync" button, the alarm values will be automatically set to the current date and time. The minutes is set to one minute in advance, after clicking on the "Set Alarm" button to set the alarm. Otherwise please check that alarm has a time and date in advance of the current settings. This can be done using the Days, Hours and Minutes buttons in the GUI.

To verify the time synchronisation operation, Windows Date and Time applet can be launched to check that the values displayed by the GUI demonstration are synchronised with the PC system date and time.

Renesas Electronics: RL78/L12 Low F	Power Demonstrator	
🛲 Home 🛄 Data Logger	🕓 Real Time Clock 🖌 Memory Demo	Eli Low Power 🔶 Self Test
Set RTC Time Synchronize the RL78 RTC registers with the Host system clock S <u>v</u> nc	Alarm Function Set and use the alarm function of the RL78 Alarm time: Hours: D. Minutes: D Day of week: Every Day Set Alarm	Interval Interrupt Select interval: One Second 💌 Enable
RTC Time: 2013/01/21 12:00:11		9

Figure 21: On-Board RTC Operation

### 9.5 Memory Demonstration

This section of the GUI demonstrates the Data Transfer Controller operation and the RAM data retention in the lowest power mode. Note the RAM window can also be used for the RAM write protection function in the "Self Test" tab as this is part of the user write protection RAM area.

In the "Memory Content" box, using the drop-down button to select 'DMAC to RAM', the RL78/L12 enables transfers of the external voltage (2 bytes) and internal temperature's hexadecimal values starts writing 4 bytes of data into the RAM memory for every ADC measurement.

The 4 bytes of data written are:

- The external voltage 2 bytes.
- The temperature value 2 bytes (The first byte represents the integer part and the second byte the fractional part of the value)

The active window shows the current 64 bytes of the RAM memory block being written into. The current 4 bytes written in the RAM memory appear in red colour. RAM memory content is not retained after the power is removed from the device, so that if you stop the GUI demonstration, unplug the *YRPBRL78L12 LCD board* and then plug it back and restart the GUI, the RAM memory content will be re-initialised, and the window will show the data being written.

User data can be written to the internal RAM of the RL78/L12 device by entering or changing the values in the write window and clicking the "Write" button.

The data is then written to a specific location within the internal RAM of the RL78/L12. To read the data back click the "Read" button and the same internal RAM location is read by the RL78/L12 and sent back to the GUI. The upper text field in white colour can be modified to enter random values to be written to the internal RAM. The lower text field displays the actual contents reported by the RL78/L12.

Renesas Electronics: RL78/L12 Low Power Demonstrator			
🛲 Home	Data Logger 🚫 Real Time Clock 🗼 Memory Demo 📰 Low P	ower 🔶 Self Test	
	Memory Content		
CRC	CRC - GUI: 0x5559 RL78: 0x5559 Mode: DMAC to RAM 💌		
RESULTS	00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 00 03 FF 00 D1 03 FF 00 D0 03 FF 00 D0 FF FF FF FF 10 FF	DMAC TRANSFER DATA	
	- Data RAM Content	_	
DATA READ FROM	00         01         02         03         04         05         06         07           Bytes to write:         00         00         00         00         00         00         00         00	DATA WRITTEN	
RAM	Bytes read: 00 00 00 00 00 00 00 00 <u>R</u> ead	RAM	
RTC Time: 2013/01/21 1	12:21:51	9	

Figure 22: DMAC and RAM Content

# 9.6 Low Power Demonstration

The RL78/L12 supports 3 low power modes that the GUI utilizes and demonstrates (HALT, STOP and SNOOZE). For more details on the power saving modes of the RL78/L12 please consult the device user manual.

When using the low power demonstration the user clicks the appropriate button in the GUI window. The RL78/L12 will enter the selected low power mode and will remain in that state until released by the GUI again. The different low power modes supported by this GUI demonstration are explained below.

Renesas Electronics: RL78/L12 Low Power Demonstrator		X
Home Data Logger 🕢 Real Time Clock	🛹 Memory Demo 📧 Low Power 🔶 s	elf Test
Low Power Modes Put the RL78 into Low Power Halt mode: Halt Put the RL78 into Low Power Stop mode: Stop Turn off LCD	Snooze Mode Set ADC trigger conditions: Voltage limits: Auto Limit Upper: 3.9 Lower: 2.9 Sample interval: One Second Put the RL78 into Snooze mode: Snooze	
RTC Time: 2012/11/13 12:18:22		

Figure 23: Low Power Mode Selection

#### 9.6.1 HALT Mode

In this mode only the operating system clock to the CPU is stopped. The system clock can still be supplied to the peripherals. The total current consumption of the system can be reduced by using this mode in combination with the normal operation mode for intermittent operation.

Renesas Electronics: RL78/L12 Low Power Demonstrator			
🖚 Home 🕅 Data Logger 🐼 Real Time Clock	🗼 Memory Demo 📨 Low Power 🔶 Self Test		
Low Power Modes Put the RL78 into Low Power Halt mode: Release Put the RL78 into Low Power Stop mode: Stop Turn off LCD	Snooze Mode Set ADC trigger conditions: Voltage limits: Auto Limit Upper: 3.9 Lower: 2.9 Sample interval: One Second Put the RL78 into Snooze mode: Snooze		
RTC Time: 2012/11/13 12:28:47			

Figure 24: HALT Mode

#### 9.6.2 STOP Mode

In this mode all the operations of the internal circuits except the sub-clock oscillator are stopped. This mode can reduce the power consumption to a level lower than all other modes. The RL78/L12 is still operating the RTC and the LCD driver using the 32.768 KHz sub-system clock. The user is given the option of turning off the LCD operation to further reduce power. A switch press is required to exit STOP mode.



Figure 25: STOP Mode

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### 9.6.3 SNOOZE Mode

The SNOOZE mode is a new mode which allows the RL78 to test for a valid "Wake Up" mode to occur, before waking the CPU. In this way the average power consumption can be reduced. The test is performed solely in hardware without having to wake the CPU each time until the valid event is met.

SNOOZE is technically part of STOP mode, but for the purposes of the demonstration the user only has to press the "SNOOZE" button to activate this demonstration.

Before activating SNOOZE mode, the user must specify the voltage window that the device is to wake up, as well as set the trigger interval of the ADC measurements. In this demonstration the RL78 has been set to wake on an external ADC voltage (Potentiometer value) that is "Outside" the specified voltage window.

SNOOZE mode alternates between STOP mode and SNOOZE mode where the system clock is supplied periodically to the ADC according to the RTC trigger intervals specified. An ADC measurement is automatically performed on the selected input channel (In our case, it is channel 2 where the potentiometer is connected). If the result of the AD conversion is outside the specified voltage range, the ADC issues an interrupt and the system clock is supplied to the CPU so that the RL78 device fully wakes up. If the ADC value is not outside the range, the CPU is not woken and the device returns to STOP mode until the next RTC trigger event.

For example, for an upper limit voltage set to 2.9V and a lower limit voltage set to 2.5V, the device will only wake up if you adjust the potentiometer voltage to a voltage below 2.5V (between 0V and 2.5V) or above 2.9V (between 2.9V and 4.8V). As this is the only way of waking the RL78 in this mode, a window will pop up to remind the user to adjust the potentiometer. To automatically set the limits, adjust the potentiometer, click on 'Auto Limit'.

Only integer values can be used when manually setting the upper and lower limits. Use the voltage increment/decrement buttons to fractionally increment/decrement the limits.

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Tooltips and set-up instructions are provided in the GUI for the user to follow the instructions given in the SNOOZE mode box before clicking the "SNOOZE" button and entering this mode.

Other wake-up options are available for the RL78 device, but are not included in this demonstration. Please refer to the RL78/L12 user manual for more details.

These are:

- a. The wake up voltage can be set to occur inside the specified voltage range
- b. CSI00 or UART0 reception



Figure 26: SNOOZE Mode

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## 9.7 Self-Test Functions Demonstration

The RL78/L12 includes several new self-test and safety hardware functions, amongst these are:

1. CRC Test

The RL78/L12's performs a Cyclic Redundancy Check on the 64-byte data transferred by the DMAC using the dedicated CRC peripheral. This can be compared to the GUI calculated CRC.

- Guard Test RAM guard function is used to protect data in the specified memory space (first 128 bytes of RAM). SFR guard function is used to protect data in the control registers used by the port function.
- Switch Test Enables the RL78/L12 to detect switch presses using interrupts. Pressing switch SW3 will also measure the switch press duration.

Each of them is demonstrated in the GUI.

1. Switch Test

The switch test function is used to poll the RL78/L12 for the status of the user switches. Click the "Enable" button to start sending switch state requests. Pressing the switches on the YRPBRL78L12 LCD board will update the GUI. (Note that there may be a delay of up to 1 second between pressing a switch and it updating the GUI).

2. Guard Test

#### RAM Guard -

The 8 bytes of RAM memory displayed in the Memory Demo tab are located in the first 128 bytes, starting from address 0xFEA00. To enable the "RAM Guard" option, click on the Self Test tab, select 'RAM Guard' and click the "Enable" button. Click on the Memory Demo tab, specify the 'Bytes to write' in the Data RAM Content section then click "Write". Attempt to read the written bytes by clicking on "Read". The bytes read from RAM will not change. Switch to the Self-Test tab and disable RAM guard. Switch back to the Memory Demo tab and attempt to read the bytes. Notice that the bytes read from RAM does not match the bytes written to RAM.

#### SFR Guard -

LED1 is toggled every second by changing the mode of the corresponding port pin between output and input. The internal pull-up resistor is enabled so that when the port pin is set as an input, the state is high (1), that is LED1 turned off. When the port pin switches to output, the output is specified as low (0) that is D2 turned on. When the SFR guard is enabled, the LED1 stops flashing as the corresponding port mode register is protected.

When the "Disable" button is pressed the RAM or SFR returns to normal, RAM data can be written and the variables in the GUI now update and the LED starts to flash as before.

#### 3. CRC

To initiate the RL78/L12's peripheral CRC, enable "DMAC to RAM" transfers. To initiate the GUI CRC, click on the "Enable" button under the CRC Test. Please refer to the "Memory Demo" tab, as the CRC check is performed on the 64 bytes of the RAM memory content displayed in this window. Each time a new set of 4 bytes is written in this 64 bytes memory window, both the GUI and the RL78/L12 calculate the CRC check and should display the same results in the Memory Demo tab.



CRC Test Guar	d Test Switch Test
Renesas Electronics: RL78/L12 Low Power Demonstrator  Real Time Clock  CRC Test Peripheral CRC: Enable  CRC Test Peripheral CRC: Enable  CRC Test Peripheral CRC: Enable	Memory Demo E Low Power Self Test
RTC Time: 2013/01/21 12:36:03	

Figure 27: Self-Test Functions



# Chapter 10 Troubleshooting

#### • In driver installation, recognition based on Plug and Play is disabled.

Cause:

The USB connector may not be inserted normally into the USB port of the personal computer.

Action:

Check that the USB connector is inserted fully into the USB port of the personal computer.

Alternatively, disconnect the USB connector, and then insert the USB connector again after a while.

# • In checking by Device Manager, "USB Serial Port" or "USB High Speed Serial Converter" is not displayed. Alternatively, the "!" or "´" is prefixed

Cause:

The USB connector may not be inserted normally into the USB port of the personal computer.

Action:

Check that the USB connector is inserted fully into the USB port of the personal computer.

Alternatively, disconnect the USB connector from the USB port, then insert the USB connector again after a while.

Cause:

The driver may not have installed correctly.

Action:

- <1> When this product is connected to the personal computer, right-click the driver marked with "!" or "´". Click Erase when displayed.
- <2> In Device Manager, execute [Hardware Modification Scan].
- <3> Install the driver again with Plug and Play.

Cause:

The device may not be recognized (in the case of connection with the USB hub).

Action:

Try the following:

- · Disconnect the USB connector, and then insert the USB connector again.
- Connect the USB connector to another port of the USB hub.

If the same symptom occurs, do not use the USB hub, but directly connect the connector to the USB port of the personal computer.

#### • Manually changing the COM port.

Cause:

The GUI is not able to establish connection to the YRPBRL78L12 demo board when the board is connected to the host PC via a USB cable and the assigned virtual COM port number is bigger than 9.

Action:

- <1> Open Device Manager.
- <2> Locate 'Renesas Starter Kit Virtual UART' under Ports (COM &LPT).
- <3> Right-click on 'Renesas Starter Kit Virtual UART' and select Properties. Alternatively, double-click on 'Renesas Starter Kit Virtual UART'.
- <4> Select the Port Settings tab.
- <5> Click on 'Advanced...'.
- <6> Select an available COM port number lower than 10.
- <7> Click OK.
- <8> Click OK on the Properties dialog.
- <9> Close Device Manager.
- <10> Disconnect then reconnect the USB cable.

# • When this product is connected with a personal computer, the "Add New Hardware Wizard" screen is displayed

Cause:

If the USB connector of this product is not inserted into the USB port used at the installation time but into another USB port, this product may be recognized as a new hardware item.

Action:

#### Install the driver by referring to Chapter 6 - Software Installation

Alternatively, the driver can be installed manually by following the provided guideline below.

#### · Installation on Windows XP

<1>

When the *YRPBRL78L12 LCD board* is connected to the host machine, the board is recognized by "Plug and Play", and the wizard for finding new hardware is started.

Select "No, not this time" and click Next >.

Found New Hardware Wizard		
	Welcome to the Found New Hardware Wizard         Windows will search for current and updated software by looking on your computer, on the hardware installation CD, or on the Windows Update Web site (with your permission).         Read our privacy policy         Can Windows connect to Windows Update to search for software?         O Yes, this time only         Yes, now and every time I connect a device         No, not this time	
	Click Next to continue.	

Figure 28: Found New Hardware Wizard (Windows XP)

Select "Install from a list or a specific location (Advanced)" and click Next >.



Figure 29: Install from a Specific Location (Windows XP)

<2>

Mark "Include this location in the search" and then browse the computer to select the following directory: C:\Program Files\Renesas\RPB\RL78L12\USB Drivers\win2k\ Click Next > .

lease ch	noose your search and installation options.
<li>Sea</li>	irch for the best driver in these locations.
	the check boxes below to limit or expand the default search, which includes local as and removable media. The best driver found will be installed.
[	Search removable media (floppy, CD-ROM)
[	✓ Include this location in the search:
	n Riles\Renesas\RPB\RL78G14\USB Drivers\win28 🗸 🕑 🛛 Browse
O Dor	nt search. I will choose the driver to install.
	ose this option to select the device driver from a list. Windows does not guarantee driver you choose will be the best match for your hardware.

Figure 30: Search Location Specification (Windows XP)



<3>

Click Continue Anyway to continue the installation.

Hardware Installation			
<u>.</u>	The software you are installing for this hardware: Renesas Starter Kit Virtual UART has not passed Windows Logo testing to verify its compatibility with Windows XP. ( <u>Tell me why this testing is important.</u> ) <b>Continuing your installation of this software may impair</b> or destabilize the correct operation of your system either immediately or in the future. Microsoft strongly recommends that you stop this installation now and contact the hardware vendor for software that has passed Windows Logo testing.		
	Continue Anyway STOP Installation		

Figure 31: Installation Confirmation (Windows XP)

#### <4>

Click Finish to close the installation wizard.



Figure 32: USB Driver Installation Completion (Windows XP)



#### Installation on Windows Vista

```
<1>
```

When the *YRPBRL78L12 LCD board* is connected to the host machine for the first time, the board is recognized by "Plug and Play" and the "Found New Hardware" window will pop up.

Select "Locate and install driver software (recommended)".



Figure 33: Found New Hardware Wizard (Windows Vista)

Then select "Browse my computer for driver software (advanced)" on the next window.



Figure 34: Install from a Specific Location (Windows Vista)

#### <2>

Select the folder where the installer has copied the USB drivers from the DVD.

For 32-bit Operating Systems, select the following location:

 $C:\Program Files\Renesas\RPB\RL78L12\USB Drivers\win2k\$ 

For 64-bit Operating Systems, select this location:

 $C:\Program Files\Renesas\RPB\RL78L12\USB Drivers\wlh\_amd64\$ 

#### Click Next >



Figure 35: Search Location Specification (Windows Vista)

#### <3>

Click Install when the driver has been found..



Figure 36: Installation Confirmation (Windows Vista)

<4> Click Close to close the installation wizard.





Figure 37: USB Driver Installation Completion (Windows Vista)

#### Installation on Windows 7

#### <1>

.

When the *YRPBRL78L12 LCD board* is connected to the host machine, the board is recognized as an "Unknown Device" in the Device Manager. Right click on the "Unknown Device" and select "Update Driver Software..." within the Device Manager window.

Select "Browse my computer for driver software".

6	Update Driver Software - Unknown Device	×
	How do you want to search for driver software?	
	Search automatically for updated driver software Windows will search your computer and the Internet for the latest driver softwa for your device, unless you've disabled this feature in your device installation settings.	re
	Browse my computer for driver software Locate and install driver software manually.	
		Cancel

Figure 38: Update Driver Software (Windows 7)

#### <2>

Select the folder where the installer has copied the USB drivers from the DVD.

For 32-bit Operating Systems, select the following location:

 $C:\Program Files\Renesas\RPB\RL78L12\USB Drivers\Win2k\$ 

For 64-bit Operating Systems, select this location:

 $C:\Program Files\Renesas\RPB\RL78L12\USB Drivers\wlh\_amd64\$ 

Click Next >.



Figure 39: Search Location Specification (Windows 7)

#### <3>

Click Install when the driver has been found.

• Windows Security	×
Would you like to install this device software? Name: Renesas Electronics Corporation Ports (C Publisher: Renesas Electronics Corporation	
Always trust software from "Renesas Electronics Corporation".	Install Don't Install
You should only install driver software from publishers you trust. software is safe to install?	How can I decide which device

Figure 40: Installation Confirmation (Windows 7)



#### <4>

Click Close to close the installation wizard.

💭 📱 Update Driver Software - Renesas Starter Kit Virtual UART (COM5)
Windows has successfully updated your driver software
Windows has finished installing the driver software for this device:
Renesas Starter Kit Virtual UART
Close

Figure 41: USB Driver Installation Completion (Windows 7)

After the manual installation, please go to

#### Chapter 6.1.1 – Confirmation of USB Driver Installation.

#### • Communication with the YRPBRL78L12 is disabled

Cause:

The driver may not be installed correctly.

Action:

Check if the USB driver is installed correctly by referring to

#### **Chapter 6 – Software Installation**.

Cause:

The COM port selected via the "Port list box" within device setup menu of Renesas Flash programmer may not be set correctly.

Action:

Set the port checked using Device Manager.

Cause:

The YRPBRL78L12 LCD board is operating in GUI Demo / Virtual UART mode.

Action:

Set the board to the On-Board Debugging / Flash Programming mode.

#### • Known e<sup>2</sup>studio Limitations

Start e2studio. From the menu bar click Help > Welcome. Select the 'What's New' icon as highlighted below.



On the Welcome tab, select 'Release notes'.



#### · Cygwin Heap Error

For some 32-bit Windows 7 machines a build error may occur when using cygwin GNU compilers due to virtual memory allocation issues.

This can happen for any project and is described in the following technical note:

http://support.code-red-tech.com/CodeRedWiki/VirtualAllocPointerNull



# Chapter 11 Low Power Measurements

The different power consumptions were measured under the peripheral states shown in the table below. An Agilent U1241B multi-meter (serial: MY51140128) was used to measure the power.

The results obtained cannot be guaranteed to be exactly the same for each board; but should not be significantly different. It is good to note that the result can be affected by the test environment and equipment used.

The YRPBRL78L12 was designed to support two of the three available voltage generation methods: internal voltage boosting and capacitor split. The third method, external resistance division is not supported.

During normal power mode (RUN), the internal voltage boosting method is used as it provides a better display and wider viewing angle, compared to the capacitor split method.

The capacitor split method proved to be better in terms of power consumption as it uses less current compared to the internal boosting voltage. For this reason, before entering the low power modes, the voltage generation method is changed from internal voltage boost to capacitor split mode.

Results are shown on the table below.

Power Mode													
	CPU	24 MHz	32 KHz	UART	Timer	ADC	RTC	LVD	Flash	RAM	LCD	Power Consumption	
RUN	On	On	On	On	On	On	On	Off	Off	On	On	~3500 uA	
HALT	Off	On	On	On	On	On	On	Off	Off	On	On	800uA	
STOP	Off	Off	On	Off	Off	Off	On	Off	Off	On	On/Off	LCD On 2.0uA	LCD Off 0.5uA
SNOOZE	Off	Off	On	Off	Off	On (periodically)	On	Off	Off	On	On	2.0uA – 4.0uA	



# Chapter 12 Resonator from Seiko Instruments Inc.

# 12.1 Features of the 32 KHz Resonator

The SSP-T7-FL is a tuning fork 32 KHz low CL crystal resonator from Seiko Instruments Inc. designed for the new low power RL78 MCU family. Microcontrollers from the RL78 family using this 32 KHz low CL resonator achieves ultra low power oscillation circuit (0.2  $\mu$ A max.) with fast start-up time (within 1 sec.), high accuracy (±10 sec. per month), and high efficiency (oscillation allowance up to 20%).

# 12.2 Product Name and Specifications

The different product names offered by Seiko Instruments Inc. for the RL78 microcontrollers are:

- SSP-T7-FL (SMD type low CL resonator)
- VT-200-FL (Cylinder type Low CL resonator)

The main specifications are as follows:  $32.768 \text{ KHz} \pm 20 \text{ppm} \text{ CL} 4.4 \text{pF}.$ 

# 12.3 Web URL

Fore more information about those products, please refer to the following URL:

www.sii-components.com/quartz/renesas/



# 12.4 Seiko Instruments Inc. Contact Addresses

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