

Tutorial for RH850 Multi core (Debug)

R20UT3068EJ0100 Rev.1.00 2014.9.20

Introduction



Welcome to the world of development environment CubeSuite+. This tutorial introduces you to the integrated development environment provided by CubeSuite+ and the operation of CubeSuite+. By carrying out all the steps described in this tutorial, from creating a program to debugging of the microcontroller, you can easily experience the operation of CubeSuite+. In this tutorial, you will use the E1 (on-chip debugging emulator) and MSRHQ176CP01 (target board: RH850/E1x evaluation board (made by Hitachi ULSI Systems Co., Ltd.)) to actually experience microcontroller system development using CubeSuite+.



Features of CubeSuite+

CubeSuite+ is an integrated development environment that provides an environment for developing microcontrollers from code generation, build, and debugging all in one tool.

Easy GUI customization

You can customize the screen as you like using such features as "docking", "floating", or "automatic hiding" to manipulate various panels of CubeSuite+ at will. CubeSuite+ now provides a feature to save the development environment in addition to the conventional feature to save the project environment. All of these features help you develop a microcontroller system more smoothly.

Easy preparation of development environment

Since the development environment for system development is integrated, it is easy to install the required tools. Because it is equipped with an automatic update function, you can update the software to its latest version (including documents) with a single click.

Reading this tutorial and the following document enables you to learn Overeview of programming for the RH850 multi-core.

Overview of programming for the RH850 multi-core(R20UT3069EJ)



Flow of Microcontroller System Development

This section describes a flow of system development using CubeSuite+.

General flow of system development (V-shaped model)



Following are the functions of CubeSuite+ corresponding to the flow of system development.

<function></function>	<description></description>			
Edit function/ Build function	The edit function is for editing a program. After completing creation of a program, the built function is used to build the program.			
Debugging function	This function enables you to debug the object code after downloading it to the target microcontroller.			
Analysis function	This function helps you improve the execution performance and control the quality of a program by checking the analysis result.			

Overview of Sample Program

This section describes an overview of the sample program and target board (MSRHQ176CP01).

1. Overview of sample program

The program used here controls (turns on/off) a different LED for each core (CPU1 and PCU) of RH850/E1x.

For a detailed description of the program, refer to appendix, Description of Sample Programs.

CPU1 core: Controls LED9 and makes LED9 turn on and off. PCU core: Controls LED10 and makes LED10 turn on and off.

2. Overview of target board (MSRHQ176CP01)

The following is an overview of MSRHQ176CP01 which is used as the target board.

MSRHQ176CP01



LED8 to LED15: Lights when P2_n (n = 0-7) of port group 2 is high. CN10: Used at on-chip debugging or data writing



Installing

This section describes a procedure to install CubeSuite+.

1. Installing Microsoft software products prior to installation

You must install .NET Framework and Visual C++ Runtime Library before installing CubeSuite+. If these software products have not been installed in the PC used, they will be installed at the time of the setup of CubeSuite+.

Insert CubeSuite+ product DVD into the drive of the PC. The following screen appears automatically.



Installing

2. Running the integrated installer

CubeSuite+ products are installed by running the integrated installer.

Click [Begin CubeSuite+ Setup] and start the setup of CubeSuite+.

 Click the button below to start CubeSuite+ setup application. 	
	Installation
De sin Oute Ouite - Ostur	Click the button below to start CubeSube+ setup application.
Begin CubeSuite+ Setup	Microsoft .NET Framework 4 and Microsoft Visual C++ 2010 SP1 runtime libraries are required to run CubeSulter.
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	Please use the latest edition for Microsoft NET Framework 4 and Microsoft Visual C++ 2010 SP1. Please visit Microsoft's web site for more information about the latest edition.
	 If you are required to restart Windows during the setup, please display this screen after restart and click the [Begin CubeSuite* Setup] button.
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Make settings following the instructions provided by the installation wizard. As a final step, click the [Finish] button and complete installation.

* Restart the PC after installation.



Starting CubeSuite+

This section describes the procedures from starting CubeSuite+ to creating a project.

1. Starting CubeSuite+

Start CubeSuite+ by selecting [Start] > [All Programs] > [Renesas Electronics CubeSuite+] > [CubeSuite+].

The One Point Advice dialog box opens when CubeSuite+ is started. Click the [Next] button if you want to read the content. Clicking the [OK] button displays the start screen of CubeSuite+.



Tip

About the Start panel

When you start to use CubeSuite+ to create a new project, click the "Start panel" button (see the figure below). The Start panel opens where you can easily create a new project or open the project you used recently or your favorite project. (The Start panel is displayed when you start CubeSuite+ for the first time after installation. If you have created a project, the latest project opens when you start CubeSuite+.)



Starting CubeSuite+

2. Loading the sample project

In this step, load the sample project.

This document describes the step using a project that has been created according to the construction method for a project using CubeSuite+. For details, refer to Tutorial for RH850 Multi-core Environment (Build).

Tutorial for RH850 Multi-core Environment(R20UT3070EJ)

In the "Open Existing Project" field, click the [GO] button, then select the created project (.mtpj).



Follow the on-screen instructions. A sample project opens as shown in the figure below.

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Tip

About sample projects

CubeSuite+ provides sample projects.

Sample projects are in a state after the "Editing the Program" operations in this document have been performed.

When using a sample project provided by CubeSuite+, load the sample project as shown below.

In the "Open Sample Project" field, from the [RH850] tab, select "RH850_Multicore_E1x_Tutorial_Basic_Operation", then click the [GO] button.

	Many sample projects that can be built immediately are p destination folder to copy the selected sample project.
GO	78K0 78K0R RH850 RL78 RX V850 RH850 RL78 RX V850 RH850_F1L_Tutorial_Analysis RH850_F1L_Tutorial_Basic_Operation RH850_MultiCore_E1x_Tutorial_Basic_Operation

Follow the on-screen instructions. A sample project opens as shown in the figure below.





Manipulating Windows

In CubeSuite+, you can customize the windows at will. This section describes the configuration of windows and window customization functions such as "automatic hiding", "floating", and "docking".

1. Configuration of windows

The following figure shows the configuration of windows of CubeSuite+.



Project Tree panel: Displays the functions of CubeSuite+ corresponding to the flow of system development.

Main panel: Displays the panel (Editor panel, etc.) corresponding to the function selected in the Project Tree.

Output panel: Displays the output results.



Manipulating Windows

2. Automatic hiding

By clicking the Pin icon on the title bar of each panel, you can easily change the setting that determines whether or not to hide the panel automatically. By hiding the panels not necessary for operation, you can use the screen more effectively.

(a) Hiding the panel automatically (example: Project Tree)

Click the Pin icon in the Project Tree.	

The Project Tree automatically disappears and the tab representing it appears.



(b) Displaying the hidden panel (example: Project Tree)

Place the pointer on the [Project Tree] tab.

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Locations to hide the panels

You can hide the panel in three locations: one to the left of the window, one to the right of the window and one below the window. You can also hide multiple panels in the same location.

Manipulating Windows

3. Floating

Right-clicking on the title bar and selecting [Floating] from the menu allows you to move the panel at will.

(a) Making the panel float (example: Project Tree)





Manipulating Windows

4. Docking

You can attach the floating panel to any of the four sides of another panel such as the main panel. You can easily change the position of the panel by dragging and dropping it to a desired location using a navigation icon.

(a) Moving the panel (example:Project Tree)

<image>

Place the pointer on the navigation icon located in the desired destination location and the destination area is highlighted in blue.





Drop the panel there and the Project Tree moves to the desired location (the figure below shows the example of attaching it to the right of the main panel).

H850_MultiCore_E1x_Tutorial_Basic_Operation - CubeSuite+ - [Project Tree]		
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6		RHISO MultiCore Fly, Tutorial Basic Operation
/**************************************	*******/	R7F701207 (Microcontroller)
/*	*/	Cir Boot Loader (Configuration Tool for Multi-con
/* FILE :Main.c	*/	CC-RH (Build Tool)
/* DATE :Non, Mar 4, 2013	*/	S PHPEO E (() PD) (Debug Tool)
/* DESCRIPTION :Main Program	*/	The second secon
/* CPU TYPE :	*/	and have been
/*	*/	Boot.asm
/* NOTE:THIS IS A TYPICAL EXAMPLE.	*/	
/*	*/	iodefine.h
/**************************************	******	interrupt.c
		m1 (Subproject)
void main (void) ;		R7F701207 (Microcontroller)
int al=1;		CC-RH (Build Tool)
int bi;		RH950 E1(LPD) (Debug Tool)
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Tip

Saving and restoring layouts

You can save up to four panel layout (panel location information) states for before and after connecting to the debug tool. To do so, from the menu bar, select [View] -> [Save or Restore Docking Layout].

* Becomes a debugging-specific layout only when the debug tool is connected.



Editing the Program

In this section, you edit the user program.

First, the basic editing method is explained, and then you can edit the program through a simple copy and paste procedure. Edit the program following the steps listed below.

1. How to open a source code file

The following step describes how to open a source code file.

Find the source code file you want to edit in the Project Tree and double-click it.



This displays the source code in the main panel.





Editing the Program

2. Renaming a file

Rename the file following the steps listed below.

Select main.c of pm1 (subproject) in the Project Tree, right-click it to display the pop-up menu, then select "Rename" from the pop-up menu.



Since the file name can now be edited, change it to pm1_main.





In a similar manner, rename the following files: cstartm.asm of pm1 (subproject) -> pm1_cstartm.asm main.c of pm3 (subproject) -> pm3_main.c cstartm.asm of pm3 (subproject) -> pm3_cstartm.asm





Editing the Program

3. Editing

Edit the program following the steps listed below.

Change the main() function name of pm1_main.c to pm1_main(), and copy the following code and paste it in the pm1_main() function.

void pm1_main(void {	1)
	func1(); func_cmn();
	cmn_gHwinitFlag =1;
	<pre>while(1) { func_cmn(); if ((cmn_gCounter & 0xffff) == 0) { pm1_dat ^= 1; PORT.P2.BIT.P2_0 = pm1_dat; } }</pre>
}	





{

}

void hdwinit2(void)		
cmn_gHwinitFlag = 0;		
PORT.PMC2.BIT.PMC2_0 = 0; PORT.PMC2.BIT.PMC2_1 = 0; PORT.PMC2.BIT.PMC2_2 = 0;	22	
PORT.PMC2.BIT.PMC2_3 = 0;	22	
$PORT.PMC2.BIT.PMC2_4 = 0;$	23	void hdwinit2(void)
PORT.PMC2.BIT.PMC2_5 = 0;	24	- {
$PORT.PMC2.BIT.PMC2_6 = 0;$	25	cmn_gHwinitFlag = 0;
$PORT.PMC2.BIT.PMC2_7 = 0;$	26	
PORT PSR2 BIT PSR2 1 = 1	27	<pre>PORT.PMC2.BIT.PMC2_0 = 0;</pre>
PORT PSR2 BIT PSR2 2 = 1	28	<pre>PORT.PMC2.BIT.PMC2_1 = 0;</pre>
PORT PSR2 BIT PSR2 $3 = 1$	29	PORT.PMC2.BIT.PMC2 2 = 0;
PORT.PSR2.BIT.PSR2 $4 = 1$;	30	PORT.PMC2.BIT.PMC2 3 = 0;
PORT.PSR2.BIT.PSR2_5 = 1;	31	PORT.PMC2.BIT.PMC2 $4 = 0;$
PORT.PSR2.BIT.PSR2_6 = 1;	32	PORT.PMC2.BIT.PMC2 $5 = 0;$
PORT.PSR2.BIT.PSR2_7 = 1;	33	PORT.PMC2.BIT.PMC2 = 0;
PORT.PIPC2.BIT.PIPC2_0 = 1;	34	PORT.PMC2.BIT.PMC2 7 = 0;
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PORT PIPC2 BIT PIPC2 $5 = 1$	38	PORT PSR2 BIT PSR2 3 = 1:
PORT.PIPC2.BIT.PIPC2 $6 = 1;$	39	PORT PSR2 BIT PSR2 4 = 1.
PORT.PIPC2.BIT.PIPC2_7 = 1;	40	DODT DSD2 BIT DSD2 5 = 1.
$PORT.PM2.BIT.PM2_0 = 0;$	41	PORT DSD2 BIT DSD2 $6 = 1$
PORT.PM2.BIT.PM2_1 = 0;	12	PORT DED2 BIT DED2 7 - 1.
$PORT.PM2.BIT.PM2_2 = 0;$	12	PORT.PSR2.BII.PSR2_/ = 1;
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$PORT.PM2.BIT.PM2_4 = 0;$ $PORT.PM2.BIT.PM2_5 = 0;$	44	PORT.PIPC2.BIT.PIPC2_I = 1;
PORT PM2 BIT PM2 6 = 0	45	PORT.PIPCZ.BIT.PIPCZ_Z = 1;
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, · · · · · · · · · · · · · · · · · · ·	47	<pre>PORT.PIPC2.BIT.PIPC2_4 = 1;</pre>
}	48	<pre>PORT.PIPC2.BIT.PIPC2_5 = 1;</pre>
	49	<pre>PORT.PIPC2.BIT.PIPC2_6 = 1;</pre>
	50	<pre>PORT.PIPC2.BIT.PIPC2_7 = 1;</pre>
	51	<pre>PORT.PM2.BIT.PM2_0 = 0;</pre>
	52	<pre>PORT.PM2.BIT.PM2_1 = 0;</pre>
	53	<pre>PORT.PM2.BIT.PM2_2 = 0;</pre>
	54	<pre>PORT.PM2.BIT.PM2_3 = 0;</pre>
	55	<pre>PORT.PM2.BIT.PM2_4 = 0;</pre>
	56	PORT.PM2.BIT.PM2_5 = 0;
	57	PORT.PM2.BIT.PM2 6 = 0;
	58	PORT.PM2.BIT.PM2 $7 = 0;$
	59	}
	60	

To add the hdwinit2() function to pm1_main.c, copy and paste the following code.



To add include statements to pm1_main.c, copy and paste the following code.

#include "iodefine.h" #include "cmn.h" #include "prg1.h"



To add the hdwinit2() call to the pm1_main() function, copy and paste the following code.

hdwinit2();





Change the main() function name of pm3_main.c to pm3_main(), and copy the following code and paste it in the pm3_main() function.

```
void pm3_main(void)
{
```

```
func3();
      while(1)
       {
               if ( cmn_gHwinitFlag != 0 )
               {
                      break;
               }
      }
       while(1)
       {
               cmn gCounterPm3++;
               if ( ( cmn_gCounterPm3 & 0xffff ) == 0 )
                {
                     pm3_dat ^= 1;
                     PORT.P2.BIT.P2_2 = pm3_dat;
               }
if ( ( cmn_gCounter & 0xfffff ) == 0 )
               .
pm3_dat2 ^= 1;
               PORT.P2.BIT.P2_7 = pm3_dat2;
       }
}
```



To add include statements to pm3_main.c, copy and paste the following code.

#include "iodefine.h"
#include "cmn.h"
#include "prg3.h"



Change the branch destination of pm1_cstartm.asm to pm1_main() and the branch destination of pm3_cstartm.asm to pm3_main().



	pm1	l_main.c* 🝸 pm3_m	ain.c* 🍯 pm1_cstartm.asm*	
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65 66 67 68 69 70		movhi or Idsr	0x0001, r0, r11 r11, r10 r10, 5, 0	; enable FPU
71 72 73		ldsr Idsr	r11, 6, 0 r0, 7, 0	; initialize FPSR ; initialize FPEPC
74 75 76		;xori	0x0020, r10, r10	; enable interrupt
77 77 78		;or	r11, r10	; supervisor mode -> user mode
79 80		ldsr	r10, 3, 0	; FEPSW <- r10
81	_	mov	<u>#_oxit, lp</u>	; lp <- #_exit
82 88 84		mov Idər	#_pm1_main, r10 r10, 2, 0	;FEPC <- #_main



Change the label name "_pm1_setting_table" of pm3_cstartm.asm to "_pm3_setting_table".



🖉 pm1_main.c* 📓 pm3_r	nain.c* 📓 pm1_cstartm.asm* 📓 pm3_cstartm.asm*
割 割 🔿 🤉 🕥 カ	∋ ⊿•
行 1 2 3 4 5 6 7 7 8 9 10	 Copyright(c) 2013, 2014 Renesas Electronics Corporation RENESAS ELECTRONICS CONFIDENTIAL AND PROPRIETARY. This program must be used solely for the purpose for which it was furnished by Renesas Electronics Corporation. No part of this program may be reproduced or disclosed to others, in any form, without the prior written permission of Renesas Electronics Corporation. NOTE : THIS IS A TYPICAL EXAMPLE FOR MULTI CORE PROJECT.
12 13 14 15 16 17 18 19	processing module setting table .section <u>"section</u> public



Editing the Program

4. Adding files

Add programs following the steps listed below.

Select a file of pm1 (subproject) in the Project Tree, right-click it to display the pop-up menu, and then select "Add File..." from the pop-up menu.



This opens the Add Existing File dialog box. Add the files in the following folder.

<Folder created when loading the sample project>¥ RH850_MultiCore_E1x_Tutorial_Basic_Operation¥pm1

- prg1.c
- prg1.h
- cmn.h







In a similar manner, add the following files to pm3 (subproject). <Folder created when loading the sample project>¥ RH850_MultiCore_E1x_Tutorial_Basic_Operation¥pm3 - prg3.c - prg3.h





Editing the Program

5. Deleting files

Delete programs following the steps listed below.

Select common.c of pm1 (subproject) in the Project Tree, right-click it to display the pop-up menu, then select "Remove from Project" from the pop-up menu.





Editing the Program

6. Changing the property of the compiler (CC-RH)

Change the property of CC-RH following the steps listed below.

Display the property of CC-RH of pm3 (subproject) in the Project Tree, then click the Add button to add an additional include path as a compile option.



This opens the Path Edit dialog box. Click the Browse button.



This opens the Browse For Folder dialog box. Select the pm1 folder shown below. <Folder created when loading the sample project>¥ RH850 MultiCore E1x Tutorial Basic Operation¥pm1





After confirming that the folder has been added, click the OK button.

Path Edit
Path(One path per one line): 🛛 🙀
\pm1 Files\Renesas Electronics\CubeSuite+\SampleProjects -
Browse Permit gon-existent path Include gubfolders automatically Elevabolder
Placeholder Value
ActiveProjectDir C:\Users\toolgi\Documents\RH850_MultiCore_E1x_1
ActiveProjectName RH850_MultiCore_E1x_Tutorial_Basic_Operation
BuildModeName DefaultBuild
MainProjectDir C:\Users\toolgi\Documents\RH850_MultiCore_E1x_1
MainProjectName RH850_MultiCore_E1x_Tutorial_Basic_Operation
MisomToolDath C-1Drogram Eilpol Dianoosa Elastronias (Cubo Cubo Cubo Cubo Cubo Cubo Cubo Cubo
OK Cancel Help

Editing the Program

7. Editing the section start address

Change the section start address following the steps listed below.

Display the property of CC-RH of pm3 (subproject) in the Project Tree, then click the Edit button at the section start address of the section group as a link option.



This opens the Section Settings dialog box. Click the Add button to add the .const.cmn section and make settings and edit settings as shown below.

Address	Section		<u>A</u> dd
0x00004000	.const.cmn		N
	.const		Modiry
	.INIT_DSEC.const		New <u>O</u> verlay
	.INIT_BSEC.const		Pamoua
	.text		
	.data		<u>Up</u> <u>D</u> owr
0xFEDF8000	.data.R		
	.bss		
	.stack.bss		
		•	Import
			<u>E</u> xport
	ОК	Cancel	Help

Rebuilding a Program

Rebuild the program of the loaded sample project.

1. Building a project

In this step, rebuild the program of the loaded sample project.



In this section, you debug the program using the E1. First, make preparations for debugging.

1. Selecting a debug tool

In this step, select [RH850 E1(LPD) (Debug Tool)] as a debug tool to be used.

Right-click on the Debug Tool in the Project Tree and select [Using Debug Tool] -> [RH850 E1(LPD)].



[RH850 E1(LPD) (Debug Tool)] is selected as a debug tool.





2. Setting E1 for connection to the target board

Right-click the debug tool in the Project Tree to select [Property].



Make settings as shown below in the [Connection with Target Board] tab.



Tip

About the security ID

The 128-bit ID code can be written to the microcontroller so that the flash memory contents are not read by an unauthorized user. If the code that is input by the user when the debugger is started does not match the ID code written to the microcontroller, flash memory cannot be accessed. Settings should be made by a flash programmer. When a blank product (all flash memory contents are erased) is used, only F should be input for the security ID.

3. Connecting E1

In this step, on-chip debugging is performed using E1.

Connect E1 to the RH850/E1x board (MSRHQ176CP01). Align pin 1 of the connector.



Connect E1 to the PC. ("Found New Hardware Wizard" appears when E1 is connected for the first time. Select "Install software automatically" and install the USB driver following the instructions.)



Turn on the power of MSRHQ176CP01.



About option bytes

In flash memory, there is an extended area (option bytes) for holding data specified by the user for various purposes. In the RH850/E1x-FCC1 microcontroller, not only are settings for the debugging interface made, but settings for WDT-related features and the operating mode and startup area of the microcontroller are to be made. When the program of this tutorial is used, set the OPBT0 register to H'53FFFED and the OPBT2 register to H'BFFFFFF.



4. Downloading a load module file to E1

In this step, download the load module file generated by the build process to the target microcontroller.

When download is complete, the program can be executed.



Tip

Registering load module files

Load module files generated in subprojects need to be registered as load module files subject to download.

Load module files can be registered in the [Download File Settings] tab of the Property panel of the debug tool.



Property		×
RH850 E1(LPD) Property		+ هر
🖂 Download		
Download files	[3]	
	Defe	adtBuild'RH058_MaltiCon_E1x_Tutorial_Basic_Operation.abs
	pm1	\DefaultBuild\pm1.abs
	pm3	\DefaultBuild\pm3.abs
CPU Reset after download	Tes	
Erase flash ROM before download	No	
Automatic change method of event setting posi	tion Sus	pend event
🗆 Debug Information		
Execute to the specified symbol after CPU Res	et Yes	
Specified symbol	_ma	in
The upper limit size of the memory usage [MBy	tes] 500	
Download files		
Specifies the file to be downloaded. The downloa downloaded.	d file dialog box is opened by p	pressing the [] button. In the download file dialog box, specify the file to be
Connect Settings / Debug Tool Settings	Download File Settings	Flash Options Settings / Hook Transaction Settings /



Switching Cores

Now that download of the load module file to the target is complete, let's switch the core to be debugged.

1. Switching the core

There are two methods for switching the core to be debugged.

a. Switch the core from the status bar

The core can be switched using the drop-down list on the status bar of the main window.



b. Switch the core from the Debug Manager panel

Selecting [View] menu -> [Debug Manager] opens the Debug Manager panel. The core can be switched in the Debug Manager panel.

Debug Manager		×
6 D M I O	🕑 🕞 😽 Se Če	
Debug target: —		
CPU1	C PCU	
Debug target statu		_
Running status:	BREAK	
Target status:		
Current PC:	♀ 0x01000000	
		-

Here, the state of CPU1 being selected should not be changed.



Core to be debugged

Running or stopping of the program cannot be performed by only one core running or stopping the program.

Running or stopping of the program must be performed by both cores operating in a coordinated manner.

When CPU1 is set as the core to be debugged, referencing or changing memory by CubeSuite+ is effective only for CPU1. To perform operations for the other core, the core to be debugged has to be switched.



Running and Stopping the Program

Now that download of the load module file to the target is complete, the program can be executed. First, run and stop the program.

1. Running the program

Run the program after resetting the CPU.

Click the [Restart] button in the menu.
This runs the program and displays [RUN] in the status bar.
LED9 and LED10 on MSRHQ176CP01 light alternately.

Running and Stopping the Program

2. Stopping the program

In this step, stop the program.

Click the Stop button.



This stops the program and displays [BREAK] in the status bar.



The source code line where the program stopped (current position of the program counter (PC)) is highlighted in yellow.

73 74			<pre>while(1) {</pre>	
75 76 77	0000121e 00001222	⇒ –	<pre>runc_cmn(); if ((cmn_gCounter & 0xffff) == 0 {</pre>	
78 79	000011f6 00001206		<pre>pm1_dat ^= 1; PORT.P2.BIT.P2_1 = pm1_dat;</pre>	
				A constraint of the second sec



Running and Stopping the Program

3. Resetting the program

In step 1, you have reset and run the program using a single button, but you can also carry out the reset operation independently.



About the program counter

The program counter (PC) is a control register that holds information on the next program address. When the RH850/E1x-FCC1 microcontroller generates a reset signal, 0000000H is set in the PC for user mode and 01000000H is set in the PC for user boot mode. In the program of this tutorial, since the program is set to run up to the _____start function after a reset, the program enters the break state after executing the ____start function. Such kind of behavior can be changed in the [Download File Settings] tab of the Property panel of RH850 E1(LPD).

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4 Demelord	
Dominical	[2]
> IOL	Details Mobile Mobiles Etc. Total Pasis Operation also
> [0]	nm1)Dafa dBuildings1 also
- DI	an 2 Date AD did DM2 also
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Ersen Bash DOM helere developed	Ne
Automatic chapped method of exact cattion position	Surrend auset
Adonatic change menod or event sening position	superu even
Execute to the specified suppol after CPU Reset	Yes
Specified workol	BH850 MultiCore F1x Tutorial Basic Operation abs\$ start
The unner limit size of the memory unage [MRider]	500
)ownload files	
inerifier the file to be downloaded. The download file dialog	how is onened by massion the E. I button. In the download life dialon how snariby
he file to be downloaded.	need to obtained all become a set of the set



Referring to a Variable

Watch function

By registering a variable as a watch-expression, it is possible to display the value of that variable. Here, register the two shared variables (variables which are located in a shared area that can be referenced from both the CPU1 core and PCU core) "cmn_gCounter" and "cmn_gCounterPm3" as watch-expressions and confirm that the values of the variables are incremented. cmn_gCounter and cmn_gCounterPm3 are variables that are incremented by the CPU1 core and PCU core, respectively, while the program is running. It is possible to confirm that the count values at a break differ because the execution speed of the CPU1 core and PCU core are different.



Tutorial for RH850 Multi core(Debug)

	Register to Analysis Chart Register Action Event	
	Cut Crif Copy Crif Paste Crif Find Crif Go To Go to Mere Cursor Position Back to Last Cursor Position Go to Here Set PC to Here Jump to Function F	HX HC HF HG n 12
 A methysic sectors A methysic secto	Tag Jump Shift+F Jump to Disassemble Bookmarks Advanced Break Settings Trace Settings Save Source Mixed Data As	12

This opens the Watch panel where you can confirm that the variable is now registered. The current value of "cnm_gCounter" is ?.



In a similar manner, register "cmn_gCounterPm3" in the Watch panel from pm3_main.c.

Click the [Restart] button in the menu. After several seconds have passed, click the [Stop] button.



Watch1			ч ×
🛛 🗶 🐉 🧏 🦃 🖾	Jotation - High		
Watch	Value	Type(Byte Size)	Address]
<pre> cmn_gCounter cmn_gCounterPn3 </pre>	10527282 2871106	unsigned int(4) unsigned int(4)	Oxfeee0004 Oxfeee0008

The difference in the operating frequency of the CPU1 core and PCU core cause the execution count of cmn_gCounter and cmn_gCounterPm3 to differ. The difference in the execution count can be confirmed by the fact that LED9 turns on and off at a high speed whereas LED10 turns on and off at a lower speed than LED9.



Setting a Breakpoint

Setting a breakpoint

If you want to stop the program at a specific position intentionally in the source code, you can break the program before executing the instruction at the specified address ("before execution" break) by setting a breakpoint.

Let's see how the variable (cnm_gCounter) that was registered as a watch-expression changes by running the program and causing it to break.

Click the empty column to the left of the desired line in the source code as shown below. A hardware break is set and the line is highlighted in red.







¹⁴ Belliell en et durant verstell Anderen int, en gleinit in antiped int, en gleinit i uniped int, en gleiniteful Briges antien af all.

IDECTs IDECTs IDECTs

The program breaks again at the line where the break has been set. See the Watch panel and confirm that the value of [cnm_gCounter] is counted up to 0x1.



Tip

Break in a multi-core device

Normally, the position where a break occurred in the program is displayed when a break occurs. In a multi-core device, if a break was caused by a break source of another core (core not being debugged), a break occurs in the target core (core being debugged) at an address where no break condition has been set. The break source can be checked in the Output panel.

In the example below, the Output panel shows that a break (relay break) in another core is the break source.

Stopped by user operation Stopped by user operation Stopped by user operation Stopped by Hardware Break Stopped by Hardware Break Stopped by Relay Break. Direct Cause: CPU1 Stopped by Hardware Break Stopped by Relay Break. Direct Cause: CPU1 Stopped by Hardware Break (E0F) All Messages *Rapid Build *Build Tool *Debug Tool FH FS Y Hardware Break. Line 33/48 Column 1 Insert Western European (Windows)	Output				
Stopped by user operation. Stopped by Hardware Break. Stopped by Hardware Break. Stopped by Hardware Break. Stopped by Hardware Break. Stopped by Relay Break. Direct Cause: CPUI Stopped by Hardware Break. [EOF] All Messages *Rapid Build *Build Tool *Debug Tool / F4 F5 F6 F7 F8 F8 y Hardware Break. Line 33/48 Column 1 Insert Western European (Windows) PCU © BREAK	Stopped by user operation.				
Stopped by Hardware Break Stopped by Hardware Break Stopped by Hardware Break Stopped by Hardware Break Stopped by Relay Break. Direct Cause:CPU1 Stopped by Hardware Break [B07] All Messages *Rapid Build / *Build Tool / *Debug Tool / F4 F5 F6 F7 F8 F7 Hardware Break. Line 33/48 Column 1 Insert Western European (Windows) PCU BREAK	Stopped by user operation.				
Stopped by Hardware Break, J Stopped by Hardware Break, J Stopped by Hardware Break, J Stopped by Relay Break. Direct Cause: CPU1 Stopped by Hardware Break, J [BOF] All Messages *Rapid Build *Build Tool *Debug Tool FH F5 F5 F5 F7 F8 F7 V Hardware Break. Line 33/48 Column 1 Insert Western European (Windows) PCU BREAK	Stopped by user operation.				
Stopped by Hardware Break. Stopped by Hardware Break. Stopped by Relay Break. Direct Cause: CPU1 Stopped by Hardware Break. (BOF) All Messages *Rapid Build (*Build Tool (*Debug Tool / FH FS FH FS Y Hardware Break. Line 33/48 Column 1 Insert Western European (Windows)	Stopped by Hardware Break.				
Stopped by Hardware Break. Stopped by Relay Break. Direct Cause: CPU1 Stopped by Hardware Break. [E0F] All Messages *Rapid Build / *Build Tool / *Debug Tool / F4 F5 F6 F7 F8 F7 Hardware Break. Line 33/48 Column 1 Insert Western European (Windows) PCU BREAK	Stopped by Hardware Break.				
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y Hardware Break. Line 33/48 Column 1 Insert Western European (Windows) PCU 💌 🔳 BREAK					
	y Hardware Break.	Line 33/48 Column 1	Insert Western Eu	ropean (Windows)	U 🔽 🔳 BREAK

Acquiring the Execution History

Acquiring the execution history

In general, the execution history of the program is called trace information. If a program gets out of control, it is extremely difficult to investigate the cause only from the memory contents or stack information after the runaway occurred. However, using the trace function and analyzing the acquired trace information enables the process until the runaway occurred to be directly investigated.



Collecting the Execution History

Setting trace operation

When the trace function starts recording, the execution process of the program currently running is recorded in trace memory (when program execution stops, the trace function also stops automatically).

Settings related to tracing need to be made in advance to use the trace function.



This opens the Trace panel.





Settings for tracing can be made in the [Trace] category on the [Debug Tool Settings] tab of the Property panel. Select the [Debug Tool Settings] tab and make the settings as shown below.

Q	RH850 E1(LPD) Property	+ - Q
۵	Memory	
Þ	Memory mappings	[33]
	Verify on writing to memory	Yes
4	Access Memory While Running	
	Access during the execution	No
	Update display during the execution	Yes
	Display update interval[ms]	500
۵	Set Event While Running	
	Set event by stopping execution momentarily	No
Þ	Break	
4	TIGCE	
		Branch PC
_	Trace priority	Coased priority
	Clear trace memory before running	Yes
	Operation after trace memory is full	Non stop and overwrite to trace memory
	Trace range setting	Traces section
	Trace target setting	Debug core only
4	Mask for Input Signal	
	Mask WAIT signal	No
	Mask RESET signal	No
Sel	lect trace data ects the type of the trace data to be acquired.	



Click the [Go] button in the menu.



A break occurred and the execution history is displayed in the Trace window.



Tracing in a multi-core device

Tip

When trace data is acquired with the core to be debugged set to CPU1, only information for the CPU1 side can be observed. In order to acquire trace data for the PCU side, execute the program again after switching the core to be debugged to PCU.

After trace data has been acquired with the core to be debugged set to CPU1, even though the core to be debugged is switched to PCU, trace data for the PCU side cannot be observed.

			1	Trace	е					Ф ×
				2	💱 🛞 🌒 /	👬 <u>N</u> otation •	JSI	1 🐁 🥑		
			/	N	Time (h:min:	s,ms,µs,ns)	Time(Line Number/	Source	e/Disassem
On SA Dav Down SA Dave Dav SD R Sar 2 3 9 8 2 3 3 4	metric metric (definition (and the constant $($ parts ($)$	an. Airdeireanna	/						_pm3_	main:
1 0 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	default defauer defautement defautement demart for the second defautement defautement the second defautement defautement the second defautement defautement	1		0				pm3_main.c#25	if (cmn_gHwini
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	0 0000419 set_incl.*c.i 0 0000419 FER.76.307.76.2 0 . .	2 Sortour gras (Val)		1-1				pm3_main.c#33	cmn_g	CounterPm3
2 Statistic production - Classific b - Statistic	Regard to save specified at through to save specified at through to save specified at through to save specified at through to the same specified at through to the save specified through to the save fraction. Facula (HS, through by Banthow Brook, J hogsel by Ranthow Brook	2		1-1 1-2	00h00min00s0	00ms000µs150n	:: 12	0x0000408e pm3 main.c#33	+118 cmn c	mov CounterPm3
n 100 100	The space of the Technical Network of Linear CPU. Respect or Restingence Technical J Technical Information Network (State of Vice Respect for Restingence Technical J Technical Network (State of Vice Rest of Vice R	en in		1-2				0x00004092	+122	1d.
	PCU 💌	BREAK	(> 0:	×00004096		E1(LPD)	1.454 μs		*



Canceling a Break

Canceling a Break

In this section, you cancel the break which was set in the previous section. The previously set break is set as a hardware break. A hardware break is registered as an event. Deleting the event will cancel the hardware break.

An event is an operation of the microcontroller such as fetch, read, and write. The event can be used as an action trigger to enable debugging functions such as setting a breakpoint or tracing. The hardware break that was set in the previous section was an event causing the program to break when a particular address is fetched (before it is executed).



This opens the Event panel. Select [Break0001].





Left-click the Delete button. This cancels the access break.



Confirm that [Break0001] has been deleted from the Event panel.

Events		џ ×
× 1 🔍 🔍 💱 🖏 🗐		
Name	Detail	Comment
🔽 学 Unconditional Trace	•	
🔽 🇐 Bun-Break Timer	Total:1333 ns	



Displaying Special Function Registers (IORs)

Displaying IORs

In this section, you observe the values of registers that implement peripheral functions built in the microcontroller. First, let's make the panel float so that it is easier to view.





The IOR panel enters the floating state and it becomes easier to view.



Tip

About display of IOR Bits

The IOR panel does not support displaying of IOR bits. Therefore, in order to check the bits of an IOR, that IOR has to be registered in a Watch panel so it can be referenced.

Select [Add New Watch] from the context menu in the desired Watch panel and input a watch-expression. To specify register bits, enter as shown below.

AAA0.BBB.CCC

<Module name>.<Register name>.<Bit name>

[Example]

Watch-expression for registering the P2_1 bit in the P2 register of a (general I/O) port in a Watch panel:

PORT.P2.P2_1



Displaying Memory

Displaying the Memory panel

The Memory panel displays the state of memory. In this example, two of the four Memory panels are displayed. If [Memory 1] and [Memory 2] are displayed at the same time, they are tabbed by default, making it impossible to view both panels at same time. Let's dock these two panels so that they can be viewed together.





It becomes possible to view the Memory 1 and Memory 2 panels at the same time.



RENESAS



Disconnecting a Debug Tool

When finishing debugging, disconnect the debug tool.

Click the "Disconnect from Debug Tool" button.



Tip

Downloading a program

If a program is changed after being downloaded to the target, you have to perform the build process again and download the program. If a program is changed after downloading, the right side of the line number turns yellow (or green) and breakpoints cannot be set.





Termination Procedure

This section describes the termination procedure.



Tip

Saving the development environment (project saving function and packing function)

CubeSuite+ provides two functions (project saving function and packing function) for saving the development environment. Each of these functions is used to save the contents shown in the figure below. Either saving function can be chosen depending on the development phase.





About Flash Programming

When using the E1 emulator to write a .hex file to the microcontroller, use Renesas Flash Programmer (RFP).

- Start Renesas Flash Programmer (RFP) by selecting [Start] -> [All Programs] -> [Renesas Electronics Utilities] -> [Programming tools].
- Refer to the user's manual for the usage method.



Description of Sample Programs

The flow diagrams of sample programs are shown below.





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