RX671 Group
Target Board for RX671
User’s Manual

RENESAS 32-Bit MCU
RX Family/RX600 Series

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(Rev.5.0-1 October 2020)
General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)
   A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity.
   Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on
   The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state
   Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements.
   Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins
   Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals
   After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin
   Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between VIL (Max.) and VIH (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between VIL (Max.) and VIH (Min.).

7. Prohibition of access to reserved addresses
   Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products
   Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.
How to Use This Manual

1. Purpose and Target Readers

This manual is designed to provide the user with an understanding of the basic specifications and correct usage of this product.

The target users are those who will be using it in evaluating MCUs and debugging programs.

The target readers of this manual require basic knowledge regarding the facilities of MCUs and debuggers.

Particular attention should be paid to the precautionary notes when using the manual. These notes occur within the body of the text, at the end of each section, and in the Handling Precautions section.

The revision history summarizes the locations of revisions and additions. It does not list all revisions. Refer to the text of the manual for details.

The following documents apply to the Target Board for RX671. Be sure to refer to the latest versions of these documents. The newest versions of the listed documents are available on the Renesas Electronics Web site.

<table>
<thead>
<tr>
<th>Document Type</th>
<th>Description</th>
<th>Document Title</th>
<th>Document No.</th>
</tr>
</thead>
<tbody>
<tr>
<td>User’s manual</td>
<td>Target Board for RX671 hardware specifications</td>
<td>Target Board for RX671 User’s Manual</td>
<td>R20UT4894EJ</td>
</tr>
<tr>
<td>Circuit schematics</td>
<td>Target Board for RX671 circuit schematics</td>
<td>Target Board for RX671 Circuit Schematics</td>
<td>R20UT4898EJ</td>
</tr>
<tr>
<td>Parts list</td>
<td>Target Board for RX671 parts list</td>
<td>Target Board for RX671 BOM LIST</td>
<td>R12TU0116EJ</td>
</tr>
<tr>
<td>User’s manual for the hardware*</td>
<td>Hardware specifications (pin assignments, memory maps, peripheral function specifications, electrical characteristics, timing charts) and descriptions of operation</td>
<td>RX671 Group User’s Manual: Hardware</td>
<td>R01UH0899EJ</td>
</tr>
</tbody>
</table>

Note: Download the documents for the RX671 from the product page for the RX671.
2. List of Abbreviations and Acronyms

<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Full Form</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU</td>
<td>Central Processing Unit</td>
</tr>
<tr>
<td>DIP</td>
<td>Dual In-line Package</td>
</tr>
<tr>
<td>DNF</td>
<td>Do Not Fit</td>
</tr>
<tr>
<td>IDE</td>
<td>Integrated Development Environment</td>
</tr>
<tr>
<td>IRQ</td>
<td>Interrupt Request</td>
</tr>
<tr>
<td>HOCO</td>
<td>High-Speed On-Chip Oscillator</td>
</tr>
<tr>
<td>LOCO</td>
<td>Low-Speed On-Chip Oscillator</td>
</tr>
<tr>
<td>LED</td>
<td>Light Emitting Diode</td>
</tr>
<tr>
<td>MCU</td>
<td>Micro-controller Unit</td>
</tr>
<tr>
<td>n/a (NA)</td>
<td>Not applicable</td>
</tr>
<tr>
<td>n/c (NC)</td>
<td>Not connected</td>
</tr>
<tr>
<td>PC</td>
<td>Personal Computer</td>
</tr>
<tr>
<td>Pmod™</td>
<td>Pmod™ is a trademark of Digilent Inc. The Pmod™ interface specification is the property of Digilent Inc. For the Pmod™ interface specification, refer to the Pmod™ License Agreement page at the Web site of Digilent Inc.</td>
</tr>
<tr>
<td>RAM</td>
<td>Random Access Memory</td>
</tr>
<tr>
<td>RFP</td>
<td>Renesas Flash Programmer</td>
</tr>
<tr>
<td>ROM</td>
<td>Read Only Memory</td>
</tr>
<tr>
<td>SPI</td>
<td>Serial Peripheral Interface</td>
</tr>
<tr>
<td>UART</td>
<td>Universal Asynchronous Receiver/Transmitter</td>
</tr>
<tr>
<td>USB</td>
<td>Universal Serial Bus</td>
</tr>
</tbody>
</table>

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1. Overview

1.1 Package Components

Thank you for purchasing the Target Board for RX671 evaluation tool from Renesas (hereinafter referred to as “this product”). This product consists of the Target Board for RX671 (RTK5RX6710C00000BJ).

1.2 Purpose

This product is an evaluation tool for a Renesas MCU. This user’s manual describes the hardware specifications, ways of setting switches, and the basic setup procedure.

1.3 Features

This product can handle the following tasks.

- Programming of the Renesas MCU
- Debugging of user code
- User circuits for switches and LEDs
- Sample applications*1
- Samples of peripheral-function initialization code*1

Note: These are available for downloading from the Renesas Web site.

https://www.renesas.com/rxtb

1.4 Preparation

Install the integrated development environment (IDE) and required software from the following URL on the host PC.

https://www.renesas.com/development-tools
### 1.5 Target Board for RX671: Table of Specifications

Table 1-1 shows the specifications of this product.

<table>
<thead>
<tr>
<th>Item</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Evaluation MCU</td>
<td>Part No.: R5F5671EHDFP Package: 100-pin LFQFP On-chip memory: 2-MB ROM, 384-KB RAM, 8-KB data flash memory</td>
</tr>
<tr>
<td>Board size</td>
<td>Size: 54.0 mm x 90.0 mm Thickness: 1.6 mm</td>
</tr>
<tr>
<td>Power-supply circuit</td>
<td>USB connector: 5-V input Power-supply IC: 5-V input, 3.3-V output External power-supply header*: 3.3-V input, 2 pins x 1</td>
</tr>
<tr>
<td>Current drawn</td>
<td>Max. 200 mA</td>
</tr>
<tr>
<td>Current measurement header*</td>
<td>Header: 2 pins x 1</td>
</tr>
<tr>
<td>Main clock*</td>
<td>Crystal oscillator (surface-mount technology (SMT)) for the main system clock Crystal oscillator or ceramic resonator (lead type) for the main system clock</td>
</tr>
<tr>
<td>Sub-clock*</td>
<td>Crystal oscillator (SMT) for the sub-clock</td>
</tr>
<tr>
<td>Push switches</td>
<td>Reset switch x 1</td>
</tr>
<tr>
<td></td>
<td>User switch x 1</td>
</tr>
<tr>
<td>LEDs</td>
<td>Power indicator: green x 1 User: green x 2 ACT LED: green x 1</td>
</tr>
<tr>
<td>USB</td>
<td>USB Micro-B</td>
</tr>
<tr>
<td>Pmod™ connector</td>
<td>Connector: Angle type, 12 pins</td>
</tr>
<tr>
<td>MCU headers*</td>
<td>Headers: 50 pins x 2</td>
</tr>
<tr>
<td>Patterns for cutting</td>
<td>16</td>
</tr>
<tr>
<td>Emulator reset header*</td>
<td>Header: 2 pins x 1</td>
</tr>
</tbody>
</table>

Note: This part is not mounted.
1.6 Block Diagram

Figure 1-1 shows the block diagram of this product.

![Block Diagram](image)

Note: Gray shading of blocks indicates parts that are not mounted on the board.
2. Board Layout

Figure 2-1 shows the external appearance of the top side of this product.
3. Parts Layout

Figure 3-1 shows the parts layout of this product.

Figure 3-1  Parts Layout
4. Operating Environment

Figure 4-1 shows the operating environment of this product. Install the IDE from the following URL on the host PC. The installer automatically installs all required drivers along with the IDE.

https://www.renesas.com/development-tools
5. User Circuits

5.1 Evaluation MCU

The MCU specifications for the power supply, system clock, and reset at the time of shipment are as follows.

- Power supply: Fixed 3.3 V (including the analog power supply)
- System clock: Operation with an on-chip oscillator
- Reset: Directed by the reset switch or IDE

5.2 USB Connector

The shape of the connector is USB micro-B for the IDE and for the Renesas Flash Programmer (RFP). Connect the connector to the computer by a USB cable. If the power supply on the host side is on, the power is supplied to this product at the same time as connection of the cable.

Note: The package does not include a USB cable.

5.3 ACT LED

The ACT LED displays the state of operation of the emulator control software. The illumination conditions are listed below. The LED is green.

- Illuminated: Indicates that the emulator is connected to the target.
- Blinking: Indicates that the host machine (PC) has recognized the emulator.
- Not illuminated: Indicates that the emulator cannot be used for some reason (including its power being off).

5.4 Power LED

While the power LED is illuminated, power is being supplied to the board. The LED is green.

5.5 User LEDs

The optional user LEDs can be used for any purpose. LED0 and LED1 are mounted on the board and are respectively connected to the following ports. The LEDs are green.

- LED0: Pin 18, connected to port P32
- LED1: Pin 17, connected to port P33
5.6 External Power-Supply Header

When the evaluation MCU is to have a desired power-supply voltage, or when more current is required than the USB is capable of supplying, use the external power-supply header (J4) to supply power. The usable voltages depend on the evaluation MCU. When this header is to be used, remove the pattern for cutting (SS8). Place the emulator in the forced reset state by short-circuiting the header (J6) (refer to section 5.13, Emulator Reset Header). Figure 5-1 shows the position of the pattern for cutting. Figure 5-2 shows the position of the external power-supply header.

Figure 5-1 Position of the Pattern for Cutting (SS8) (Soldered Side)

Figure 5-2 Position of the External Power-Supply Header (J4) (Top Side)
5.7 Pmod™ Connector

The specification of the Pmod™ connector (CN1) is on the assumption that Pmod™ modules are to be connectable.

CN1 is for connection to Pmod™ Interface Type 6A (Type 6 + Type 1) modules* in the product as shipped. Remodeling of the board by removing patterns for cutting enables the connection of CN1 to Type 2A or Type 3A modules. Use Pmod™ modules for operation at the same potential as that for the evaluation MCU.

However, we do not guarantee connection to all types of Pmod™ module. Confirm the specifications of this product against any Pmod™ module you intend to use.

Note: The specifications of Type 6A differ from those described in the Pmod™ interface specification of Digilent.

Table 5-1 Pin Assignments of the Pmod™ Connector

<table>
<thead>
<tr>
<th>Pin No. (CN1)</th>
<th>Specifications of the Connector</th>
<th>Evaluation MCU</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Type 6A (I2C)</td>
<td>Type 2A (SPI)</td>
</tr>
<tr>
<td>1</td>
<td>INT</td>
<td>CS</td>
</tr>
<tr>
<td>2</td>
<td>RESET</td>
<td>MOSI</td>
</tr>
<tr>
<td>3</td>
<td>SCL</td>
<td>MISO</td>
</tr>
<tr>
<td>4</td>
<td>SDA</td>
<td>SCK</td>
</tr>
<tr>
<td>5</td>
<td>GND</td>
<td>GND</td>
</tr>
<tr>
<td>6</td>
<td>VCC</td>
<td>VCC</td>
</tr>
<tr>
<td>7</td>
<td>GPIO</td>
<td>GPIO/INT</td>
</tr>
<tr>
<td>8</td>
<td>GPIO</td>
<td>GPIO/RESET</td>
</tr>
<tr>
<td>9</td>
<td>GPIO</td>
<td>GPIO</td>
</tr>
<tr>
<td>10</td>
<td>GPIO</td>
<td>GPIO</td>
</tr>
<tr>
<td>11</td>
<td>GND</td>
<td>GND</td>
</tr>
<tr>
<td>12</td>
<td>VCC</td>
<td>VCC</td>
</tr>
</tbody>
</table>

Notes: 1. If you wish to use Type 2A or 3A modules, change the connection of the pin to PC3 (remove SS13 and short-circuit SC1).
2. If you wish to use Type 2A or 3A modules, change the connection of the pin to PC1 (remove SS14 and short-circuit SC2).
Figure 5-3  Circuit Schematic for the Pmod™ Connector
5.8 Current Measurement Header

The current measurement header (J5) is used to measure the current drawn by the evaluation MCU (an actual header component is not mounted on the board as shipped). The current drawn can be measured by connecting an ammeter to the evaluation MCU. When this header is to be used, remove the relevant pattern for cutting (SS10) on the soldered side. Figure 5-4 shows the positions of the header and pattern for cutting.

![Figure 5-4 Positions of the Current Measurement Header (J5) and Pattern for Cutting (SS10) (Soldered Side)](image)

5.9 MCU Headers

MCU headers are provided for by two sets of through holes (CN2 and CN3) for 32-pin headers (actual header components are not mounted on the board as shipped). The pin headers have a pitch of 2.54 mm and are connected to the evaluation MCU. Pin numbers of the MCU headers correspond to those of the evaluation MCU, and most pins are connected (with pin 5 as an exception).

5.10 Reset Switch

Pressing the RESET switch applies a hardware reset to the evaluation MCU.

5.11 User Switch

An optional user switch (SW1) is mounted. It is connected to pin 16 of the evaluation MCU, which operates as pin function P34. The IRQ4 interrupt is multiplexed on the same pin.

5.12 Patterns for Cutting

Patterns must be cut or repaired by soldering if necessary. Figure 5-5 shows examples of the states of patterns for cutting.

![Figure 5-5 Examples of States of Patterns for Cutting](image)
5.13 Emulator Reset Header

The emulator is placed in the forced reset state by short-circuiting its reset header (J6) (actual header components are not mounted on the board as shipped). The emulator alone can be operated in the forced reset state without controlling the evaluation MCU from the IDE. Figure 5-6 shows the position of the emulator reset header.

![Emulator reset header](image-url)
6. Configurations

6.1 Modifying the Target Board for RX671

This section describes how to change the setting of this product by using option-link resistors.

An option-link resistor is a 0-Ω surface-mount resistor, which is used to short-circuit or isolate a part of circuits. The subsequent sections contain lists of option-link resistors for individual functions. Fit or remove option-link resistors to switch functions with reference to the list. **Resistor numbers in bold blue type** indicate the initial state of the configuration on the Target Board for RX671 as shipped. For the positions of the option links, refer to chapter 3, Parts Layout.

When removing soldered components, do not apply a soldering iron to the Target Board for RX671 for more than 5 seconds. This time restriction is to avoid any damage components mounted nearby on the board.

6.2 Analog Power Supply

Table 6-1 and Table 6-2 show the option-link resistors for the analog power supply.

**Table 6-1 Option-Link Resistors for the Analog Power Supply of the 12-Bit A/D Converter (Unit 0)**

<table>
<thead>
<tr>
<th>Analog Power-Supply Source of the 12-Bit A/D Converter (Unit 0)</th>
<th>Mounted</th>
<th>Not Mounted</th>
<th>Remark</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power supplied to MCU_VCC</td>
<td>R3, R6</td>
<td>R4, R5</td>
<td>—</td>
</tr>
<tr>
<td>MCU headers</td>
<td>R4, R5</td>
<td>R3, R6</td>
<td>AVCC0 and AVSS0 are assigned to an MCU header (CN3 side).</td>
</tr>
</tbody>
</table>

**Table 6-2 Option-Link Resistors for the Reference Power Supply of the 12-Bit A/D Converter (Unit 0)**

<table>
<thead>
<tr>
<th>Reference Power-Supply Source of the 12-Bit A/D Converter (Unit 0)</th>
<th>Mounted</th>
<th>Not Mounted</th>
<th>Remark</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power supplied to MCU_VCC</td>
<td>R41, R42</td>
<td>R24, R40</td>
<td>—</td>
</tr>
<tr>
<td>MCU headers</td>
<td>R24, R40</td>
<td>R41, R42</td>
<td>VREFH0 and VREFL0 are assigned to an MCU header (CN3 side).</td>
</tr>
</tbody>
</table>

**Table 6-3 Option-Link Resistors for the Analog Power Supply of the 12-Bit A/D Converter (Unit 1)**

<table>
<thead>
<tr>
<th>Analog Power-Supply Source of the 12-Bit A/D Converter (Unit 1)</th>
<th>Mounted</th>
<th>Not Mounted</th>
<th>Remark</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power supplied to MCU_VCC</td>
<td>R9, R12</td>
<td>R10, R11</td>
<td>—</td>
</tr>
<tr>
<td>MCU headers</td>
<td>R10, R11</td>
<td>R9, R12</td>
<td>AVCC1 and AVSS1 are assigned to an MCU header (CN3 side).</td>
</tr>
</tbody>
</table>
6.3 On-Chip Oscillator

Table 6-4 and Table 6-5 show the option-link resistors for the operation of the on-chip oscillator.

**Table 6-4 Option-Link Resistors for the HOCO**

<table>
<thead>
<tr>
<th>Setting of the HOCO</th>
<th>Mounted</th>
<th>Not Mounted</th>
<th>Remark</th>
</tr>
</thead>
<tbody>
<tr>
<td>Oscillating</td>
<td>R18, R19</td>
<td>R20, R23</td>
<td>—</td>
</tr>
<tr>
<td>Halted</td>
<td>R20, R23</td>
<td>R18, R19</td>
<td>Mount a crystal oscillator on X1 or X2 and the load capacitance as C18 or C19.</td>
</tr>
</tbody>
</table>

**Table 6-5 Option-Link Resistors for the LOCO**

<table>
<thead>
<tr>
<th>Setting of the LOCO</th>
<th>Mounted</th>
<th>Not Mounted</th>
<th>Remark</th>
</tr>
</thead>
<tbody>
<tr>
<td>Oscillating</td>
<td>R43</td>
<td>R15</td>
<td>—</td>
</tr>
<tr>
<td>Halted</td>
<td>R15</td>
<td>R43</td>
<td>Mount a crystal oscillator on X3 and the load capacitance as C16 or C17.</td>
</tr>
</tbody>
</table>
7. Handling Precautions

7.1 Adding Load

When load is added while power is being supplied via the USB, the maximum current is 300 mA during operation at 3.3 V.

When load is added while power is being supplied externally, the maximum current is 500 mA regardless of the operating voltage.

7.2 Remodeling the Board

Any modification of the board (including removing the patterns for cutting) shall be conducted at the user’s own responsibility.

7.3 Limitation on the Number of Connected Target Board for RX671 Modules

Connecting the same host PC to multiple Target Board for RX671 modules is not possible.

7.4 Power-Supply Circuits and Usage Conditions

Different power supplies can be selected by remodeling the board. Table 7-1 shows the relationship between power-supply circuits and usage conditions.

Table 7-1 Power-Supply Circuits and Usage Conditions

<table>
<thead>
<tr>
<th>Power-Supply circuit</th>
<th>Usage Condition</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Power supplied to the RX671</td>
</tr>
<tr>
<td>As shipped (default)</td>
<td>3.3 V</td>
</tr>
<tr>
<td>External power-supply header</td>
<td>2.7 V to 3.6 V</td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Notes: 1. Connecting this product to a board supported by Pmod™ shall be conducted at the user’s own responsibility and should only proceed after confirming the specifications of the power supply and interfaces.

2. Modifications after shipment for remodeling the board are stated in this column.
8. Developing Code

8.1 Using the e² studio

Figure 8-1 shows the settings of the e² studio when creating a new project for the Target Board for RX671.

- [Debug hardware]: Select [E2 Lite (RX)].
- [Power Target From The Emulator]: Select [No].

![Figure 8-1 Settings of the e² studio](image)

Note: Do not connect another Target Board for RX671 to your PC while this product is already connected.
8.2 Using CS+

Figure 8-2 and Figure 8-3 show the settings of CS+ when creating a new project for the Target Board for RX671.


![Figure 8-2 Panel for Selecting the Debug Tool](image)

- [Power target from the emulator]: Select [No].

![Figure 8-3 [Connect Settings] Tabbed Page of CS+](image)
9. Additional Information

Technical Support

For details on the RX671-group microcontrollers, refer to the RX671 Group User’s Manual: Hardware.

For details on the RX assembly language, refer to the RX Family User’s Manual: Software.

The latest information is available from this Web page: https://www.renesas.com/rxtb.

Technical Contact Details

America: techsupport.amERICA@renesas.com

Europe: https://www.renesas.com/en-eu/support/contact.html

Global & Japan: https://www.renesas.com/support/contact.html

General information on Renesas microcontrollers can be found on the Renesas website at: https://www.renesas.com/

Note

Do not install the Target Board for RX671 or sample code into your product.

The operation of sample code is not guaranteed. Confirm the operation on your own responsibility.

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<table>
<thead>
<tr>
<th>Rev.</th>
<th>Date</th>
<th>Description</th>
</tr>
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<tbody>
<tr>
<td>1.00</td>
<td>Feb.01.21</td>
<td>— First Edition issued</td>
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