

RH850 Evaluation Platform

RH850/P1x-C – 156BGA PiggyBack board

Y-RH850-P1XC-156PIN-PB-T1-V1

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Chapter 1 Introduction

The RH850/P1x-C Application Board is part of the RH850 Evaluation Platform and serves as a simple and easy to use platform for evaluating the features and performance of Renesas Electronics 32-bit RH850/P1x-C microcontrollers in a 156pin BGA package with 0.65mm pin pitch. The PiggyBack board (Y-RH850-P1XC-156PIN-PB-T1-V1) can be used as a standalone board, or can be mated with a mainboard (e.g. Y-RH850-X1X-MB-T1-V1) for extended functionality.

Main features:

- Socket for mounting of device
- Standalone operation of the board
- Direct supply of device voltage (typ. 3.3V and 1.25V) enabling dual power supply (DPS)
- Device programming capability
- Device debugging capability
- Pin headers for direct access to each functional device pin
- Reset switch
- MainOSC circuitry
- Connectors to MainBoard
- Operating temperature from 0°C to +40°C

This document describes the functionality provided by the PiggyBack board and guides the user through its operation.

For details regarding the operation of the microcontroller, refer to the corresponding User Manual.

Chapter 2 Overview

2.1 Overview

Figures 1 and 2 provide a schematic view of the PiggyBack board.

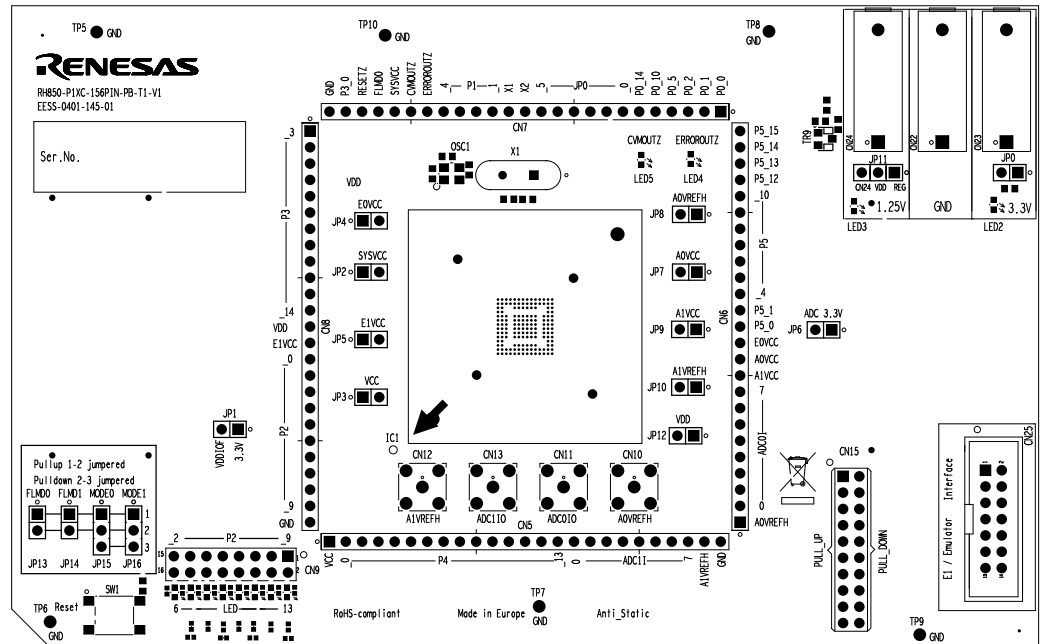


Figure 1 PiggyBack Board Schematic Top View

The black arrow denotes the position of socket pin #1.

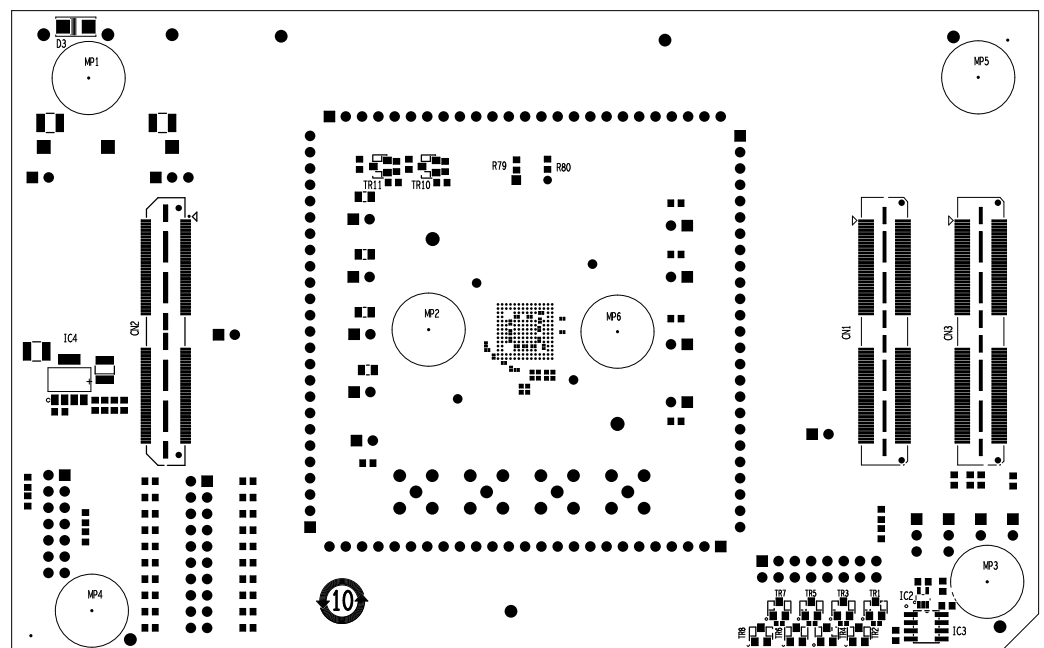


Figure 2 PiggyBack Board Schematic Bottom View

2.2 Mounting of the device

The board is designed for use with the following device:

RH850/P1M-C in 156pin BGA package with 0.65mm pin pitch

The device must be placed inside the socket IC1. To insert the device align the corner of the device package marked with a white square with the #1pin of the socket (see picture below). The #1pin of the socket is marked with a circle near to the "IC1" label (see also black arrow in Figure 1).

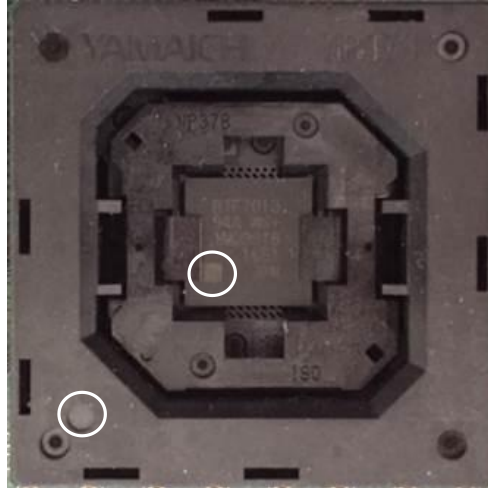


Figure 3 Alignment Mark on Device Package

First, insert the device into the socket with closed mount. Then press down the lid of the socket until the device slips into the socket and finally release the lid.

CAUTION: Please follow the mounting instruction carefully as described. Otherwise, the device might be damaged.

Chapter 3 Power supply

3.1 Board power connection

For operation of the device, a supply voltage must be connected to the board. There are several possibilities to power the device.

Within this document the following voltages are considered as 'typical' connections:

Voltage1 = 3.3V

Voltage2 = 1.25V

Direct voltage supply

Two different voltages can be supplied to the board.

The following connectors are available to supply those voltages directly:

- Three 4mm 'banana-type' connectors:
 - Two red connectors for voltages 3.3V (CN23) and 1.25V (CN24).
 - A black connector for ground (GND) connection (CN22).

Note: The three connectors are supplied with the board but not assembled.

For details about voltage distribution, refer to **Chapter 3.2 'Voltage distribution'**.

Supply by E1 emulator

The E1 emulator that is used for debug purposes and flash programming can also supply a single operating voltage ('Dbg_Voltage'). This voltage is connected to via E0VCC to CN23.

See the documentation of the E1 and **Chapter 5 'Debug and Programming interface'** for details.

Supply by MainBoard

In case the PiggyBack board is mounted on a MainBoard, the on-board regulator of the MainBoard supplies the 3.3V voltage.

CAUTION: Do not supply 3.3V directly to the PiggyBack board in case it is mounted on the MainBoard.

For each of the voltages, 3.3V and 1.25V a green LED is available to signal that the related voltage is available on the PiggyBack board. The corresponding LEDs are placed directly beneath the connectors of the related voltage.

3.2

Voltage distribution

The table shows the required device power supply pins. For detailed explanation of their function, please refer to the user documentation of the corresponding device:

Device Supply Pin
SYSVCC
VCC
EnVCC (n = 0, 1)
AnVCC (n = 0, 1)
AnVREFH (n = 0, 1)
VDD

Additional one power supply for the MainBoard can be selected:

Supply voltage	Function
VDDIOF	IO supply voltage for components located on a connected mainboard.

The following figure shows the configurable voltage distribution on the PiggyBack board.

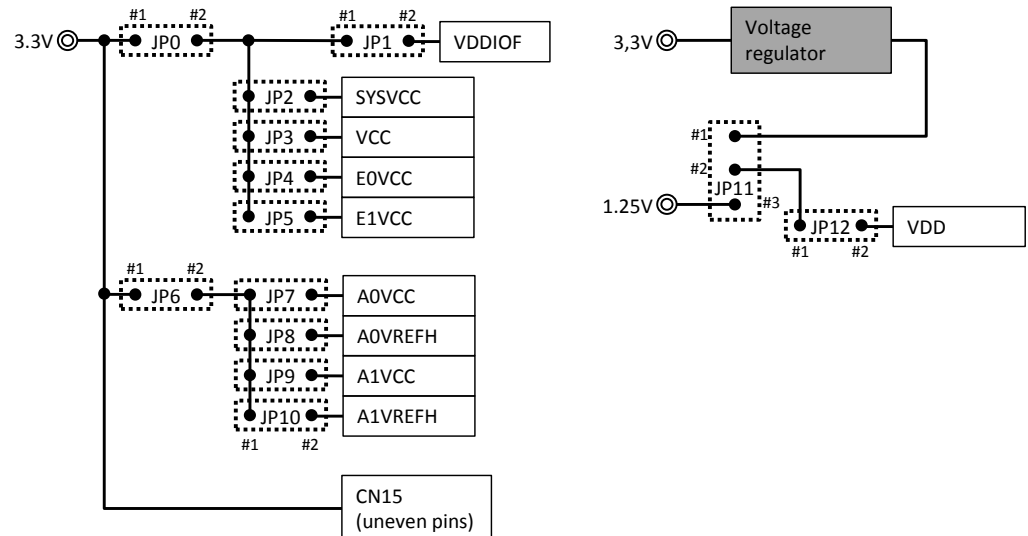


Figure 4 Voltage Distribution on the PiggyBack Board

- All power supply lines can be interrupted by jumpers. This provides the possibility to measure the current consumption of each individual power domain of the device.
- The IO supply voltage for the Mainboard (VDDIOF) can be connected via jumper JP1 to 3.3V, if the PiggyBack board is mounted on a MainBoard.
- VDD can be powered either directly from the 'banana-type' connector (CN24) or by an on-board voltage regulator. Thereby even a DPS device can be operated with one single voltage supply (3.3V).

-
- The source for VDD is selectable by the jumper JP11. The jumper JP12 connects the voltage, configured by JP11 with the VDD pins.

Chapter 4 Clock sources

One external crystal oscillator for the device clock supply is provided with the board.

4.1.1 MainOsc

A crystal or ceramic resonator can be mounted on socket X1.

A 16Mhz oscillator is supplied with the board.

4.1.2 Programmable Oscillator

It is possible to mount a programmable crystal oscillator on the PiggyBack board at OSC1. The available footprint and circuitry is designed for a SG-8002CE programmable crystal oscillator from Epson Toyocom. The output from this oscillator can be connected to port X1 of the device via resistor R82. The SG-8002CE is neither mounted nor provided with the board. For details about the available circuitry refer to **Chapter 10 'Schematic'**. A resonator mounted on socket X1 must not be used in parallel to another clock source.

Chapter 5 Debug and Programming interface

For connection of the microcontroller debug and flash programming tools, the connector CN25 with fourteen pins is provided.

The signal connection of the connector CN25 is shown in the table below:

CN25 Pin	Device Port	Device Signal
1	JP0_2	TCK / LPDCLK / FLSCI3SCKI
2	GND	GND
3	JP0_4	TRSTZ
4	FLMD0	FLMD0
5	JP0_1	TDO / LPDO / FLSCI3TXD
6	-	-
7	JP0_0	TDI / LPDI / FLSCI3RXD / FLSCI3TXD
8	'Dbg_Voltage'	3.3V via E0VCC
9	JP0_3	TMS
10	-	-
11	JP0_5	RDYZ / LPDCLKOUT
12	GND	-
13	RESET	RESETZ
14	GND	-

Chapter 6 Connectors for ports of device

Connection to each functional pin of the devices is possible via the connectors CN5 to CN8.

Please refer to the corresponding User Manual for available pins on the used device.

CAUTION: The pin headers are directly connected to the pins of the device, therefore special care must be taken to avoid any electrostatic or other damage to the device.

6.1 Connectors for ADC voltage supply

It is possible to apply the ADC related supply voltages via SMA connectors (CN10 to CN13). These are directly connected (JP8 and JP10 are bypassed) to the following pins of the device

Device Port
CN11 -> ADC0I0
CN10 -> A0VREFH
CN13 -> ADC1I0
CN12 -> A1VREFH

The SMA connectors are not mounted on nor provided with the boards. SMA connectors that fit to the following mounting holes available on the board can be mounted on the board

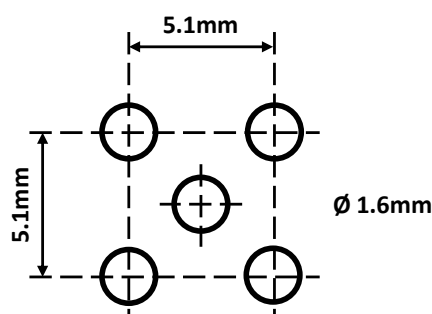


Figure 5 Mounting Holes for SMA Connectors

6.2 Push button for RESET

In order to issue a RESET to the device, the push-button SW1 is available..

6.3 Mode Selection

The PiggyBack Board gives the possibility to configure the following mode pins

- FLMD0 via jumper JP13
- FLMD1 via jumper JP14
- MODE0 via jumper JP15
- MODE1 via jumper JP16

To apply “High” or “Low” to the mode pins, the pins 1 and 2, or the pins 2 and 3 of the corresponding jumper must be shorted, respectively.

Note: Pin 1 is marked by a small circle.

CAUTION: Be careful in configuration of mode related pins, as wrong configuration can cause irregular behaviour of the devices. Be sure to check the corresponding User Manual, for details, which modes can be selected for the used device.

6.4 Connectors to MainBoard

Three connectors (CN1 to CN3) are available to connect the PiggyBack board to a MainBoard. Regarding the function on the MainBoard, please refer to the UM of any supported MainBoard (Y-RH850-X1X-MB-Tx-Vx).

The signal connection of the connectors is described in the following tables.

6.4.1 Connector CN1

Regarding the function on the MainBoard, please refer to the UM of supported MainBoards.

Pin	Function on MainBoard	Device Port	Pin	Function on MainBoard	Device Port
1	-	-	2	-	-
3	-	-	4	-	-
5	RESET	RESETZ	6	NMI	P5_7
7	-	-	8	-	-
9	INT0	P4_7	10	INT1	P2_7
11	INT2	P5_13	12	INT3	P3_8
13	-	-	14	-	-
15	UART0TX	P5_8	16	UART1TX	P5_5
17	UART0RX	P5_9	18	UART1RX	P5_4
19	LIN0TX	P5_8	20	LIN1TX	P5_5
21	LIN0RX	P5_9	22	LIN1RX	P5_4
23	-	P2_0	24	-	-
25	-	P2_1	26	-	-
27	CAN0TX	P5_1	28	CAN1TX	P4_6
29	CAN0RX	P5_0	30	CAN1RX	P4_5
31	SENT0IN	P0_14	32	SENT1IN	P3_3
33	SENT0OUT	P2_4	34	SENT1OUT	P3_13
35	-	-	36	-	-
37	-	-	38	-	-
39	-	-	40	-	-
41	FLX0TX	P3_7	42	FLX0EN	P3_5
43	FLX0RX	P3_14	44	-	P3_4
45	FLX1TX	P3_6	46	FLX1EN	P3_8
47	FLX1RX	P3_3	48	-	P4_6
49	-	-	50	-	-
51	ETH0MDIO	P3_3	52	ETH0MDC	P3_6
53	ETH0RXD0	P4_3	54	EH0TXD0	P3_9
55	ETH0RXD1	P4_4	56	EH0TXD1	P3_10
57	ETH0RXD2	P4_5	58	EH0TXD2	P3_12
59	ETH0RXD3	P4_6	60	EH0TXD3	P3_13

Pin	Function on MainBoard	Device Port	Pin	Function on MainBoard	Device Port
61	ETH0RXDCLK	P4_2	62	ETH0TXCLK	P4_1
63	ETH0RXER	P4_0	64	ETH0TXER	P3_8
65	ETH0CRSDV	P3_7	66	ETH0TXEN	P3_14
67	ETH0RXDV	P4_7	68	ETH0COL	P3_5
69	ETH0RESET	P3_0	70	ETH0LINK	P1_1
71	-	-	72	-	-
73	-	-	74	-	-
75	-	-	76	-	-
77	-	-	78	-	-
79	-	-	80	-	-
81	-	-	82	-	-
83	-	-	84	-	-
85	DIGIO_0	P5_6	86	DIGIO_1	P5_7
87	DIGIO_2	P5_10	88	DIGIO_3	P5_14
89	DIGIO_4	P5_15	90	DIGIO_5	P1_2
91	DIGIO_6	P2_0	92	DIGIO_7	P2_1
93	DIGIO_8	P2_2	94	DIGIO_9	P2_3
95	DIGIO_10	P2_4	96	DIGIO_11	P2_5
97	DIGIO_12	P2_6	98	DIGIO_13	P2_7
99	DIGIO_14	P2_8	100	DIGIO_15	P2_9
101	-	-	102	-	-
103	MUX0	P4_0	104	MUX1	P4_8
105	MUX2	P4_9	106	-	-
107	ADC0	ADC0I0	108	ADC1	ADC0I1
109	ADC2	ADC0I2	110	ADC3	ADC0I3
111	ADC4	ADC1I0	112	ADC5	ADC1I1
113	ADC6	ADC1I2	114	ADC7	ADC1I3
115	VDDIOF	-	116	VDDIOF	-
117	Voltage1	-	118	Voltage1	-
119	Voltage1	-	120	Voltage1	-

6.4.2 Connector CN2

Regarding the function on the MainBoard, please refer to the UM of supported MainBoards.

Pin	Function on MainBoard	Device Port	Pin	Function on MainBoard	Device Port
1	CAN2TX	P4_11	2	-	-
3	CAN2RX	P4_10	4	-	-
5	-	-	6	-	-
7	-	-	8	-	-
9	-	-	10	-	-

Pin	Function on MainBoard	Device Port		Pin	Function on MainBoard	Device Port
11	-	-		12	-	-
13	-	-		14	-	-
15	-	-		16	-	-
17	-	-		18	-	-
19	-	-		20	-	-
21	-	-		22	-	-
23	-	-		24	-	-
25	-	-		26	-	-
27	-	-		28	-	-
29	-	-		30	-	-
31	-	-		32	-	-
33	-	-		34	-	-
35	-	-		36	-	-
37	-	-		38	-	-
39	-	-		40	-	-
41	-	-		42	-	-
43	-	-		44	-	-
45	-	-		46	-	-
47	-	-		48	-	-
49	-	-		50	-	-
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63	-	-		64	-	-
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67	-	-		68	-	-
69	-	-		70	-	-
71	-	-		72	-	-
73	-	-		74	-	-
75	-	-		76	-	-
77	-	-		78	-	-
79	-	-		80	-	-
81	-	-		82	-	-
83	-	-		84	-	-
85	-	-		86	-	-
87	-	-		88	-	-
89	-	-		90	-	-
91	-	-		92	-	-
93	-	-		94	-	-
95	-	-		96	-	-

Pin	Function on MainBoard	Device Port	Pin	Function on MainBoard	Device Port
97	-	-	98	-	-
99	-	-	100	-	-
101	-	-	102	-	-
103	-	-	104	-	-
105	-	-	106	-	-
107	-	-	108	-	-
109	-	-	110	-	-
111	-	-	112	-	-
113	-	-	114	-	-
115	-	-	116	-	-
117	-	-	118	-	-
119	-	-	120	-	-

6.4.3 Connector CN3

Regarding the function on the MainBoard, please refer to the UM of supported MainBoards.

Pin	Function on MainBoard	Device Port	Pin	Function on MainBoard	Device Port
1	-	-	2	-	-
3	-	-	4	-	-
5	-	-	6	-	-
7	-	-	8	-	-
9	-	-	10	CSIH2CSS0	P5_14
11	-	-	12	-	-
13	-	-	14	-	-
15	-	-	16	-	-
17	-	-	18	-	-
19	-	-	20	-	-
21	CSIH2CSS2	P5_0	22	CSIH2CSS3	P5_1
23	-	-	24	-	-
25	-	-	26	DIGIO	P0_0
27	-	-	28	CSIH2SO	P5_6
29	CSIH2SC	P5_7	30	CSIH2SI	P5_10
31	-	-	32	-	-
33	-	-	34	-	-
35	-	-	36	-	-
37	-	-	38	-	-
39	DIDIO	P0_1	40	-	-
41	-	-	42	-	-
43	-	-	44	-	-
45	-	-	46	-	-

Pin	Function on MainBoard	Device Port		Pin	Function on MainBoard	Device Port
47	-	-		48	-	-
49	-	-		50	-	-
51	-	-		52	-	-
53	-	-		54	-	-
55	-	-		56	-	-
57	-	-		58	-	-
59	-	-		60	-	-
61	-	-		62	-	-
63	-	-		64	-	-
65	-	-		66	-	-
67	-	-		68	-	-
69	-	-		70	-	-
71	-	-		72	-	-
73	-	-		74	-	-
75	-	-		76	-	-
77	-	-		78	-	-
79	-	-		80	-	-
81	-	-		82	-	-
83	-	-		84	-	-
85	-	-		86	-	-
87	-	-		88	-	-
89	-	-		90	-	-
91	-	-		92	-	-
93	-	-		94	-	-
95	-	-		96	-	-
97	-	-		98	-	-
99	-	-		100	-	-
101	-	-		102	-	-
103	-	-		104	-	-
105	-	-		106	-	-
107	-	-		108	-	-
109	-	-		110	-	-
111	-	-		112	-	-
113	-	-		114	-	-
115	-	-		116	-	-
117	-	-		118	-	-
119	-	-		120	-	-

Chapter 7 Other circuitry

7.1 Signalling for CVMOUTZ and ERROROUTZ

Two red LEDs, LED5 and LED4 are available two indicate a “low” output signal from CVMOUTZ and ERROROUTZ, respectively.

7.2 Pin Headers for Pull-Down and Pull-Up

A connector CN15 is available to enable easy connection to 3.3V or GND via pull-up or pull-down resistances, respectively.

Hereby uneven pins from 1 to 19 (in total ten) are configured as pull-up pin headers, while the even numbers from 2 to 20 (in total ten) can be used for pull-down.

By connecting device port pins from CN5 – 8 to CN9 it is therefore possible to pull a desired port pin to “Low” or “High”.

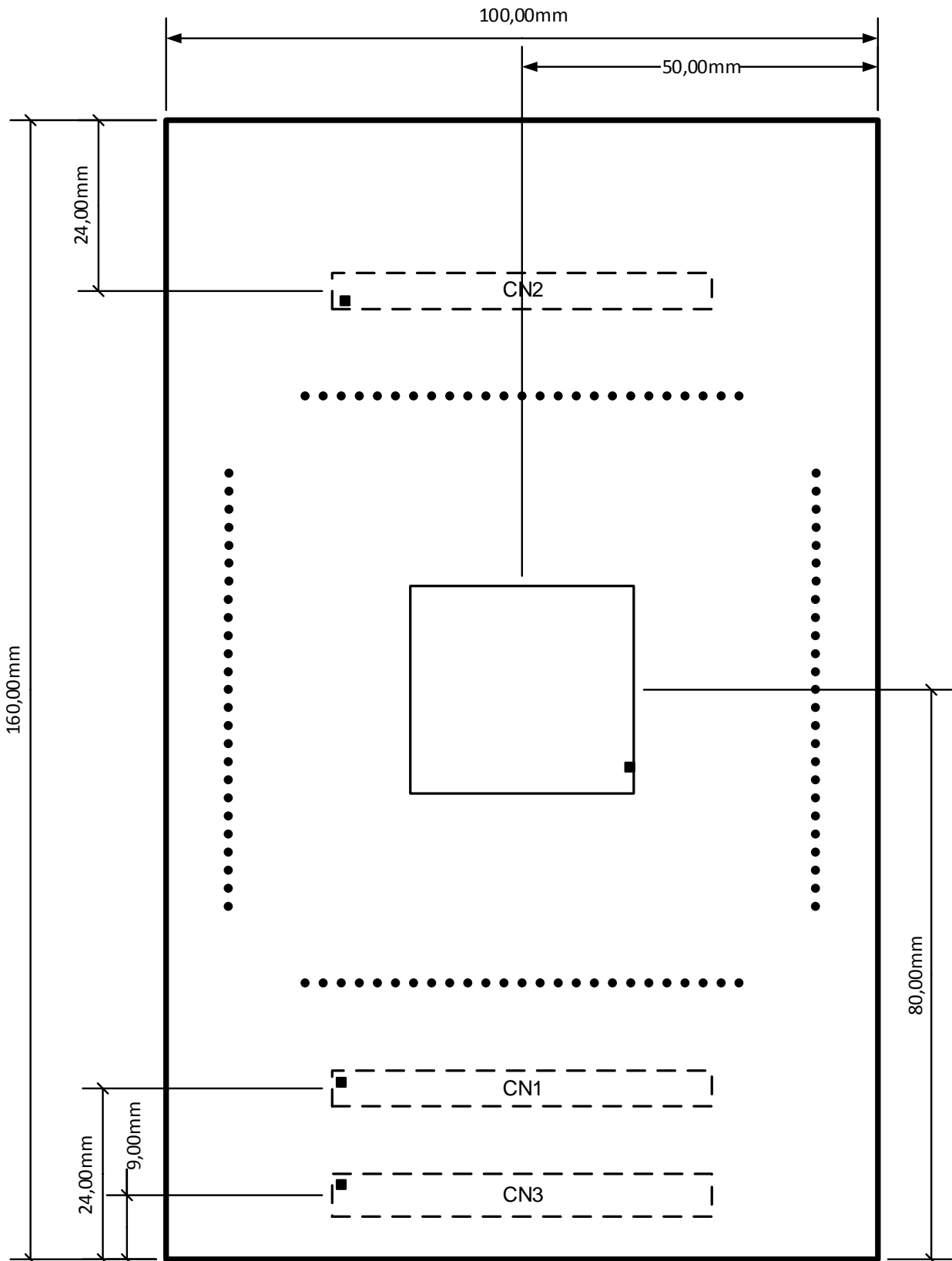
7.3 Signalling LEDs

Eight LEDs are provided to allow visual observation of the output state of device port pins. Device pins P2_2 to P2_9 are connected to the uneven pins 15 to 1 of the pin header CN9, while the LEDs 6 to 13 are connected to the even pins 16 to 2, respectively. Thus the LEDs can be either connected to the device port pins P2_2 to P2_9 via jumper or any device port pin can be connected directly to the even pin headers.

Chapter 8 Precautions

No limitations are known at the release of this document.

Chapter 9 Mechanical dimensions



Chapter 10 Schematic

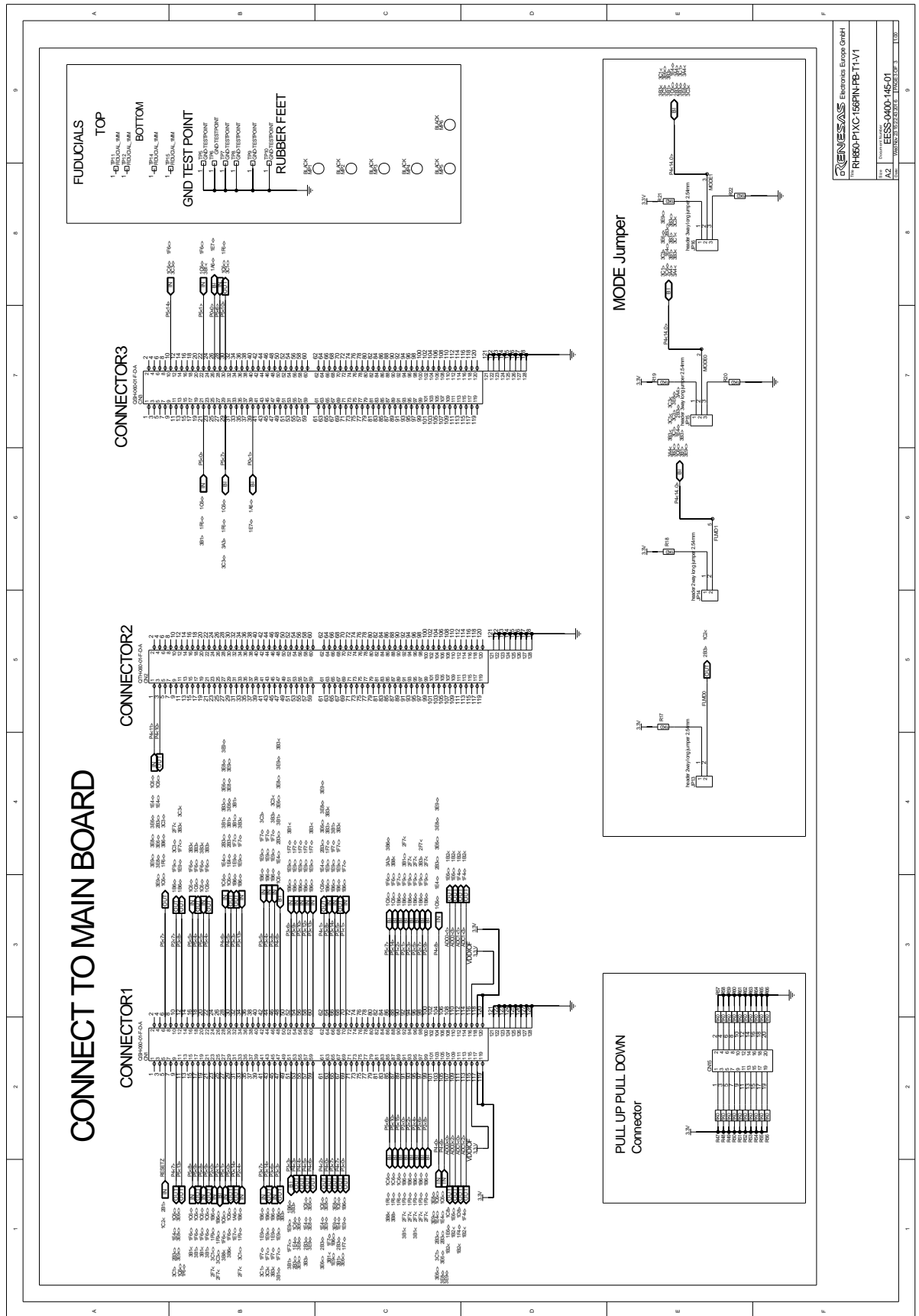
CAUTION: *The schematic shown in this document is not intended to be used as a reference for mass production. Any usage in an application design is in sole responsibility of the customer.*

The following components described in the schematic are not provided with the board:

- SMA connectors
 - CN10
 - CN11
 - CN12
 - CN13
- Oscillator OSC1
- Resistances
 - R1
 - R2
 - R12
 - R79
 - R80
 - R82
- Capacitors
 - C1
 - C2

The following components described in the schematic are provided with but not mounted on the board:

- Standard 4mm power lab sockets
 - CN22
 - CN23
 - CN24



Chapter 11 Revision History

The table provides information about the major changes of the document versions.

Date	Version	Description
2017-05-11	1.0	Initial release

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