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April 1st, 2010
Renesas Electronics Corporation

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User's Manual

QB-78F8032-EA-02T

Exchange adapter
(On board LIN Transceiver & Voltage Regulator)

Target Device:
μPD78F8032D
μPD78F8030(A2)
μPD78F8029(A2)
μPD78F8028(A2)
μPD78F8027(A2)
μPD78F8026(A2)

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(M8E0909)

第1章　概説

QB-78F8032-EA-02T は、電源と LIN Transceiver 機能を搭載したエクスチェンジ・アダプタです。

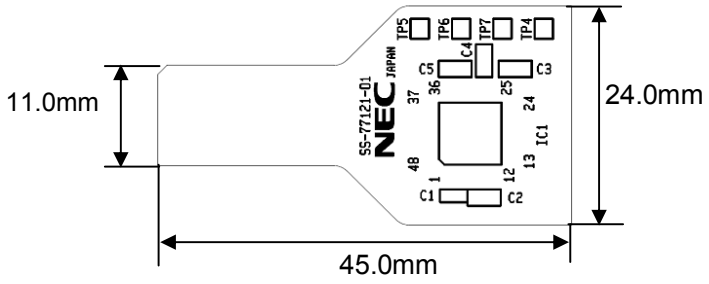
μPD78F8026(A2)/78F8027(A2)/78F8028(A2)/78F8029(A2)/78F8030(A2)/78F8032D を用いたシステム開発において、本製品と QB-78K0KX2 を組み合わせるとハードウェアおよびソフトウェアを効率的にディバッグできます。

CHAPTER　1　GENERAL

The QB-78F8032-EA-02T is an exchange adapter on board voltage regulator and LIN transceiver function.

Hardware and software can be debugged efficiently in the development of systems in which the μPD78F8026(A2)/78F8027(A2)/78F8028(A2)/78F8029(A2)/78F8030(A2)/78F8032D are used by combining with this product and QB-78K0KX2.

Top View (QB-78F8032-EA-02T parts side)



Bottom View (QB-78F8032-EA-02T connection side)

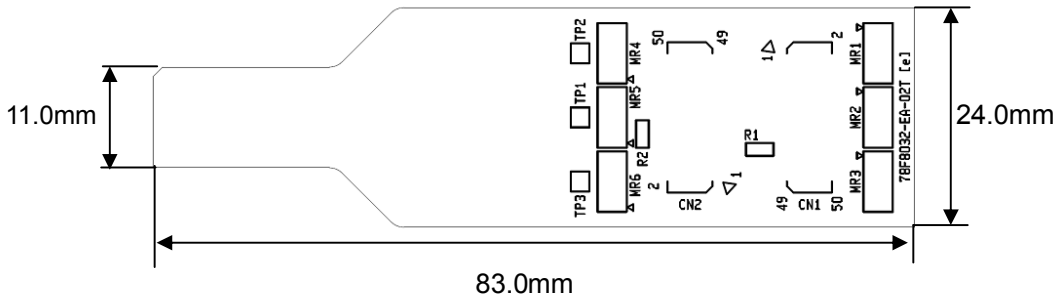
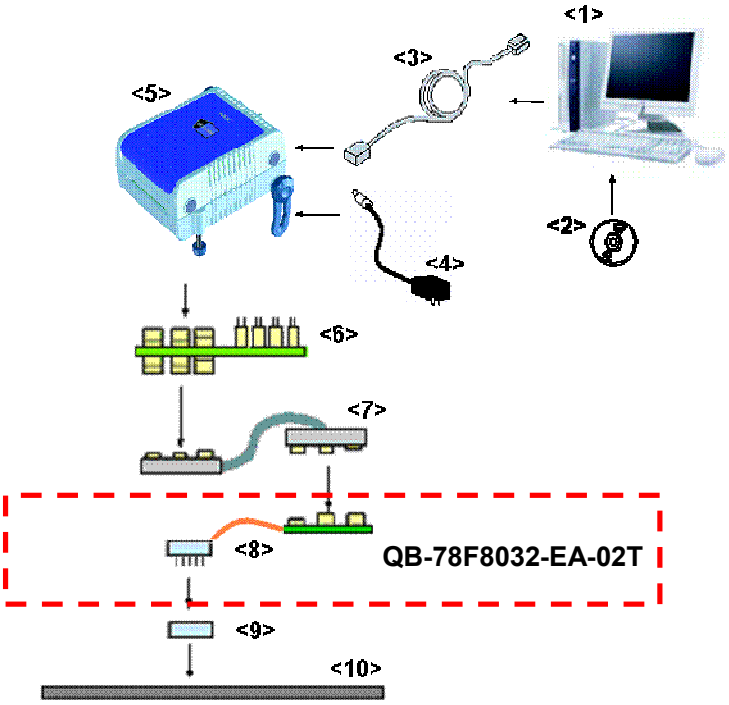
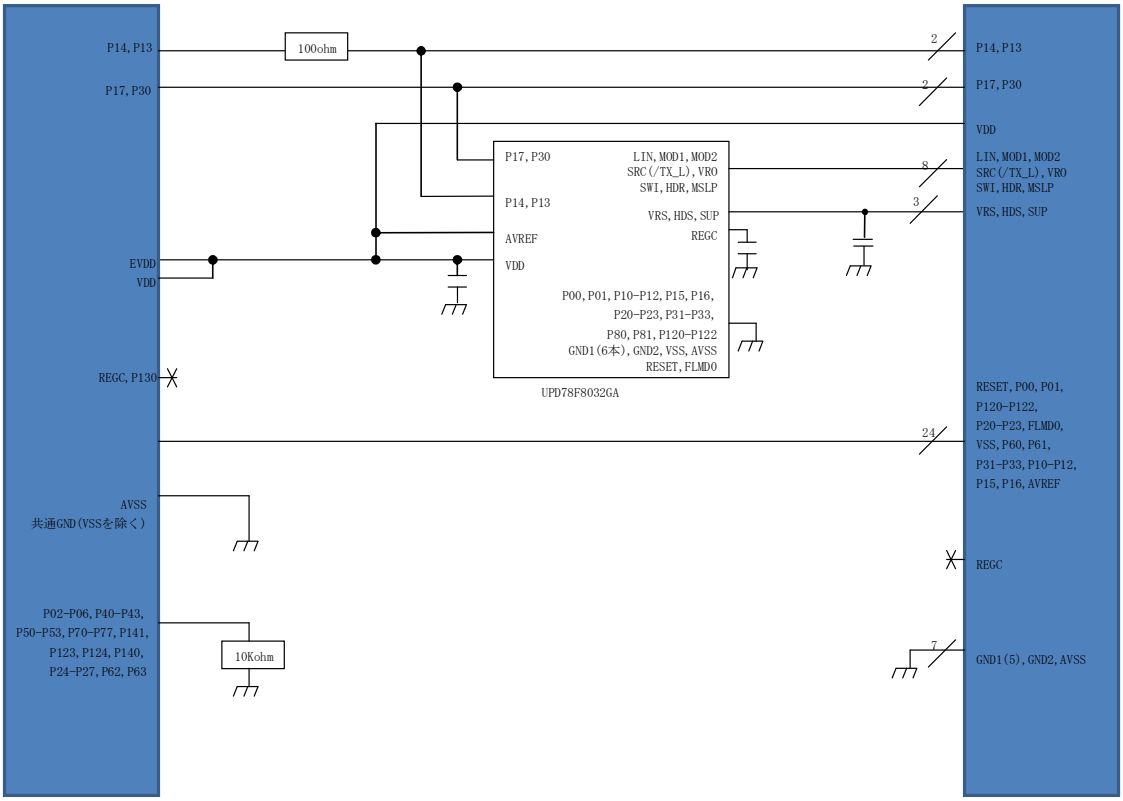


Figure 1-1. Emulation System Configuration for µPD78F8026/78F8027/78F8028/78F8029/78F8030/78F8032D



<1>	Host machine:	Windows PC (Windows2000 and WindowsXP) IBM PC/AT compatible can be used
<2>	ID78K0-QB Disk/Accessory Disk:	Debugger, USB drivers, manual, etc.
<3>	USB interface cable:	Cable connecting QB-78K0KX2 to host machine
<4>	AC adapter:	AC adapters classified by region
<5>	QB-78K0KX2:	In-Circuit Emulator
<6>	Check pin adapter:	Adapter used when observing waveforms on oscilloscope
<7>	Emulation probe:	Flexible type of emulation probe
<8>	**Exchange adapter:** **QB-78F8032-EA-02T**	**On board LIN transceiver and Voltage Regulator adapter that performs pin conversion**
<9>	Target connector: QB-48K8-NQ-01T	Connector soldered to target system
<10>	Target system	

QB-78F8032-EA-02T Block Diagram



QB-78K0KX2 コネクタ　　Target コネクタ

第2章　注意事項

１．QB-78F8032-EA-02T は、電源回路、LIN トランシーバ回路の過熱保護機能を保証していません。
　　QB-78F8032-EA-02T を過熱状態および過電流状態で使用しないでください。
２．QB-78F8032-EA-02T 上の SUP,AVREF,VDD 端子にターゲット・ボードから電源が必ず供給された状態でディバッグしてください。
３．SUP と HDS は同電位,VRO,VRS と VDD 端子は同電位としてください。
４．SUP,HDR,HDS,LIN,SWI 端子には、20V 以上印加しないでください。

CHAPTER　2　CAUTIONS

1. The over temperature protect function of Voltage regulator circuit and LIN transceiver circuit is not guarantee on QB-78F8032-EA-02T. To operate with overheat or over current on QB-78F8032-EA-02T is not allowed.
2. In the debug, SUP, AVREF, VDD terminal on QB-78F8032-EA-02T have to be supplied power by target board.
3. Make SUP the same potential as HDS, and make VDD the same potential as VRO, VRS.
4. To supply the SUP, HDR, HDS, LIN and SWI terminal more than 20V is not allowed.

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