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April 1st, 2010 Renesas Electronics Corporation

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M34519T2-CPE

User's Manual

Compact Emulator for 4518, 4519, 4583 and 4584 Groups

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Preface

The M34519T2-CPE is a compact emulator for the 4518, 4519, 4583 and 4584 Groups MCUs with the real-time trace function. This user's manual mainly describes specifications of the M34519T2-CPE compact emulator and how to setup it. For details on the following products included with the M34519T2-CPE, refer to each product's online manual.

- Emulator debugger: M3T-PD45M
- Assembler: ASM45

All the components of this product are shown in "1.1 Package Components" (page 13). If there is any question or doubt about this product, contact your local distributor.

The related manuals for using this product are listed below. You can download the latest manuals from the Renesas Tools homepage (http://www.renesas.com/en/tools).

Related manuals

| Item | Manual | |
|-------------------|-------------------------|--|
| Emulator debugger | M3T-PD45M User's Manual | |
| Assembler | ASM45 User's Manual | |



Important

Before using this product, be sure to read this user's manual carefully. Keep this user's manual, and refer to this when you have questions about this product.

Emulator:

The emulator in this document refers to the following products that are manufactured by Renesas Technology Corp.:

- (1) Compact emulator main unit
- (2) Package converter board for connecting the user system

The emulator herein does not include the customer's user system and host machine.

Purpose of use of the emulator:

This emulator is a device to support the development of a system that uses the 4500 Series 4518, 4519, 4583 and 4584 Groups of Renesas 4-bit single-chip MCUs. It provides support for system development in both software and hardware.

Be sure to use this emulator correctly according to said purpose of use. Please avoid using this emulator for other than its intended purpose of use.

For those who use this emulator:

This emulator can only be used by those who have carefully read this user's manual and know how to use it. Use of this emulator requires the basic knowledge of electric circuits, logical circuits, and MCUs.

When using the emulator:

- (1) This product is a development supporting unit for use in your program development and evaluation stages. In mass-producing your program you have finished developing, be sure to make a judgment on your own risk that it can be put to practical use by performing integration test, evaluation, or some experiment else.
- (2) In no event shall Renesas Solutions Corp. be liable for any consequence arising from the use of this product.
- (3) Renesas Solutions Corp. strives to renovate or provide a workaround for product malfunction at some charge or without charge. However, this does not necessarily mean that Renesas Solutions Corp. guarantees the renovation or the provision under any circumstances.
- (4) This product has been developed by assuming its use for program development and evaluation in laboratories. Therefore, it does not fall under the application of Electrical Appliance and Material Safety Law and protection against electromagnetic interference when used in Japan.
- (5) Renesas Solutions Corp. cannot predict all possible situations or possible cases of misuse where a potential danger exists. Therefore, the warnings written in this user's manual and the warning labels attached to this emulator do not necessarily cover all of such possible situations or cases. Please be sure to use this emulator correctly and safely on your own responsibility.
- (6) This product is not qualified under UL or other safety standards and IEC or other industry standards. This fact must be taken into account when taking this product from Japan to some other country.



Usage restrictions:

This emulator has been developed as a means of supporting system development by users. Therefore, do not use it as a device used for equipment-embedded applications. Also, do not use it for developing the systems or equipment used for the following purposes either:

- (1) Transportation and vehicular
- (2) Medical (equipment where human life is concerned)
- (3) Aerospace
- (4) Nuclear power control
- (5) Undersea repeater

If you are considering the use of this emulator for one of the above purposes, please be sure to consult your local distributor.

About product changes:

We are constantly making efforts to improve the design and performance of this emulator. Therefore, the specification or design of this emulator or its user's manual may be changed without prior notice.

About the rights:

- (1) We assume no responsibility for any damage or infringement on patent rights or any other rights arising from the use of any information, products or circuits presented in this user's manual.
- (2) The information or data in this user's manual does not implicitly or otherwise grant a license for patent rights or any other rights belonging to us or third parties.
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About diagrams:

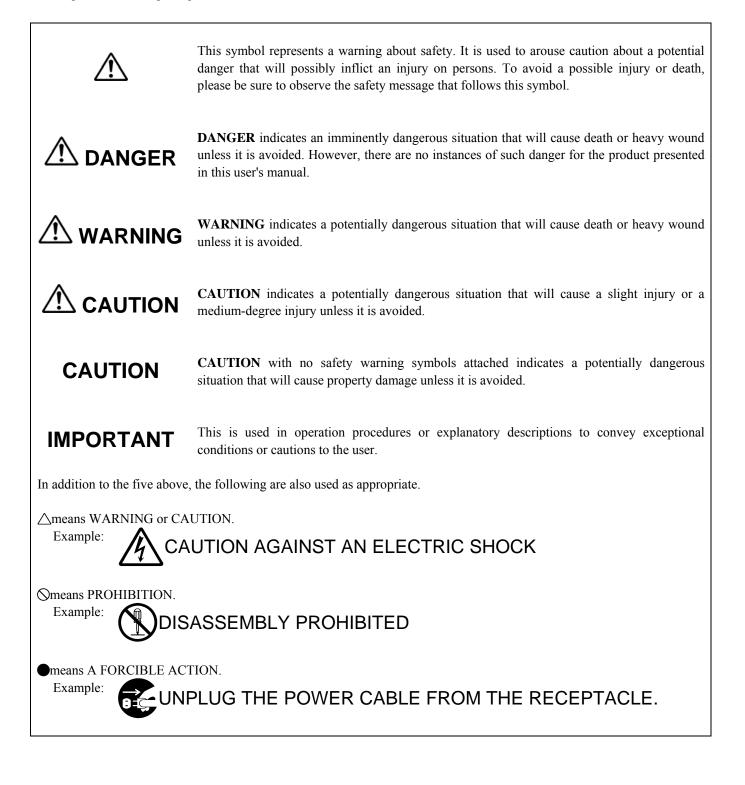
The diagrams in this user's manual may not all represent exactly the actual object.

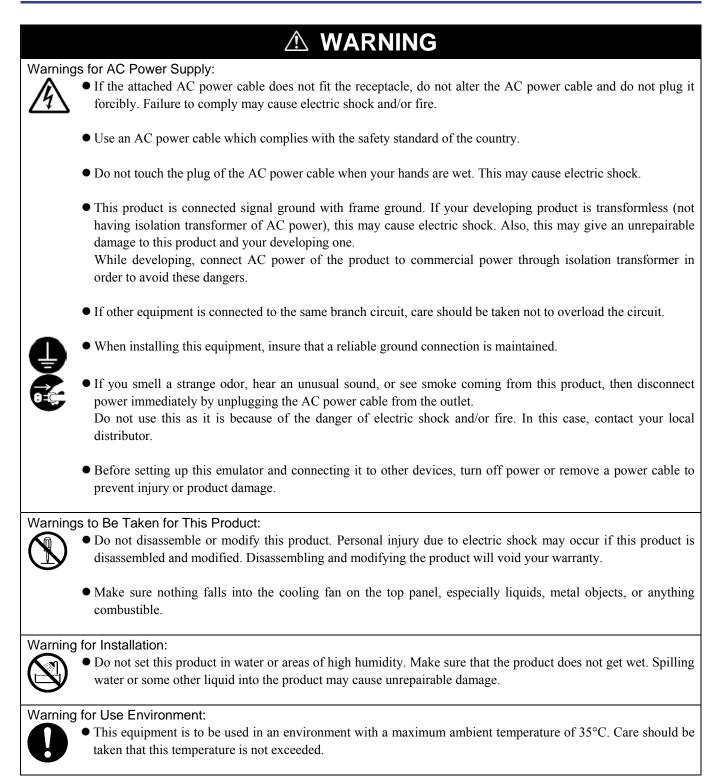
Precautions for Safety

Definitions of Signal Words

In both the user's manual and on the product itself, several icons are used to insure proper handling of this product and also to prevent injuries to you or other persons, or damage to your properties.

This chapter describes the precautions which should be taken in order to use this product safely and properly. Be sure to read this chapter before using this product.







| Notes | On Connecting the Power Supply of the Emulator: Do not use any power cable other than the one that is included with the product. |
|--------|--|
| | • The power cable included with the product has its positive and negative poles color-coded by red and black respectively. |
| | • Pay attention to the polarities of the power supply. If its positive and negative poles are connected in reverse the internal circuit may be broken. |
| | • Do not apply any voltages exceeding the product's rated power supply voltage (5.0 V ±5%). Extreme voltage may cause a burn due to abnormal heat or cause the internal circuit to break down. |
| Cautio | ns to Be Taken for Turning On the Power: |
| | • Turn ON/OFF the power of the emulator and user system as simultaneously as possible. |
| | • Do not leave either the emulator or user system powered on, because of leakage current the internal circuits ma be damaged. |
| | • When turning on the power again after shutting off the power, wait about 10 seconds. |
| Cautio | ns to Be Taken for Handling This Product: |
| | • Use caution when handling the main unit. Be careful not to apply a mechanical shock. |
| | • Do not touch the connector pins of the emulator main unit and the target MCU connector pins directly. Stati electricity may damage the internal circuits. |
| | • Do not pull this emulator by the communications interface cable or the flexible cable for connecting the use system. And, excessive flexing or force may break conductors. |
| Cautio | n to Be Taken for System Malfunctions: |
| | • If the emulator malfunctions because of interference like external noise, do the following to remedy the trouble (1) Press the RESET button on the emulator upper panel. |

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User Registration

When you have purchased the emulator presented in this user's manual, please be sure to register it. As the hardware tool user registration FAX sheet is included with this manual, fill it in and FAX it to your local distributor or email the same contents to the following address. Your registered information is used for only after-sale services, and not for any other purposes. Without user registration, you will not be able to receive maintenance services such as a notification of field changes or trouble information. So be sure to carry out the user registration.

For more information about user registration, please email to the following address.

regist_tool@renesas.com



Terminology

Some specific words used in this user's manual are defined as follows:

Emulator M34519T2-CPE

This means the compact emulator (this product) for 4518, 4519, 4583 and 4584 Groups MCUs.

Emulator system

This means an emulator system built around the M34519T2-CPE emulator. The M34519T2-CPE emulator system is configured with an emulator main unit M34519T2-CPE, emulator debugger M3T-PD45M and host machine.

Emulator debugger M3T-PD45M

This means a software tool which you can control the emulator from a host machine through the USB interface.

Firmware

This means a program stored in the flash ROM of the emulator. It analyzes contents of communication with the emulator debugger and controls the emulator M34519T2-CPE. This program is downloadable from the emulator debugger to upgrade firmware, etc.

Host machine

This means a personal computer used to control the emulator system.

Target MCU

This means the microcomputer you are going to debug.

User system

This means a user's application system using the microcomputer to be debugged.

User program

This means a user's application program to be debugged.

Evaluation MCU

This means a microcomputer mounted on the emulator which is operated in the special mode for the emulator.

*

In this user's manual, this symbol is used to show active LOW. (e.g. RESET*)



This chapter describes the package components, the system configuration and the preparation for using this product for the first time.

1.1 Package Components

The M34519T2-CPE package consists of the following items. When unpacking it, check to see if your M34519T2-CPE contains all of these items.

Table 1.1 Package components

| Item | Quantity | | | |
|--|----------|--|--|--|
| M34519T2-CPE compact emulator | 1 | | | |
| M34584MD-001FP MCU | 1 | | | |
| 50-wire normal-pitch cable for connecting user system | 1 | | | |
| External trace cable | 1 | | | |
| OSC-2 (6MHz) oscillator circuit board | 1 | | | |
| OSC-2 oscillator circuit bare board | 1 | | | |
| USB interface cable for connecting host machine and emulator | 1 | | | |
| Power supply cable for compact emulator | | | | |
| Hardware tool user registration FAX sheet (English) | | | | |
| Hardware tool user registration FAX sheet (Japanese) | 1 | | | |
| M34519T2-CPE User's Manual (this manual) | | | | |
| M34519T2-CPE User's Manual (Japanese) | | | | |
| M34519T2-CPE Release Notes (English) | | | | |
| M34519T2-CPE Release Notes (Japanese) | | | | |
| CD-ROM - Emulator debugger M3T-PD45M | 1 | | | |
| - Assembler ASM45 | | | | |

* Please keep the M34519T2-CPE's packing box and cushion material in your place for reuse at a later time when sending your product for repair or other purposes. Always use these packing box and cushion material when transporting this product.

* If there is any question or doubt about the packaged product, contact your local distributor.



1.2.1 System Configuration

Figure 1.1 shows a configuration of the M34519T2-CPE system.

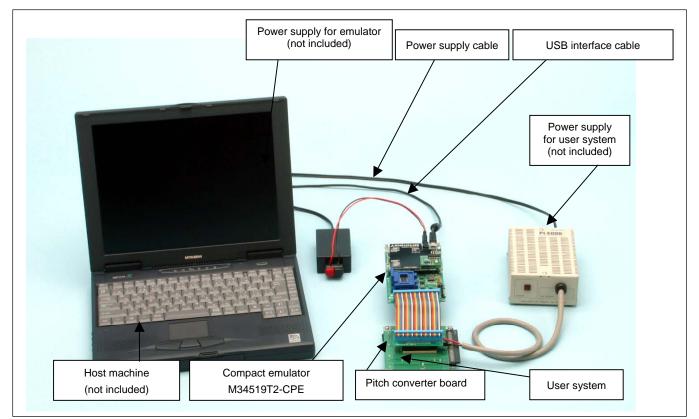


Figure 1.1 System configuration

- Compact emulator M34519T2-CPE (this product) This is a compact emulator for the 4518, 4519, 4583 and 4584 Groups with the real-time trace functions (hereafter, emulator).
- (2) USB interface cable (included)

This is an interface cable for the host machine and the emulator.

(3) Power supply for emulator

This is a power supply for the emulator. Supply 5.0 V \pm 5% (DC).

Prepare a power supply separately. The power cable is included with this product.

Note: Be aware that there are some AC adapters whose power supply voltage varies rather widely with its load. You are recommended to use an AC adapter with a switching power supply or a stabilized power supply.

(4) User system

This is your application system. This emulator can be used without the user system.



(5) Power supply for the user system

This is a power supply for the user system. As this emulator cannot supply the power to the user system, supply the power to the user system separately from the emulator.

(6) Host machine

This is a personal computer for controlling the emulator.

(7) Pitch converter board

This is a pitch converter board for connecting to an MCU foot pattern on the user system. For details, refer to "2.8 Connecting the User System" (page 31).

1.2.2 Names and Functions of each part of the Emulator

Figure 1.2 shows the names of the LEDs on the upper panel of the emulator.

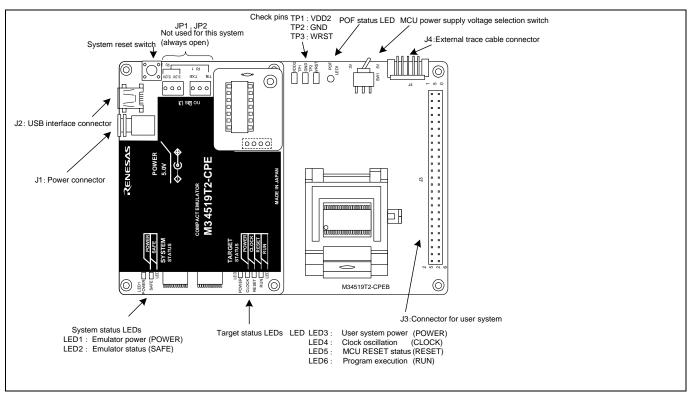


Figure 1.2 Names of the LEDs on the upper panel of the M34519T2-CPE

(1) System Status LEDs

The system status LEDs indicate the emulator main unit's operating status etc. Table 1.2 lists the definition of the system status LEDs.

| Name | Number | Color | Status | Meaning | |
|-------|--------|--------|--------|---|--|
| POWER | LED1 | Orange | ON | Power is supplied to the emulator. | |
| | | | OFF | Power is not supplied to the emulator. | |
| SAFE | LED2 | Green | ON | Emulator system has started normally. | |
| | | | OFF | Emulator system has not started normally. | |

Table 1.2 Definitions of the system status LEDs

(2) Target Status LEDs

The target status LEDs indicate the target MCU's power supply and operating status. Table 1.3 lists the definition of each target status LED.

| Name | Number | Color | Status | Meaning | |
|-------|--------|--------|--------|--|--|
| POWER | LED3 | Orange | ON | Power is supplied to the target MCU. | |
| | | _ | OFF | Power is not supplied to the target MCU. | |
| CLOCK | LED4 | Green | ON | Clock is output from the target MCU. | |
| | | | OFF | Clock is not output from the target MCU. | |
| RESET | LED5 | Red | ON | Target MCU is being reset. | |
| | | | OFF | Target MCU is not being reset. | |
| RUN | LED6 | Green | ON | User program is being executed. | |
| | | | OFF | User program is not being executed. | |



(3) POF Status LED

This LED shows whether the MCU is in a state of POF

Table 1.4 Definitions of the target status LEDs

| Name | Color | Status | Meaning | | | |
|--------------|-------|---|--------------------------|--|--|--|
| DOE OWNER ON | | ON | MCU is in a state of POF | | | |
| POF Orange | OFF | MCU is not in a state of POF, but normal. | | | | |

(4) System Reset Switch

By pressing the system reset switch, you can initialize the emulator system. Table 1.5 shows the functions of the system reset switch depending on the state of the emulator.

Table 1.5 Definitions of the target status LEDs

| State of Emulator | Function | |
|---------------------------------------|---|--|
| Pressing the system reset switch when | Initializes the emulator and waits for a command from the | |
| the user's program is halted | emulator debugger | |
| Pressing the system reset switch when | Stops the user's program, initializes the emulator, and | |
| the user's program is executed | waits for a command from the emulator debugger. | |

IMPORTANT

Notes on a System Reset:

- After pressing the system reset switch, restart the emulator debugger M3T-PD45M. Otherwise the display of emulator debugger and the actual value (in the emulator) may not match.
- When the emulator debugger does not start up normally even after rebooting, turn off the emulator and then turn on again.

(5) Power Connector (J1)

This is a connector for connecting the power supply to this product. For details, refer to "2.3 Connecting the Power Supply for the Emulator" (page 22).

(6) USB Cable Connector (J2)

This is a USB cable connector for connecting the host machine to this product. For details, "2.4 Connecting the Host Machine" (page 23).

(7) External Trace Cable Connector (J4)

This is an external trace cable connector when using an external trace function. For details, "2.10 Connecting the External Trace/Trigger Cable" (page 39).

(8) MCU Power Supply Voltage Selection Switch (SW1)

This is a setting for an MCU power supply voltage. For details, refer to "2.5.1 MCU Power Supply Voltage Selection Switch" (page 24)



1.3 Specifications

Table 1.6 lists specifications of the M34519T2-CPE .

| Applicable MCUs | 4518, 4519, 4583 and 4584 Groups | | | | | |
|---|--|---|------------------------|------------|--|--|
| Evaluation MCU | M34519M8-001FP (mounted in the socket of the emulator) | | | | | |
| | M34584MD-001FP (included with this product) | | | | | |
| Maximum operating frequency | 3.0 V | | Divided-by 8-mode | | | |
| | Set the MCU power | er supply voltage selection switch | Divided-by 4-mode | 6.0 MHz | | |
| | (SW1) to 3V. | | Divided-by 2-mode | | | |
| | | | Through mode | 4.4 MHz | | |
| | 5.0 V | | Divided-by 8-mode | | | |
| | Set the MCU power | Divided-by 4-mode | | | | |
| | (SW1) to 5V. | | Divided-by 2-mode | 6.0 MHz | | |
| | | | Through mode | | | |
| Applicable power supply | 3.0 V ±5 % or 5.0 V | ±5 % | · · · · | | | |
| | - The power supply | can be selected by a switch on the M | 34519T2-CPEB | | | |
| | - Available only from | m the emulator, not from the user sys | tem | | | |
| Basic debugging functions | - Download | | | | | |
| | | nax. 8 points, break after execution) | | | | |
| | | n/stop (allows free-run execution sup | porting software break | s) | | |
| | - Memory reference | 1 | F 8 | ~) | | |
| | - Register reference | • | | | | |
| | - Disassemble disp | - | | | | |
| Real-time trace function | Recording cycle | 32768 cycles | | | | |
| | Trace point- 2 address points (range/pass count can be set) | | | | | |
| | - 1 external trigger point | | | | | |
| | Trace mode | Trace mode - Before Break mode (Records 32768 cycles before program s | | | | |
| | - Before Trace mode (Records 32768 cycles before event or | | | | | |
| | | 68 cycles before/after e | | | | |
| | - After Trace mode (Records 32768 cycles after event on) | | | | | |
| Hardware break function | | | | | | |
| | point | - 1 external trigger point | | | | |
| | Break mode - Address break or trigger break | | | | | |
| | | - Stack over/under flow | | | | |
| | | - Trace event | | | | |
| | - Break at end of trace | | | | | |
| T: | т: | - Timer | | | | |
| Time measurement | Time measurement point: 2 address point designation (range can be set) Resolution: 100 ns | | | | | |
| | Measurement interval: 8 types | | | | | |
| | Count source: Emulator timer or MCU cycle | | | | | |
| Coverage | | | | | | |
| Coverage Connection to user system | C0 coverage | | | | | |
| (see "2.8 Connecting the User | For 50-pin 2.54-mm-pitch DIP | | | | | |
| (see 2.8 Connecting the User System" on page 31) | · · · · · · | | | | | |
| System on page 51) | For 32-pin SDIP M34513T-PTCA (not included) | | | | | |
| | For 32-pin LQFP | | | | | |
| | M34513T-PTCA (not included), M34513T-PTCB (not included), M34513T- | | | | | |
| | PTCC (not included) | | | | | |
| Power supply for emulator | DC 5.0 V \pm 5 %/(2 A) externally supplied (Prepare a power supply separately.) | | | | | |
| Host machine interface | USB (USB 1.1 full-speed, mini-B standard connector) | | | | | |
| | | s USB 2.0 With the USB interfac | a not all hardwara (a | uch as hos | | |

* Available to connect the host machine that supports USB 2.0. With the USB interface, not all hardware (such as host machine, USB devices, USB hub) combination will work and guaranteed.

Be sure to use thins emulator with the operating environmental of the emulator and host machine listed in Tables 1.7 and 1.8.

Table 1.7 Operating environmental conditions

| Item | Description |
|-----------------------|----------------------|
| Operating temperature | 5 to 35°C (no dew) |
| Storage temperature | -10 to 60°C (no dew) |

Table 1.8 Operating environment of the host machine

| Item | Description |
|-------------------------------|---|
| Host machine | IBM PC/AT compatibles with USB1.1 |
| OS | Windows Me |
| | Windows 98 |
| | Windows XP |
| | Windows 2000 |
| CPU | Pentium III 233 MHz or more recommended |
| Memory | 128 MB or more recommended |
| Pointing device such as mouse | Mouse or any other pointing device usable with the above OS that can be |
| | connected to the main body of the host machine. |
| CD drive | Needed to install the emulator debugger or refer to the user's manual |

* Windows and Windows NT are either registered trademarks or trademarks of Microsoft Corporation in the United States and other countries.

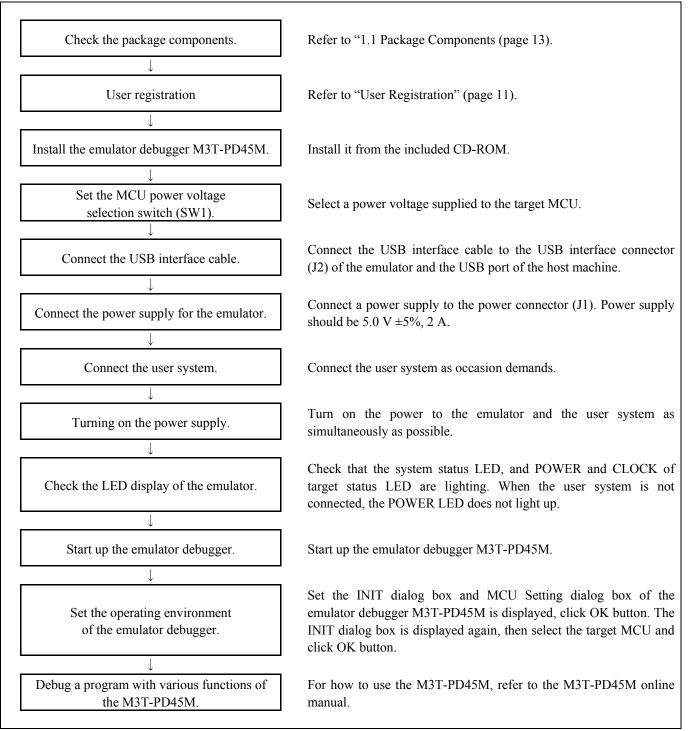


2. Setup

This chapter describes the preparation for using this product, the procedure for starting up the emulator and how to change settings.

2.1 Flowchart of Starting Up the Emulator

The procedure for starting up the emulator is shown in Figure 2.1. For details, refer to each section hereafter. And, when the emulator does not start up normally, refer to "5. Troubleshooting" (page 65).







2.2 Installing the Emulator Debugger

If the OS used in your host machine is Windows XP or 2000, this installation must be executed by a user with administrator rights. Be aware that users without administrator rights cannot complete the installation.

2.2.1 Installing the Emulator Debugger M3T-PD45M

From the CD-ROM included with your product, install the emulator debugger M3T-PD45M following the procedure described below.

- Launching the installer
 From Windows Explorer, etc., start the "setup.exe" program present in the \PD45M\W95E folder of the product disc.
- (2) Entering the user information

In the "user information" dialog box, enter the user information (contractor, section, contact address, and host machine). The supplied information will be turned into a format by which technical support will be provided by e-mail.

(3) Selecting components

In the "component selection" dialog box, select the components you want to install. In this dialog box you can change the directory in which to install.

(4) Completing the installation

A dialog box will be displayed indicating that setup has been completed. It means that the installation you made is completed.

2.2.2 Installing the USB Device Driver

Install the USB device driver following the procedure described below.

- (1) Connect the host machine and the compact emulator M34519T2-CPE with the USB cable.
- (2) Turn on the power to the compact emulator M34519T2-CPE.
- (3) A USB device will be detected, and the wizard to install the corresponding device driver will start up. Follow the instructions of the wizard, and a dialog box for specifying the setup information file (inf file) will appear. In this dialog box, specify the musbdrv.inf file present in or below the directory in which you installed the M3T-PD45M (e.g., c:\mtool\pdxx\drivers).

While you are installing, a message may be output indicating that the device driver proper musbdrv.sys cannot be found. Because musbdrv.sys is stored in the same directory as is the musbdrv.inf file, look into the directory and specify it.

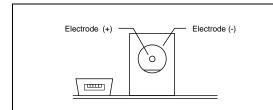


Connect the power supply for the emulator to the power connector (J1). The specification of the power supply for the emulator is listed in Table 2.1.

Table 2.1 Specification of power supply of the emulator

Power supply voltage DC $5.0 \text{ V} \pm 5\%/2 \text{ A}$

Figures 2.2 and 2.3 show the specifications of the power connector (J1) and an applicable plug, respectively.



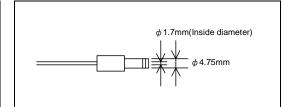
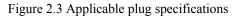


Figure 2.2 Power connector specifications



Notes on Connecting a Power Supply of the Emulator:

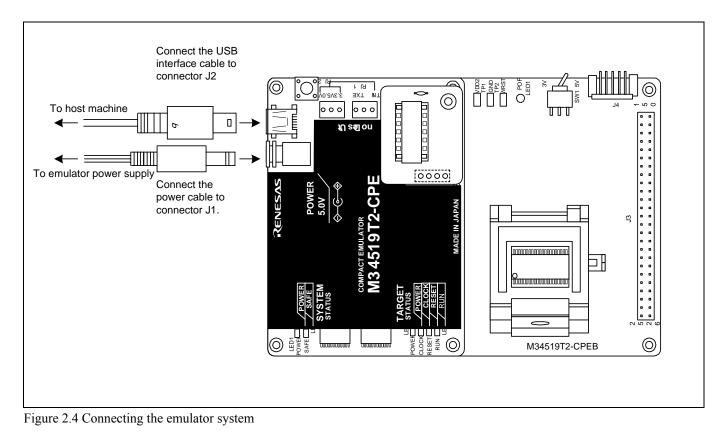
- The power cable included in this product package is colored red (+) and black (-).
- Be careful about the polarity of the power supply. Connecting to the wrong electrode could destroy internal circuits.
- Do not apply a voltage exceeding the specified voltage of the product (5.0 V ±5%), because it may cause burn injuries and the failure of internal circuits.



2.4 Connecting the Host Machine

Connect the emulator and the host machine with the USB interface cable.

Connect the USB interface cable (included) to the USB interface connector (J2) and the USB port of the host machine (see Figure 2.4).



2.5.1 MCU Power Supply Voltage Selection Switch

Set the MCU power supply source selection switch of the emulator according to conditions of use.

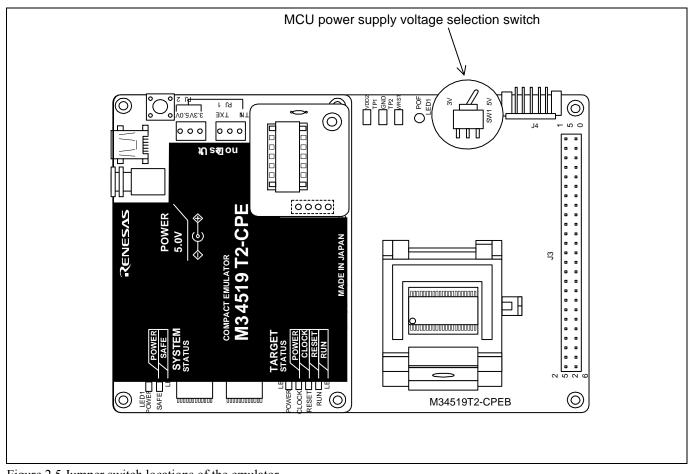


Figure 2.5 Jumper switch locations of the emulator

These are the jumper switches to select power supply to the MCU and its power voltage. As shown in Table 2.2 below, set the switch according to the connection to the user system.

Table 2.2 Setting jumper switches

| MCU power supply voltage selection switch (SW1) | Description | | |
|---|---|--|--|
| 3V | Supplied from the emulator. The MCU operating voltage is 3.0 V. | | |
| 5V | Supplied from the emulator. The MCU operating voltage is 5.0 V. | | |

Note on Jumper Switch Settings:



• Always shut OFF the emulator before changing the setting of the jumper switches, and connecting the cable. Otherwise the internal circuit may cause a break.

2.5.2 Checking Connections of the Emulator System

Before turning the power ON, check the connection of the interface cable to the host machine, emulator, and user system.

2.5.3 Turning ON/OFF the Power

Turn ON/OFF the power of the emulator and user system as simultaneously as possible.

Do not leave either the emulator or user system powered on, because of leakage current the internal circuits may be damaged. When turning ON the power again after shutting OFF the power, wait for about 10 seconds.

2.5.4 Power Supply to the User System

This emulator cannot supply the power to the user system. Therefore design your system so that the user system is powered separately.

The voltage of the user system should be within the range of $3.0 \text{ V}\pm 5\%$ or $5.0 \text{ V}\pm 5\%$. Do not change the voltage of the user system after turning on the power.



2.5.5 LED Display When the Emulator Starts Up Normally

After the emulator starts up, check the status of the LEDs to see whether the emulator operation is enabled or not. Figure 2.6 shows the positions of the emulator status LEDs.

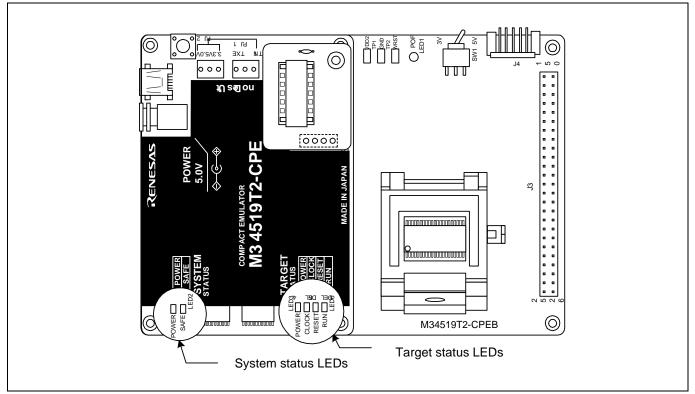


Figure 2.6 Positions of the system status LEDs and target status LEDs

(1) System status LEDs

Check that the LED1 and LED2 of the system status LEDs are lit immediately after the power is activated. If it is not lit, shut off the emulator and check the power supply for the emulator is properly connected.

(2) Target status LEDs

Target status LEDs light as shown in Figure 2.7 when the user system is not connected and as shown in Figure 2.8 when a user system is connected. When the self-check is terminated after turning the power on, only the LED2 (SAFE) lights on as shown in Figures 2.7 and 2.8

When the target status LEDs do not display as shown in Figures 2.7 and 2.8, refer to "5. Troubleshooting" (page 65).

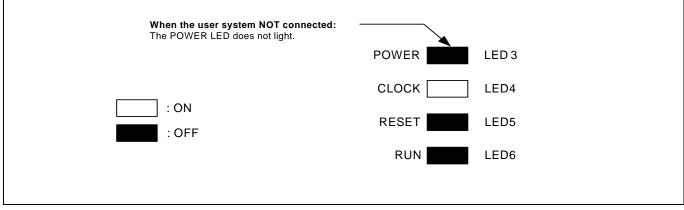
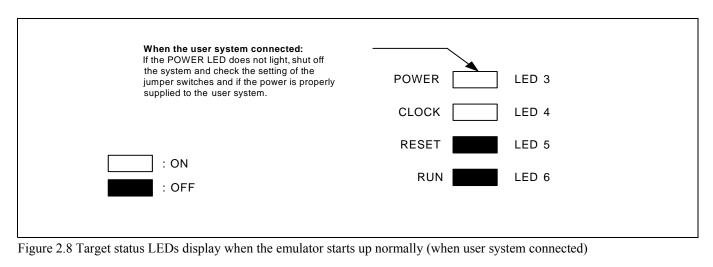


Figure 2.7 Target status LEDs display when the emulator starts up normally (when user system not connected)



IMPORTANT Note on the Target Status CLOCK LED: If the LED is not turned on, check the following. (1) After powering on the emulator (before starting up the emulator debugger): Make sure that the oscillator circuit board is properly installed in the emulator and it is oscillating normally.

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2.6 Downloading Firmware

2.6.1 When It is Necessary to Download Firmware

It is necessary to download the firmware when:

- (1) you use this product for the first time
- (2) the firmware has been upgraded
- (3) the emulator debugger M3T-PD45M has been upgraded
- If downloading firmware is not completed in the cases below, redownload the firmware.
- When the power is unexpectedly shut down during a download from the emulator debugger
- When a communications interface cable is unexpectedly pulled out

2.6.2 Downloading Firmware in Maintenance Mode

Download the firmware in maintenance mode as explained here following. The user system must not be connected when

downloading the firmware.

- (1) Connect the USB interface cable to the compact emulator and the host machine.
- (2) Within 2 seconds of activating power to the emulator, press the RESET switch on the compact emulator to switch to maintenance mode.

Switched to maintenance mode, the SAFE SYSTEM STATUS LED begins to flash.

(3) Start up the emulator debugger M3T-PD45M. When the Init dialog box setup is complete, the dialog which urges to download the firmware will appear. Download the firmware following messages. Required time for downloading the firmware is about 60 seconds.

IMPORTANT

Note on Downloading Firmware:

• Do not shut OFF power while the firmware is being downloaded. Doing so, the emulator will not start up properly. If power is shut OFF by mistake, redownload the firmware in maintenance mode.



2.7 Self-check

2.7.1 Self-check Procedure

To run the self-check of the emulator, do so as explained here below. While the self-check is in progress, the LEDs will change as shown in Figure 2.9.

- (1) If the user system is connected, disconnect it.
- (2) Set the switches as the factory-settings to execute the self-check (see Table 2.3).
- (3) Within 2 seconds of activating power to the emulator, press the system reset switch on the emulator upper panel.
- (4) Check the SAFE LED starts flashing and then press the system reset switch again.
- (5) The self-check will start. If the normal result is displayed in about 10 seconds, the self-check terminated normally.

Table 2.3 Switch settings for the self-check

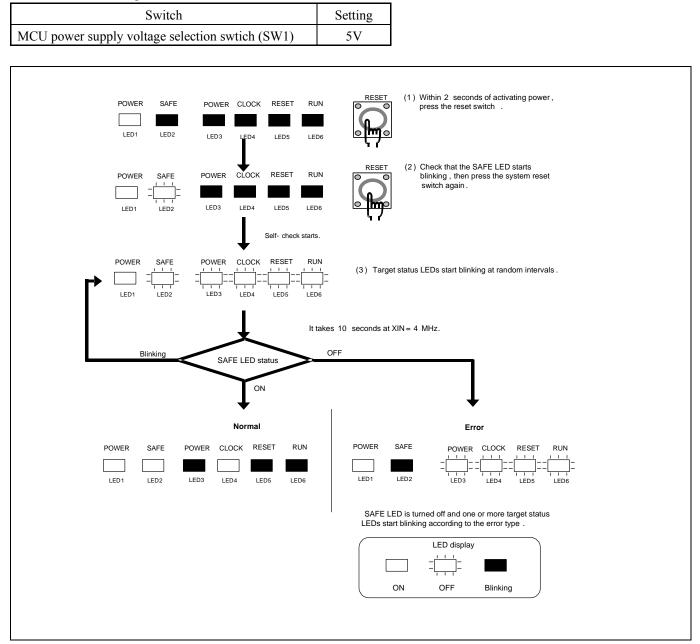


Figure 2.9 Self-check procedure

2.7.2 If an Error is Detected in the Self-check

Table 2.4 lists how to remedy the troubles if the target status LED display is abnormal in the self-check. When an error is detected, shut off the emulator and the user system and follow the steps in the Table 2.4.

| Table 2.4 Error display in the self-check and how to remedy it | it |
|--|----|
|--|----|

| LED display | | | Problem & Remedy | | |
|-----------------|-------|-------|------------------|---|--|
| POWER | CLOCK | RESET | RUN | | |
| | | | | The emulator system is not working properly. Check that power is supplied to the emulator. The emulator may be damaged. Contact your local distributor. | |
| | | | | | |
| | | | | A clock is not supplied to the emulator. Check that the oscillator circuit board (OSC-2) is attached. Check that the oscillator or oscillation module on an oscillator circuit board (OSC-2) is operating normally. | |
| | | | | The MCU is not controlled properly. Check that the MCU is mounted properly Check that the oscillation frequency of the oscillator circuit board (OSC-2) is within the MCU specifications. | |
| Others | | | | The emulator system is not working properly.The emulator may be damaged. Contact your local distributor. | |

IMPORTANT

Notes on the Self-check:

- Be sure to disconnect the user system before executing the self-check.
 - Use the preinstalled oscillator circuit board OSC-2 (6 MHz) to execute the self-check.
- If the self-check does not result normally (excluding target status errors), the emulator may be damaged. Then, contact your local distributor.

Figure 2.10 shows the connection of the M34519T2-CPE and the user system.

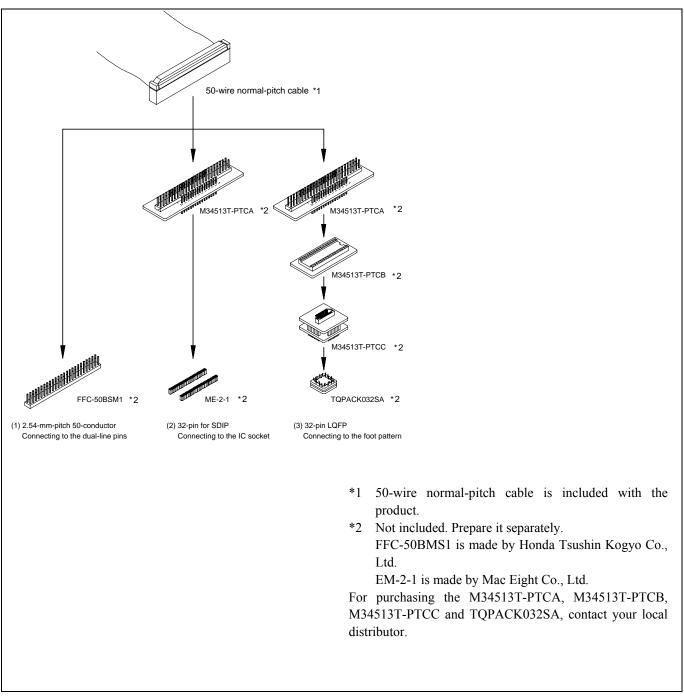


Figure 2.10 Connection of the M34519T2-CPE and user system

Note on Connecting the User System:

• Take care not to attach the converter board in a wrong direction. It may cause a fatal damage to the emulator.

2.8.1 Connecting to a 2.54-mm-pitch 50-conductor Dual-line Pins

Here following is a procedure of connecting 2.54-mm-pitch 50-conductor dual-line pins using a 50-wire normal-pitch cable (included with the M34519T2-CPE). Table 2.5 list the connector assignment of the 50-wire normal-pitch cable and Figure 2.11 show the connection of the 2.54-mm-pitch 50-conductor.

- (1) Mount the 50-conductor dual-line pins to the user system.
- (2) Attach the 50-wire normal-pitch cable (included) to the J3 connector of the M34519T2-CPE.
- (3) Attach the 50-conductor dual-line pins on the user system to the backside of the 50-wire normal-pitch cable.

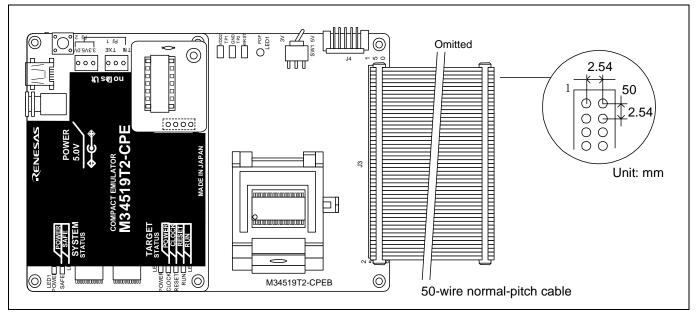


Figure 2.11 Connecting to a 50-pin 2.54-mm-pitch user system

Notes on Connecting the User System:

• Take care not to attach a converter board in a wrong direction. It may cause a fatal damage to the emulator and user system.

| Connector pin No. | MCU pin No. | Signal | Connector pin No. | MCU pin No. | Signal |
|----------------------|-------------|---------------------------|----------------------|-------------|------------|
| 1 | 1 | P13 | 50 | 42 | P12 |
| 2 | 2 | D0 | 49 | 41 | P11 |
| 3 | 3 | D1 | 48 | 40 | P10 |
| 4 | 4 | D2 | 47 | 39 | P03 |
| 5 | 5 | D3 | 46 | 38 | P02 |
| 6 | 6 | D4 | 45 | 37 | P01 |
| 7 | 7 | D5 | 44 | 36 | P00 |
| 8 | 8 | D6/CNTR0 | 43 | 35 | P43/AIN7 |
| 9 | 9 | D7/CNTR1 | 42 | 34 | P42/AIN6 |
| 10 | 10 | P50 | 41 | 33 | P41/AIN5 |
| 11 | 11 | P51 | 40 | 32 | P40/AIN4 |
| 12 | 12 | P52 | 39 | 31 | P63/AIN3 |
| 13 | 13 | P53 | 38 | 30 | P62/AIN2 |
| 14 | 14 | P20/SCK | 37 | 29 | P61/AIN1 |
| 15 | 15 | P21/SOUT | 36 | 28 | P60/AIN0 |
| 16 | 16 | P22/SIN | 35 | 27 | P33 |
| 17 | 17 | RESET* | 34 | 26 | P32 |
| 18 | 18 | NC(CNVSS ^{**2}) | 33 | 25 | P31/INT1 |
| 19 | 19 | NC(XOUT ^{**2}) | 32 | 24 | P30/INT0 |
| 20 | 20 | NC(XIN ^{**2}) | 31 | 23 | VDCE |
| 21 | 21 | VSS | 30 | 22 | VDD^{*1} |
| 22 | — | NC | 29 | _ | NC |
| 23 | | NC | 28 | _ | NC |
| 24 | | NC | 27 | _ | NC |
| 25 | _ | NC | 26 | _ | NC |

Table 2.5 Connector assignments of the 50-wire normal-pitch cable

*1 VDD is connected for the emulator system to monitor power supply of the target, and the emulator system does not supply power to the user system

*2 XIN, XOUT and CNVSS is not connected. XIN is input from oscillator board OSC-2 to the MCU, and it is not input from an oscillator circuit on the user system.

2.8.2 Connecting to an IC socket for a 32-pin SDIP on the User System

Here following is a procedure of connecting to an IC socket for a 32-pin 1.778-mm-pitch on the user system using the M34513T-PTCA (not included). Table 2.6 lists the connector assignments of pitch converter board M34513T-PTCA. When attaching the pitch converter board, check the No.1 pin positions of the cable and the connector.

- (1) Mount the 32-pin SDIP connector to the user system.
- (2) Attach the 50-wire normal-pitch cable (included) to the J3 of the M34519T2-CPE.
- (3) Attach the backside of the 50-wire normal-pitch cable to the M34513T-PTCA.
- (4) Attach the M34513T-PTCA to the 32-pin SDIP socket on the user system.

| Connector pin No. | MCU pin No. | Signal | Connector pin No. | MCU pin No. | Signal |
|----------------------|----------------|----------------------|----------------------|----------------|--------------------|
| 1 | 1 | D0 | 32 | 32 | P13 |
| 2 | 2 | D1 | 31 | 31 | P12 |
| 3 | 3 | D2 | 30 | 30 | P11 |
| 4 | 4 | D3 | 29 | 29 | P10 |
| 5 | 5 | D4 | 28 | 28 | P03 |
| 6 | 6 | D5 | 27 | 27 | P02 |
| 7 | 7 | D6/CNTR0 | 26 | 26 | P01 |
| 8 | 8 | D7/CNTR1 | 25 | 25 | P00 |
| 9 | 9 | P20/SCK | 24 | 24 | P63/AIN3 |
| 10 | 10 | P21/SOUT | 23 | 23 | P62/AIN2 |
| 11 | 11 | P22/SIN | 22 | 22 | P61/AIN1 |
| 12 | 12 | RESET* | 21 | 21 | P60/AIN0 |
| 13 | 13 | CNVSS ^{**2} | 20 | 20 | P31/INT1 |
| 14 | 14 | XOUT ^{**2} | 19 | 19 | P30/INT0 |
| 15 | 15 | XIN ^{**2} | 18 | 18 | VDCE |
| 16 | 16 | VSS | 17 | 17 | VDD ^{**1} |

Table 2.6 Connector assignments of the M34513T-PTCA

*1 VDD is connected for the emulator system to monitor power supply of the target, and the emulator system does not supply power to the user system

*2 XIN, XOUT and CNVSS are not connected. XIN is input from oscillator board OSC-2 to the MCU, and it is not input from an oscillator circuit on the user system.

Notes on Connecting the User System:



• Take care not to attach a converter board in a wrong direction. It may cause a fatal damage to the emulator and user system.

2.8.3 Connecting to a 32-pin LQFP foot pattern on the User System

Here following is a procedure of connecting to a 32-pin LQFP foot pattern on the user system using the M34513T-PTCA (not included), M34513T-PTCB (not included) and M34513T-PTCC (not included). Table 2.7 lists the connector assignments of pitch converter board TQPACK032SA. When attaching the pitch converter board, check the No.1 pin positions of the cable and the connector.

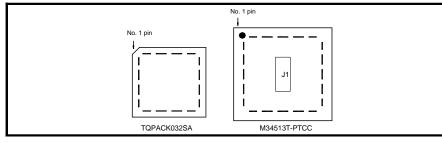
- (1) Mount the TQPACK032SA (included with the M34513T-PTCC) to the user system.
- (2) Attach the 50-wire normal-pitch cable (included) to the J3 of the M34519T2-CPE.
- (3) Attach the backside of the 50-wire normal-pitch cable to the M34513T-PTCA.
- (4) Attach the M34513T-PTCB to the M34513T-PTCA.
- (5) Attach the M34513T-PTCC to the M34513T-PTCB.
- (6) Attach the TQPACK032SA on the user system to the M34513T-PTCC (see Figure 2.12).

| Connector pin No. | MCU pin No. | Signal | Connector pin No. | MCU pin No. | Signal |
|-------------------|-------------|----------------------|-------------------|-------------|----------|
| 1 | 1 | D3 | 32 | 32 | D2 |
| 2 | 2 | D4 | 31 | 31 | D1 |
| 3 | 3 | D5 | 30 | 30 | D0 |
| 4 | 4 | D6/CNTR0 | 29 | 29 | P13 |
| 5 | 5 | D7/CNTR1 | 28 | 28 | P12 |
| 6 | 6 | P20/SCK | 27 | 27 | P11 |
| 7 | 7 | P21/SOUT | 26 | 26 | P10 |
| 8 | 8 | P22/SIN | 25 | 25 | P03 |
| 9 | 9 | RESET* | 24 | 24 | P02 |
| 10 | 10 | CNVSS ^{**2} | 23 | 23 | P01 |
| 11 | 11 | XOUT ^{**2} | 22 | 22 | P00 |
| 12 | 12 | XIN ^{**2} | 21 | 21 | P63/AIN3 |
| 13 | 13 | VSS | 20 | 20 | P62/AIN2 |
| 14 | 14 | VDD ^{**1} | 19 | 19 | P61/AIN1 |
| 15 | 15 | VDCE | 18 | 18 | P60/AIN0 |
| 16 | 16 | P30/INT0 | 17 | 17 | P31/INT1 |

Table 2.7 Connector assignments of the TQPACK032SA

*1 VDD is connected for the emulator system to monitor power supply of the target, and the emulator system does not supply power to the user system

*2 XIN, XOUT and CNVSS is not connected. XIN is input from oscillator board OSC-2 to the MCU, and it is not input from an oscillator circuit on the user system.



* Attach the M34513T-PTCC to the TQPACK032SA matching the No. 1 pin position (see Figure 2.12).

Figure 2.12 External view of the TQPACK032SA and M34513T-PTC

Notes on Connecting the User System:

• Take care not to attach a converter board in a wrong direction. It may cause a fatal damage to the emulator and user system.



2.9 Changing Settings

2.9.1 Changing the Power Supply Voltage to the MCU

These are the switch to select power supply to the MCU and its power voltage. As shown in Table 2.8 below, set the switch according to the connection to the user system.

Table 2.8 Setting switch

| MCU power supply voltage selection switch (SW1) | Description |
|---|---|
| 3 V | Supplied from the emulator. The MCU operating voltage is 3 V. |
| 5 V | Supplied from the emulator. The MCU operating voltage is 5 V. |

2.9.2 Replacing the Evaluation MCU

For debugging the 4583 and 4584 Group MCUs, it is necessary to replace the evaluation MCU to the included M34584MD-001FP. When replacing the MCU, match the No. 1 pin of the IC socket and that of the MCU as shown in Figure 2.13. Incorrect insertion may cause a fatal damage to the MCU.

The M34519M8-001FP is mounted when shipped from the factory.

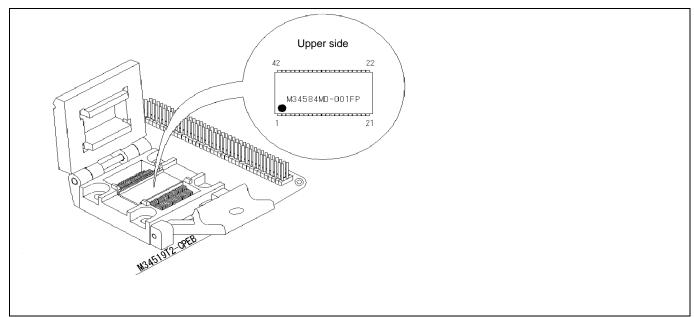


Figure 2.13 Replacing the evaluation MCU

Note on Setting Switches:

• Always shut OFF the emulator before changing the setting of the switch, and connecting the cable. Otherwise the internal circuit may cause a break.

Note on Replacing the MCU:

- .Before replacing the MCU, be sure to shut off the power supply.
- When mounting the evaluation MCU, be sure to align the No. 1 pin. Incorrect insertion may cause a fatal damage to the MCU.
- When opening and closing the IC socket, hold the adapter horizontally. Otherwise the inside of the IC socket may become damaged and cause an electrical insulation failure.

2.9.3 Selecting Clock Supply

This product always uses the internal oscillator circuit as a clock supply to the evaluation MCU.

1. Kinds of Oscillator Boards

The M34519T2-CPE comes with an oscillator circuit board OSC-2 (6 MHz). And an oscillator circuit bare board OSC-2 is included with this product. A clock supplied to an MCU can be changed by replacing oscillator circuit boards. 2. Replacing Oscillator Circuit Boards

Figure 2.14 shows how to replace the oscillator circuit boards.

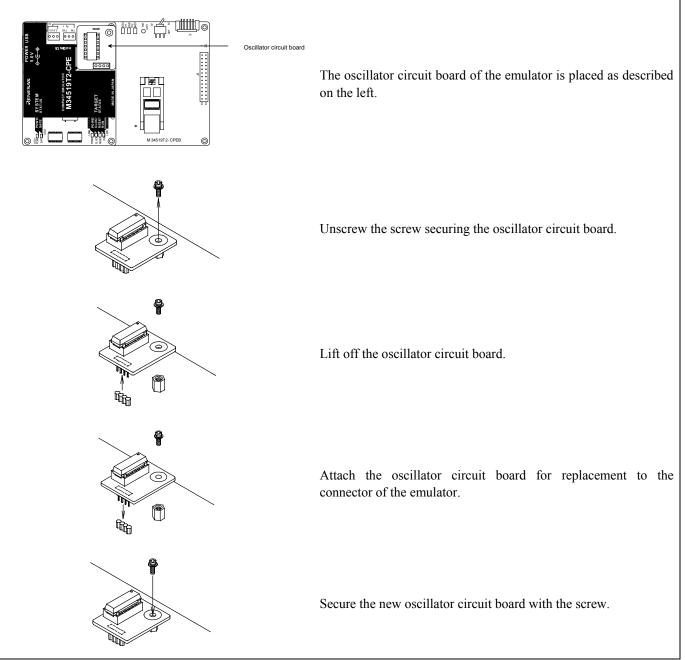


Figure 2.14 Replacing oscillator circuit boards

Note on Replacing the Oscillator Circuit Board:

• When removing the upper cover or replacing the oscillator circuit boards, be sure to shut OFF the power supply. Otherwise the internal circuit may cause a break.



3. Using the Internal Oscillator Circuit Bare Board

To use this product at a frequency you like, build a desired oscillator circuit on the included OSC-2 oscillator circuit bare board. Figure 2.15 shows an external view of the OSC-2 oscillator circuit bare board and the connector pin locations. Figure 2.16 shows the circuitry of the oscillator circuit bare board OSC-2. Use the number of oscillator circuits recommended by the oscillator manufacturer.

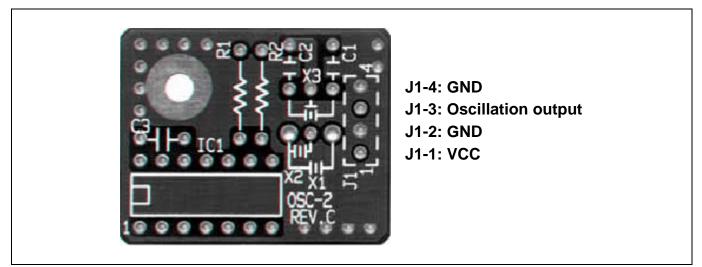


Figure 2.15 External view of the oscillator circuit board OSC-2 and its connector pin locations

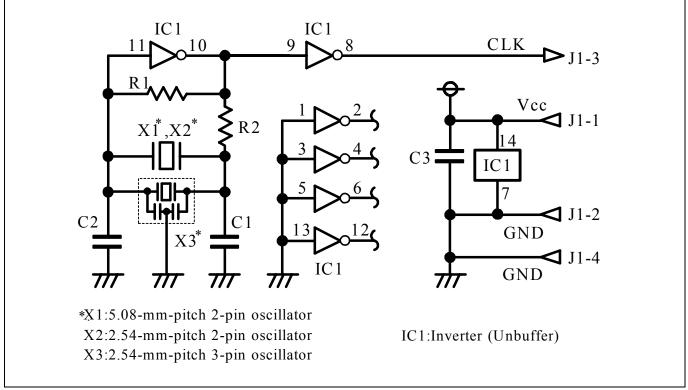


Figure 2.16 Circuits of the oscillator circuit bare board OSC-2

RENESAS

2.10 Connecting the External Trace/Trigger Cable

Using the external trace/trigger cable enables record/reference a hardware break by the external trigger, and changes of an external signal level in the trace window

2.10.1 Connecting the External Trace/Trigger Cable to the Emulator System

Connect the external trace/trigger cable to the connector J4 of the emulator

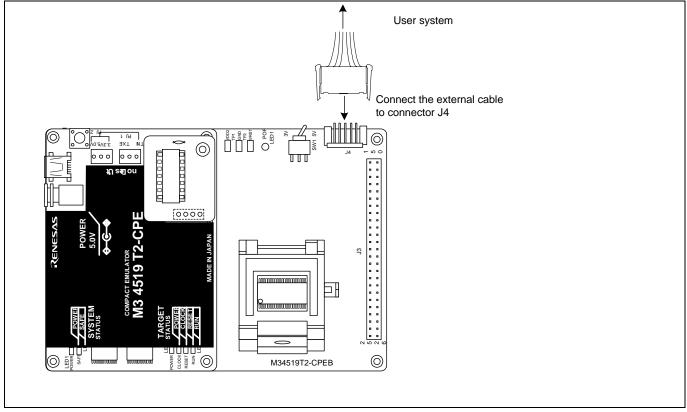


Figure 2.17 Connecting the external trace/trigger cable

2.10.2 Connecting the External Trace/Trigger Cable to the User System

Connect the GND, TRG and EXT0 to EXT3 of the external trace cable to the target system. Figure 2.18 shows the pin assignment of the external trace cable.

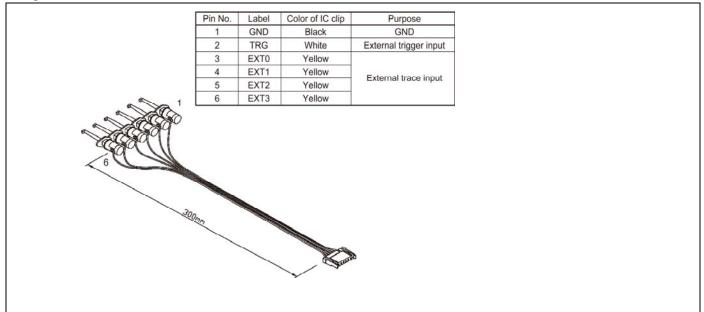


Figure 2.18 Pin assignment of the external trace cable



2.10.3 Specifications of the External Trace/Trigger Cable

Voltage input characteristics of external trace input and external trigger input are as follows.

| Item | Symbol | Min. | Max. |
|-------------------------|-------------------|------|------|
| Input voltage | V_{IN} | 0V | 5.5V |
| "H" level input voltage | V_{IH} | 2.0V | - |
| "L" level input voltage | V_{IL} | - | 0.8V |

Table 2.9 Input characteristics of the external trace cable

External trace input is latched in the timing shown in Figure 2.19, and external trigger input is latched in the timing shown in Figure 2.20.

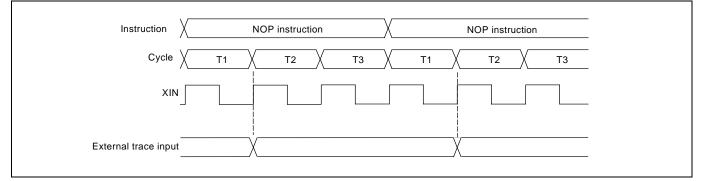


Figure 2.19 Latch timing of external trace input

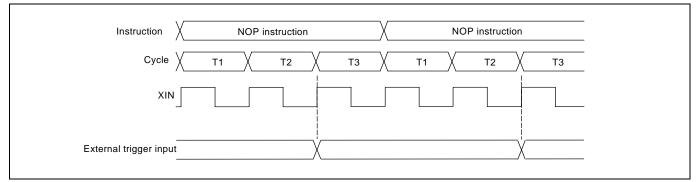


Figure 2.20 Latch timing of external trace input



2.11 Watchdog Timer Initialization Cycle Check Pin

The watchdog time cannot be used with this emulator system. The watchdog timer initialization cycle can be verified by observing the waveform at the check pin (WRST) of the emulator.

2.11.1 Check pin WRST (TP3) on the Emulator Main Unit

Figure 2.21 shows the positions of the check pins WRST (TP3) and GND (TP2).

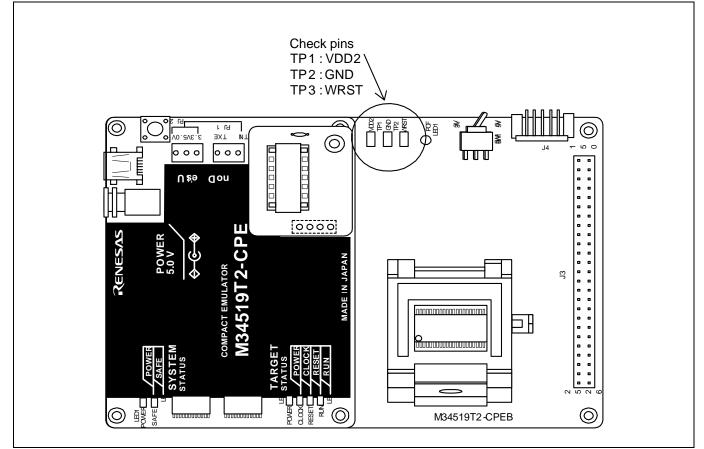


Figure 2.21 Positions of the check pins

2.11.2 Output Waveform of the Check Pin WRST

A waveform similar to the one shown in Figure 2.22 is output when executing the SRST instruction hat initializes the watchdog timer. By observing a period in which the check pin (WRST) is goes high, it is possible to know when the watchdog timer is initialized.

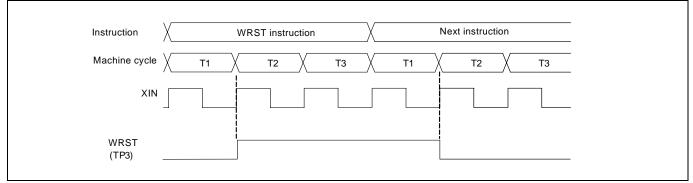


Figure 2.22 Output waveform of the check pin WRST

3. Usage (How to Use the Emulator Debugger)

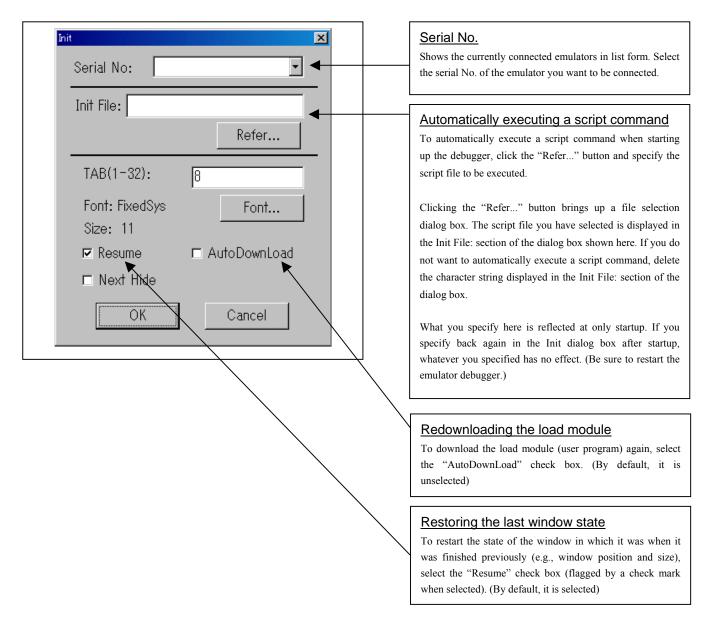
This chapter describes how to start up the emulator debugger and how to use the major windows.

3.1 Starting Up the Emulator Debugger (Init Dialog Box)

To launch the emulator debugger, click the Start menu of Windows and then select

Programs (P) >> [Renesas] >> [PD45M V.xx.xx Release x] >> [PD45M]. When the emulator debugger started up, the Init dialog box appears.

(1) Setting the Init dialog box





(2) Setting the Init dialog box (2/2)

By pressing [OK] after setting the Init dialog box (1/2), the following Init dialog box will be displayed.

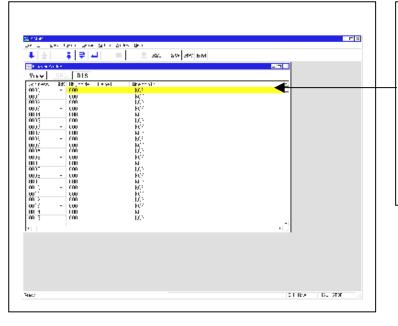
| nit 🛛 🔀 MCU: <u>M34519M6</u> | Specifying the MCU file Specify the MCU to be debugged. |
|---|---|
| M34518M6 Init File: M34518M8 M34519M6 M34519M8 M34583MD M34584MD | |
| TAB(1-32): Font: System Font Size: 14 | |
| Resume AutoDownLoad Next Hide | |
| OK Cancel | |



3.2 Program Window

(1) Downloading a program

1. Initial screen of the program window



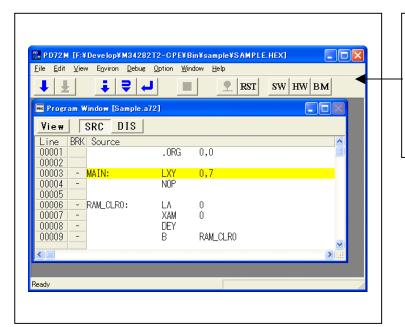
Initial screen of the program window

The program window is a window that always shows the content of the source file corresponding to the current position of the program counter. It automatically opens when the emulator starts up. The program counter position is identified by the yellow background color. Here, you can execute the program up to the cursor position, and set or clear software breakpoints.

With the emulator system, the ROM area at power-on in initialized to 000h (NOP instruction).

2. Downloading the program

| Menu | Menu item | Function |
|--------------|---|---|
| <u>F</u> ile | <u>D</u> ownload <u>L</u> oad Module <u>M</u> emory Image <u>S</u> ymbol | Downloads the user program. Downloads machine language data and debug information. Downloads only machine language data. Downloads only debug information. |
| | <u>R</u> eload | Reloads the user program. |
| | <u>U</u> pload | Uploads the user program. |
| | <u>S</u> ave Disasm | Saves the disassembled result. |



Display after downloading the program

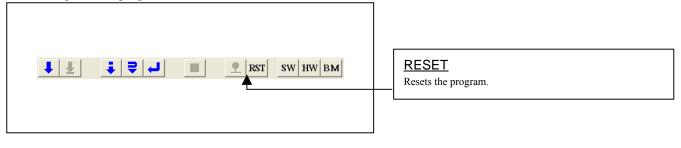
The program window has the following two display modes.

- Source display mode Displays the source file of the user program.
- Disassemble display mode Displays the disassembled result of the user program.

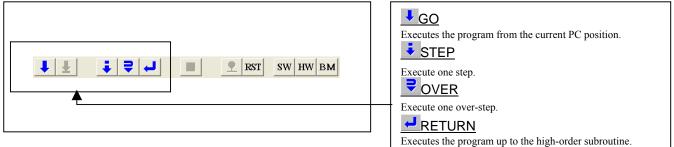


(2) Executing the program

1. Resetting the user program



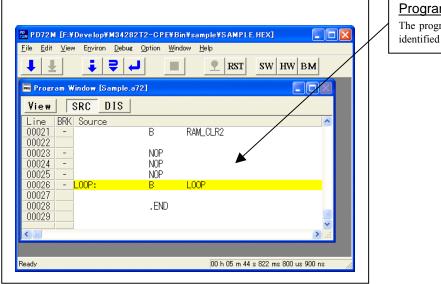
2. Executing the user program



3. Stopping the user program

| Stops the program. |
|--------------------|
| |

4. Program window screen after the user program has stopped



Program window screen

The program position at which the program has stopped is identified by the yellow background color.

(3) Setting breakpoints

1. Screen after breakpoint setup

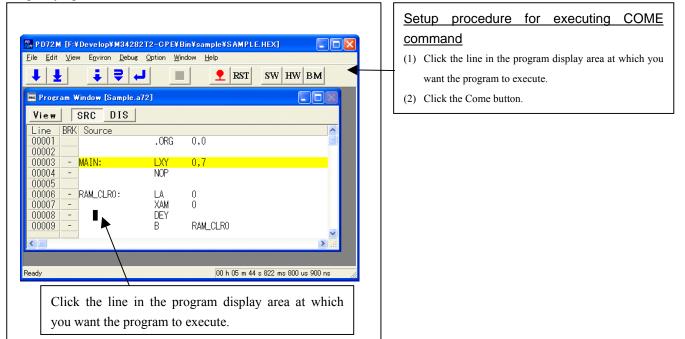
| | | 200 WA 100 WATER | |
|------------|---------------------------|----------------------|---------|
| AF \$1. 14 | .0K 0.1 | Break mode select bu | tton |
| W.C | <u>0.3</u> NU | | |
| E Franker | | | |
| | (3)¥ 0 302 1.5910 € | | |
| Ŀ | 37 LANCES | | |
| 4 | | 2- | |
| | | Break mode displa | iy area |
| Breakpo | int display area | | 10 |
| | | | ~ |
| | 11.1.5 | anterana (en s | v Ju - |

| Breakpoint setup screen |
|--|
| There are two types of breakpoints as described below. It is |
| necessary to select the breakpoint by the break mode button. |
| The current breakpoint is displayed in the break mode |
| display area. |
| BM:SW Software break mode |
| BM:HW Hardware break mode |
| |
| - Software breakpoint (B) |
| A software breakpoint can be set or cleared by double- |
| clicking the breakpoint display area. |
| If the breakpoint you set is a software breakpoint, the |
| program stops before executing the instruction at the set |
| breakpoint. Up to eight breakpoints can be set. |
| - Hardware breakpoint |
| A hardware breakpoint can be set or cleared by the |
| hardware break point setting dialog box. |
| If the breakpoint you set is a hardware breakpoint, the |
| program stops after executing the instruction at the set |
| breakpoint. Up to two breakpoints can be set, and |
| conditions of an address range or external trigger, etc., |
| |

can be also set.



- (4) Executing up to the cursor position (Come command)
- 1. Specifying the Come command



2. After the Come command has finished

| ↓ ± | ÷ | ₽ | | | P RST | SW HW | вм |
|-----------|------------------|----------------|------|-----|--------------|-------|----|
| 🛤 Program | Step Window L |] Sample.a7 | 2] | | | | |
| Vie w | SRC | DIS | | | | | |
| Line B | RK Sourc | e | | | | | ~ |
| 00001 | | | .ORG | 0,0 | | | |
| 00002 | | | | | | | |
| 00003 | - MAIN: | | LXY | 0,7 | | | |
| 00004 | - | | NOP | | | | |
| 00005 | | | | | | | |
| 00006 | - RAM_CL | R0: | LA | 0 | | | |
| 00007 | - | | XAM | 0 | | | |
| 00008 | - | | DEY | | | | |
| 00009 | - | | В | RAM | CLRO | | |
| | | | | | | | × |
| < | | | | | | | > |



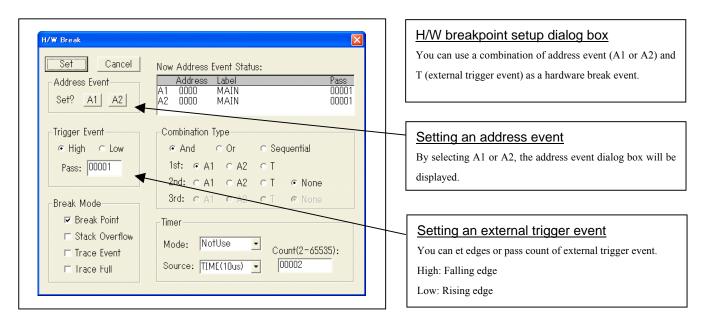
3.3 Hardware Breakpoint Setting Window

(1) Breakpoint setup dialog box

1. Opening the hardware breakpoint setup dialog box



2. Hardware Break Point Setting Window in initial state



3. Address event setting dialog box

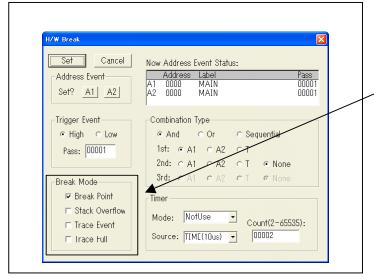
| Break Point A1 | Setting an address event You can select one of three conditions, and you can also set a pass count. Point: Specified address |
|--|--|
| Select Type: Point Range In Range Out Point: 0000 Pass: 00001 OK Cancel | Range In: Specified address range Range Out: Outside of specified address range |

(2) Setting the combinatorial event condition

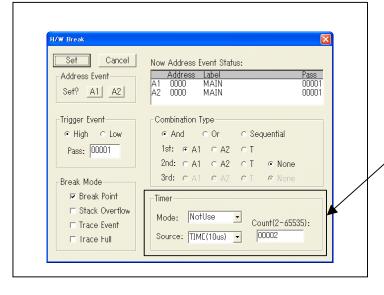
1. Window for setting the combinatorial event condition

H/W Break Set Cancel Now Address Event Status: Address Label Address Event Pass 00001 Set? A1 A2 0000 MAIN 00001 Α2 Trigger Event Combination Type 🖲 High 🛛 🤉 Low • And • Or Sequential Pass: 00001 1st: • A1 • A2 • T 2nd: © A1 © A2 © T None 3rd: C A1 C A2 OT C No Break Mode 🔽 Break Point Timer E Stack Overflow Mode: NotUse • Count(2-65535): Trace Event Source: TIME(10us) 00002 -🗆 Irace Full

2. Setting a break event



3. Timer setup area



Setting the combinatorial event condition

Select a combinatorial condition for A1, A2, and T. One of the following three combinatorial conditions can be selected..

- AND
 - All of the specified conditions are met.
- OR
 - One of the specified conditions is met.
- Sequential

The specified conditions are met sequentially in a specified order.

When you have finished setting the combinatorial event condition, click the "Set" button.

Setting a break event The following four break conditions can be set. You can set two or more break conditions at the same time. Break Point: The program breaks when a breakpoint is reached. Stack Overflow: The program breaks when the stack overflow or underflows. Trace Event: The program breaks when a trace event is met Trace Full: The program breaks when it finished writing to the trace memory. Timer setup area The following four operation modes of the timer can be specified. NotUse: The timer is not used. TimeOut: The program breaks when a breakpoint is reached within a specified time. TimeCount: The program breaks when a specified time has elapsed after it started running. DelayCount: The program breaks when a specified time has elapsed after a breakpoint is reached. One of the following two can be specified as the count source for the timer. TIME (10us): The passage of time if counted using the emulator's timer(10us) CYCLE: The passage time is counted by using MCU

cycle

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3.4 Trace Window

(1) Trace window

1. Trace window

| Menu | Menu item | Function |
|-----------------|--------------|-------------------------|
| OptionalWindows | Trace Window | Opens the trace window. |

| BUS | DIS | SRC | For | ward B | ack | Step | | Go | те | | |
|--------|----------|------|--------|--------|-------|--------|------|----|----------|------|---|
| Range: | -32767 . | 0000 | Area: | Break | File: | Cycle: | -002 | 26 | Address: | 05F7 | |
| Cycle | Label | | Addres | | Areg | Skip | Int | So | 0123 | | |
| -0026 | | | 05F7 | 0EB | F | 0 | 0 | 0 | 0000 | | |
| -0025 | | | 05F8 | 276 | F | 1 | 0 | 0 | 0000 | | |
| -0024 | W1 | | 05F6 | 281 | F | 0 | 0 | 0 | 0000 | | |
| -0023 | | | 05F7 | 0EB | F | 0 | 0 | 0 | 0000 | | |
| -0022 | | | 05F8 | 276 | F | 1 | 0 | 0 | 0000 | | |
| -0021 | W1 | | 05F6 | 281 | F | 0 | 0 | 0 | 0000 | | |
| -0020 | | | 05F7 | 0EB | F | 0 | 0 | 0 | 0000 | | |
| -0019 | | | 05F8 | 276 | F | 1 | 0. | Ĵ. | 0000 | | |
| -0018 | W1 | | 05F6 | 281 | F | 0 | 0 | 0 | 0000 | | |
| -0017 | | | 05F7 | 0EB | F | 0 | 0 | 0 | 0000 | | |
| -0016 | | | 05F8 | 276 | F | 1 | 0 | 0 | 0000 | | |
| -0015 | W1 | | 05F6 | 281 | F | 0 | 0 | 0 | 0000 | | |
| -0014 | | | 05F7 | 0EB | F | 0 | 0 | 0 | 0000 | | |
| -0013 | | | 05F8 | 276 | F | 1 | 0 | 0 | 0000 | | |
| -0012 | W1 | | 05F6 | 281 | F | 0 | 0 | 0 | 0000 | | |
| -0011 | | | 05F7 | 0EB | F | 0 | 0 | 0 | 0000 | | |
| -0010 | | | 05F8 | 276 | F | 1 | 0 | 0 | 0000 | | |
| -0009 | W1 | | 05F6 | 281 | F | Û | 0 | 0 | 0000 | | |
| -0008 | | | 05F7 | 0EB | F | 0 | 0 | 0 | 0000 | | - |

Trace window

The trace window is used to show the results of real-time trace measurements. It has the following three display modes:

- Bus mode

_

Bus information per cycle can be inspected. The contents are displayed in order of execution paths. Disassemble mode

- The execution paths of the executed instructions can be inspected. The contents are displayed in order of execution paths.
- Source mode
- The execution paths of the source program can be inspected.
- These modes can be switched over using the respective toolbar buttons.

The trace window shows the measurement result when a real-time trace measurement has finished. The trace window remains blank until the real-time trace measurement in progress finishes.

2. Trace window (bus display)

| Rever -32767 0000 Area Break File Cycle -0028 Address 0057 -0028 05F7 0EB F 0 0 00000 -0028 05F7 0EB F 0 0 00000 -0023 05F7 0EB F 0 0 00000 -0022 05F8 276 F 1 0 0 00000 -0022 05F7 0EB F 0 0 00000 -0021 W1 05F8 276 F 1 0 00000 -0013 05F7 0EB F 0 0 00000 -0014 05F7 0EB F 0 0 00000 -0013 05F8 276 F 1 0 0 00000 -0014 05F7 0EB F 0 0 00000 0 0 00000 -0010 | BUS | /indow DIS | SRC | Forv | vard B | ack | Step | 1 | Çoj | пе | | |
|---|--|--|---------------------------|-------|---|--|--------------------------------|-------|-------|--------|------------|----------|
| Core le Label Address Data Areg Skip Int So 0123 -0026 05F7 0EB F 0 0 00000 -0022 05F8 276 F 1 0 0 00000 -0022 05F8 276 F 1 0 0 00000 -0021 W1 05F6 281 F 0 0 0 00000 -0021 W1 05F6 281 F 0 0 0 00000 -0012 W1 05F6 281 F 0 0 00000 -0013 00F7 0EB F 0 0 00000 00000 -0014 05F6 281 F 0 </th <th></th> <th></th> <th>-</th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th>05F7</th> <th></th> | | | - | | | | | | | | 05F7 | |
| 0026 05F7 0EB F 0 0 0000 0024 W1 05F8 276 F 1 0 0 0000 0022 05F7 0EB F 0 0 0 0000 0022 05F7 0EB F 0 0 0 0000 0022 05F7 0EB F 0 0 0 0000 0020 05F7 0EB F 0 0 0 0000 0018 W1 05F6 281 F 0 0 0 0000 0018 W1 05F6 281 F 0 0 0 0000 0013 05F8 276 F 1 0 0 0000 0010 05F8 276 F 0 0 0 0000 0011 05F7 0EB F 0 0 0 0000 0010 05F7 0EB F 0 0 0 0000 | | | | | | | | | | | | |
| 0025 05F8 276 F 1 0 0 0000 0023 05F7 0EB F 0 0 0000 0021 05F7 0EB F 0 0 0000 0022 05F7 0EB F 0 0 0000 0021 W1 05F6 281 F 0 0 0000 0013 05F7 0EB F 0 0 0000 0000 0016 05F7 0EB F 0 0 0000 0000 0014 05F7 0EB F 0 0 00000 0000 0012 W1 05F6 281 F 0 0 00000 0010 05F7 0EB F 0 0 00000 0000 0011 05F7 0EB F 0 0 0 00000 0010 05F7 0EB F 0 0 0 00000 0010 05F7 0EB | | | | | | F | | | | | | _ |
| 00024 W1 05F6 281 F 0 0 00000 00021 05F7 0EB F 0 0 00000 0022 05F7 0EB F 0 0 00000 0020 05F7 0EB F 0 0 0 00000 0010 05F7 0EB F 0 0 0 00000 0011 05F7 0EB F 0 0 0 00000 0011 05F7 0EB F 0 0 0 00000 0011 05F6 281 F 0 0 0 00000 0013 05F6 281 F 0 0 0 00000 0010 05F8 276 F 1 0 0 00000 0010 05F7 0EB F 0 0 0 00000 0000 05F7 0EB F 0 0 0 0 0 0 0 0 | | 1 | | | | F | | | | | | |
| 0022 05F7 0EB F 0 0 0000 0021 W1 05F8 276 F 1 0 0 0000 0018 05F8 276 F 1 0 0 0000 0018 W1 05F6 281 F 0 0 0000 0018 W1 05F6 281 F 0 0 0000 0017 05F7 0EB F 0 0 0000 0000 0014 05F7 0EB F 0 0 0000 0000 0012 W1 05F6 281 F 0 0 0000 0000 0012 W1 05F6 281 F 0 0 00000 00000 00000 00000 0000 | | W1 | | | | F | | | | | | |
| 0022 05F8 276 F 1 0 0000 0020 05F6 281 F 0 0 0000 0019 05F6 281 F 0 0 0000 0013 W1 05F6 281 F 0 0 0000 0017 05F7 0EB F 0 0 00000 0016 05F7 0EB F 0 0 00000 0013 05F7 0EB F 0 0 00000 0013 05F7 0EB F 0 0 00000 0013 05F7 0EB F 0 0 00000 0010 05F7 0EB F 0 0 00000 0001 05F7 0EB F 0 0 0 00000 0001 05F7 0EB F 0 0 0 00000 0000 05F7 0EB F 0 0 0 0 0 < | | | | | | F | | | | | | |
| ■0010 ₩1 05F8 276 F 1 0 0 0000 ■0008 ₩1 05F6 281 F 0 0 0 0000 ■0008 W1 05F7 0EB F 0 0 0 0000 ■0008 W1 05F7 0EB F 0 0 0000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come -0103 0000 0.77 MAIN: LYY 0.7 0.000 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 002 003 060 XAM 0 MAIN -0093 0002 080 RAM_CLR0: LA 0 MAIN -0093 00 | | ī. | | | | F | | | | | | |
| ■0010 ■0009 ₩1 05F8 05F6 281 281 F 0 0 0000 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Pance -0103 0000 0C7 MAIN: LYY 07 -0103 0000 0C7 MAIN: LYY 07 0101 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 | -0021 | W1 | | | | F | 0 | | | | | _ |
| ■0010 ■0009 ₩1 05F8 05F6 281 281 F 0 0 0000 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Pance -0103 0000 0C7 MAIN: LYY 07 -0103 0000 0C7 MAIN: LYY 07 0101 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 | | 1 | | | | F | 0 | 0 | 0 | 0000 | | |
| ■0010 ■0009 ₩1 05F8 05F6 281 281 F 0 0 0000 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Pance -0103 0000 0C7 MAIN: LYY 07 -0103 0000 0C7 MAIN: LYY 07 0101 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 | -0019 | | | 05F8 | 276 | F | 1 | 0 | 0 | 0000 | | |
| ■0010 ■0009 ₩1 05F8 05F6 281 281 F 0 0 0000 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Pance -0103 0000 0C7 MAIN: LYY 07 -0103 0000 0C7 MAIN: LYY 07 0101 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 | | W1 | | 05F6 | | F | 0 | | 0 | | | |
| ■0010 ■0009 ₩1 05F8 05F6 281 281 F 0 0 0000 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Pance -0103 0000 0C7 MAIN: LYY 07 -0103 0000 0C7 MAIN: LYY 07 0101 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 | -0017 | | | 05F7 | 0EB | F | 0 | 0 | 0 | 0000 | | |
| ■0010 ■0009 ₩1 05F8 05F6 281 281 F 0 0 0000 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Pance -0103 0000 0C7 MAIN: LYY 07 -0103 0000 0C7 MAIN: LYY 07 0101 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 | -0016 | | | 05F8 | 276 | F | 1 | 0 | 0 | 0000 | | |
| ■0010 ■0009 ₩1 05F8 05F6 281 281 F 0 0 0000 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Pance -0103 0000 0C7 MAIN: LYY 07 -0103 0000 0C7 MAIN: LYY 07 0101 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 | -0015 | W1 | | 05F6 | 281 | F | 0 | 0 | 0 | 0000 | | |
| ■0010 ■0009 ₩1 05F8 05F6 281 281 F 0 0 0000 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Pance -0103 0000 0C7 MAIN: LYY 07 -0103 0000 0C7 MAIN: LYY 07 0101 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 | | | | | | F | | | | | | |
| ■0010 ■0009 ₩1 05F8 05F6 281 281 F 0 0 0000 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Pance -0103 0000 0C7 MAIN: LYY 07 -0103 0000 0C7 MAIN: LYY 07 0101 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 | -0013 | | | | 276 | F | | | | 0000 | | |
| ■0010 ■0009 ₩1 05F8 05F6 281 281 F 0 0 0000 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 W1 05F7 0EB F 0 0 00000 ■0008 DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Pance -0103 0000 0C7 MAIN: LYY 07 -0103 0000 0C7 MAIN: LYY 07 0101 -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 | | W1 | | | | F | | | | | | |
| ■0000 WI 05F6 281 F 0 0 0 0000 ■0000 05F7 0EB F 0 0 0 0000 ■0000 Trace Window ■ | -0011 | | | | | F | 0 | | 0 | 0000 | | |
| ■0000 WI 05F6 281 F 0 0 0 0000 ■0000 05F7 0EB F 0 0 0 0000 ■0000 Trace Window ■ | | | | | 276 | F | | | 0 | | | |
| Image: Oisassemble Display (DIS) Irace Window Image: Otom BUS DIS SRC Forward Back Step Come Range: -0103 0000 Arace Break File Cycle Address: 0000 Cycle Address Obj -code Label Mmemonic Image: 0000 Address: 0000 -0102 0001 000 000 RAM_CLR0: LA 0 MAIN -0103 0004 017 DEY 0002(00/02) F -0096 0003 060 RAM_CLR0: LA 0 MAIN -0097 0002 0B0 RAM_CLR0: LA 0 MAIN -0096 0003 060 XAM 0 MAIN -0091 0004 017 DEY DIS Source Display (SRC) -0091 0004 017 DEY DIS Source Come -0091 0000 Area Break File Sample.a72 Cycle -0103 A | -0009 | W1 | | | 281 | F | | | 0 | | | |
| Disassemble Display (DIS) ■ Trace Window Image: Trace Window Image: Trace Window Image: Trace Window BUS DIS SRC Forward Back Step Come Ramee: -0103 .0000 Area Break File: Cycle -0103 Address: 0000 Cycle Address Obj -code Label Mmemonic Image: Memory Image: M | | | | 05F7 | 0EB | F | 0 | 0 | 0 | 0000 | | |
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| ■ Trace Window ■ If SRC Forward Back Step Come Bus DIS SRC Forward Back Step Come Panee -0103 0000 Area Break File Cycle -0103 Address 0000 -0103 0000 0C7 MAIN: LXY 07 -0103 0000 0C7 MAIN: LXY 07 -0100 0003 060 XAM 0 MAIN -0098 0004 017 DEY 0002(00/02) F -0097 0002 0B0 RAM_CLR0: LA 0 MAIN -0096 0003 060 XAM 0 MAIN 0 -0093 0002 0B0 RAM_CLR0: LA 0 MAIN -0094 0005 182 B 0002(00/02) F -0093 0002 0B0 RAM_CLR0: LA 0 -0091 < | | | D | isass | seml | ble D | ispla | iy (I | DIS | S) | | |
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| Cycle Address Obj-code Label Mnemonic -0103 0000 0C7 MAIN: LXY 07 -0102 0001 000 NOP -0101 0002 0B0 RAM_CLR0: LA 0 MAIN -0100 0003 060 XAM 0 MAIN -0098 0005 182 B 0002(00/02) F -0097 0002 0B0 RAM_CLR0: LA 0 MAIN -0096 0003 060 XAM 0 MAIN -0096 0003 060 XAM 0 MAIN -0094 0005 182 B 0002(00/02) F -0093 0002 080 RAM_CLR0: LA 0 MAIN -0091 0004 017 DEY 0002(00/02) F -0093 0002 080 RAM_CLR0: LA 0 MAIN -0090 | Range: | -0103 | 0000 | Area: | Break | File: | Cycle: | -0 | 103 | Addres | - ss: f | INNN |
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| -0099 0004 017 DEY -0098 0005 182 B 0002(00/02) F -0097 0002 0B0 RAM_CLR0: LA 0 MAIN -0095 0004 017 DEY -0093 0002 0B0 RAM_CLR0: LA 0 MAIN -0093 0002 0B0 RAM_CLR0: LA 0 MAIN -0092 0003 060 XAM 0 MAIN -0091 0004 017 DEY -0090 0005 182 B 0002(00/02) F -0090 0005 182 B 0002(00/02) F -0090 0005 182 C C C C C C C C C C C C C C C C C C C | | | | | | | | | | | | |
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| -0095 0004 017 -0094 0005 182 -0092 0003 060 -0091 0004 017 -0090 0005 182 B 0002(00/02) F XAM 0 MAIN -0091 0004 017 -0090 0005 182 B 0002(00/02) F COME BUS DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come BUS DIS SRC Forward Back Step Come Com | | | | | | TAN_OL | 110. | | | | | |
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| Source Display (SRC) BUS DIS SRC Forward Back Step Come Range: -0103 0000 Area Break File: Sample.a72 Cycle: -0103 Addre Line Now Source | -0 | | | | | | | | | 00007 | 00.7003 | <u>,</u> |
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| Trace Window Image: 1003 OUDD Area Broward Back Step Come BUS DIS SRC Forward Back Step Come Addre Range: -0103 0000 Area Break File: Sample.a72 Cycle: -0103 Addre 00001 .0RG 0,0 .0000 .0000 .0000 .00000 .00000 .000000 .000000 .000000 .0000000 .000000000000000000000000000000000000 | | | | | F | Vienle | | | | | | <u> </u> |
| BUS DIS SRC Forward Back Step Come Range: -0103 0000 Area: Break File: Sample.a72 Cycle: -0103 Addre 00001 .0RG 0,0 .0RG 0,0 00003 00003 .0RG 0,0 .0000 .00000 .00000 .00000 .00000 .000000 .000000 .00000 .000000 .000000 .000000 .0000000 .000000000000 .000000000000000000000000000000000000 | | | | 2 | | | | | ` | | | |
| Rance: -0103 0000 Area: Break File: Sample.a72 Cycle: -0103 Addre Line Now Source | < | | | Sour | ce L | Jispie | ay (S | RC |) | | | |
| Line Now Source 00001 .0RG 0,0 00002 | < III | Windo | w | | | | | 1 |) | Para | | |
| 00001 .0RG 0,0 00003 >> MAIN: LXY 0,7 00004 - NOP 00005 - .0RG 0,0 00006 - RAM_CLR0: LA 0 00007 - XAM 0 00008 - DEY 00010 00011 - LXY 1,7 00012 - RAM_CLR1: LA 0 00013 - XAM 0 00013 00014 - DEY | Trace BUS | e Windo | S SRC | Fo | orward | Back | St | .ep | | | | |
| 00003 >>> MAIN: LXY 0,7 00004 - NOP NOP 00005 - NOP - 00006 - RAM_CLRO: LA 0 00007 - XAM 0 - 00008 - DEY - - 00010 - B RAM_CLRO - 00011 - LXY 1,7 - 00012 - RAM_CLR1: LA 0 00013 - XAM 0 - 00014 - DEY - - | Contracts BUS Range: | • Windo DIS -0103 | S SRC . 0000 | Fo | Break | Back File: | St | .ep | | | | |
| 00004 - NOP 00005 - RAM_CLR0: LA 0 00007 - XAM 0 00008 - DEY 0 00009 - B RAM_CLR0 00010 - - U 00011 - LXY 1,7 00012 - RAM_CLR1: LA 0 00013 - XAM 0 00014 - DEY - | Trace BUS Range: Line 00001 | • Windo DIS -0103 | S SRC . 0000 | Fo | Break | Back File: | S ample | .ep | | | | |
| 00005 - RAM_CLR0: LA 0 00007 - XAM 0 00008 - DEY 0 000010 - B RAM_CLR0 00011 - LXY 1,7 00012 - RAM_CLR1: LA 0 00013 - XAM 0 0 00014 - DEY - - | Trace BUS Range: Line 00001 00002 | • Windo DIS -0103 Now | S SRC . 0000 Source | Fo | Break | Back File: G O, | Sample | .ep | | | | |
| 00005 - RAM_CLR0: LA 0 00007 - XAM 0 00008 - DEY 0 000010 - B RAM_CLR0 00011 - LXY 1,7 00012 - RAM_CLR1: LA 0 00013 - XAM 0 0 00014 - DEY - - | Trace DUS Range: Line 00001 00002 00003 | • Windo DIS -0103 Now | S SRC . 0000 Source | Fo | Break . ORC | Back File: G O, | Sample | .ep | | | | |
| 00007 - XAM 0 00008 - DEY 00009 - B RAM_CLR0 00011 - LXY 1,7 00012 - RAM_CLR1: LA 0 00013 - XAM 0 00013 00014 - DEY Image: State St | Trace BUS Range: Line 00001 00002 00003 00004 | • Windo DIS -0103 Now | S SRC . 0000 Source | Fo | Break . ORC | Back File: G O, | Sample | .ep | | | | |
| 00007 - XAM 0 00008 - DEY 00009 - B RAM_CLR0 00011 - LXY 1,7 00012 - RAM_CLR1: LA 0 00013 - XAM 0 00013 00014 - DEY Image: State St | Trace BUS Range: Line 00001 00002 00003 00004 | • Windo DIS -0103 Now | S SRC . 0000 Source | Fo | Break . ORC | Back File: G O, | Sample | .ep | | | | |
| 00008 - DEY 00009 - B RAM_CLR0 00010 - LXY 1,7 00012 - RAM_CLR1: LA 0 00013 - XAM 0 00014 - DEY - | Trace Trace BUS Range: Line 00001 00002 00003 00004 00005 | e Windo DIS -0103 Now | Source | Area: | Break Break . OR LXY NOP | Bjack File: G O, O, | Sample | .ep | | | | |
| 00009 - B RAM_CLR0 00010 - | Trace Trace BUS Range: Line 00001 00002 00003 00004 00005 00006 | e Windo DIS -0103 Now | Source | Area: | Break OR UXY NOP | Back File: G O, O, | Sample | .ep | | | | |
| 00010 00011 - LXY 1,7 00012 - RAM_CLR1: LA 0 00013 - XAM 0 00014 - DEY | Trace Trace BUS Range: Line 00001 00002 00003 00004 00005 00006 00007 00007 | e Windo DIS -0103 Now | Source | Area: | Break Break . ORO LXY NOP LA | Back File: G O, O, | Sample | .ep | | | | |
| 00011 - LXY 1,7 00012 - RAM_CLR1: LA 0 00013 - XAM 0 00014 - DEY - | Trace BUS Range: Line 00001 00002 00003 00004 00005 00007 00008 | Windo D I S -0103 Now | Source | Area: | Break Break . OR LXY NOP LA XAM DEY | Back File: G O, O, O | Sample | a72 | | | | |
| 00012 - RAM_CLR1: LA 0 00013 - XAM 0 00014 - DEY • | Trace Trace BUS Range: Line 00001 00002 00003 00004 00005 00006 00007 00008 00009 | Windo D I S -0103 Now | Source | Area: | Break Break . OR LXY NOP LA XAM DEY | Back File: G O, O, O | Sample | a72 | | | | |
| 00013 - XAM 0 00014 - DEY | Trace BUS Range: Line 00001 00002 00003 00006 00006 00007 00008 00009 000010 | Windo DIS -0103 Now | Source | Area: | Break Break . OR LXY NOP LA XAM DEY B | Back File: G 0, 0, 0 R/ | Sample 0,0 .7 AM_CLR0 | a72 | | | | |
| 00014 - DEY | Trace BUS Range: Line 00001 00002 00003 00004 00005 00006 00007 00008 00009 00011 00011 | Windo D I S -0103 Now - - | MAIN: | Area: | Break Break . OR LXY NOP LA XAM DEY B | Back File: G 0, 0 0 R/ | Sample 0,0 .7 AM_CLR0 | a72 | | | | |
| | Trace BUS Range: Line 00001 00002 00003 00004 00006 00006 00007 00008 00009 00010 00011 00012 | Windo DIS -0103 Now | MAIN: | Area: | Break Break . ORI LXY NOP LA XAM DEY B LXY LA | Back File: G 0, 0, 0 0 R/ 1, 0 | Sample 0,0 .7 AM_CLR0 | a72 | | | | |
| | Trace BUS Ranee: Line 00001 00002 00003 00004 00005 00006 00007 00008 00009 00010 00011 00012 00012 | Windo DIS -0103 Now -0103 -0 | MAIN: | Area: | Break . OR LXY NOP LA XAM DEY B LXY LA XAM | Back File: G 0, 0, 0 0 R/ 1, 0 | Sample 0,0 .7 AM_CLR0 | a72 | | | | |
| | Trace BUS Range: Line 00001 00002 00003 00004 00005 00006 00006 00007 00008 00009 00011 00012 00013 00014 | Windo DIS -0103 Now -0103 -0 | MAIN: | Area: | Break . OR LXY NOP LA XAM DEY B LXY LA XAM | Back File: G 0, 0, 0 0 R/ 1, 0 | Sample 0,0 .7 AM_CLR0 | a72 | | | | Addre |
| | Trace BUS Range: Line 00001 00002 00003 00004 00005 00006 00006 00006 00006 00000 00000 00001 00011 00011 00013 00013 | Windo DIS -0103 Now -0103 -0 | MAIN: | Area: | Break . OR LXY NOP LA XAM DEY B LXY LA XAM | Back File: G 0, 0, 0 0 R/ 1, 0 | Sample 0,0 .7 AM_CLR0 | a72 | | | | Addre |
| | Trace BUS Range: Line 00001 00002 00003 00004 00005 00006 00006 00006 00006 00000 00000 00001 00011 00011 00013 00013 | Windo DIS -0103 Now -0103 -0 | MAIN: | Area: | Break . OR LXY NOP LA XAM DEY B LXY LA XAM | Back File: G 0, 0, 0 0 R/ 1, 0 | Sample 0,0 .7 AM_CLR0 | a72 | | | | Addre |
| | Trace BUS Range: Line 00001 00002 00003 00004 00005 00006 00006 00006 00006 00000 00000 00001 00011 00011 00013 00013 | Windo DIS -0103 Now -0103 -0 | MAIN: | Area: | Break . OR LXY NOP LA XAM DEY B LXY LA XAM | Back File: G 0, 0, 0 0 R/ 1, 0 | Sample 0,0 .7 AM_CLR0 | a72 | | | | Addre |

Bus Display (BUS)

Explanation of the trace window (bus display) The following explains the displayed contents, from left to

right. - Address

- Shows the status of the address bus.
- Data
- Shows the status of the data bus. - Areg
 - Shows the status of the register A.
- Skip

-

- When marked by 1, it means a skipped instruction.
- Int

_

When marked by 1, it means an interrupt has occurred.

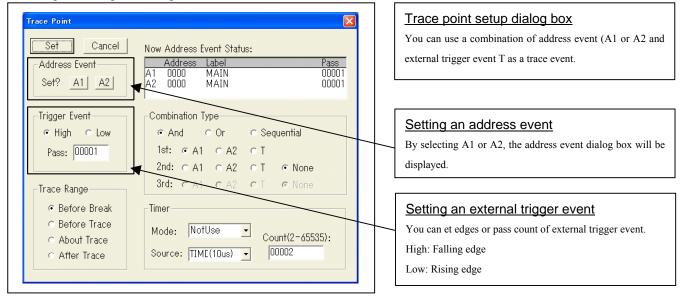
- So When marked by 1, it means that stack overflow or underflow has occurred.
- 0123
 - Shows the signal level of external trace cable of EXT0 to EXT3

(2) Trace point setup dialog box

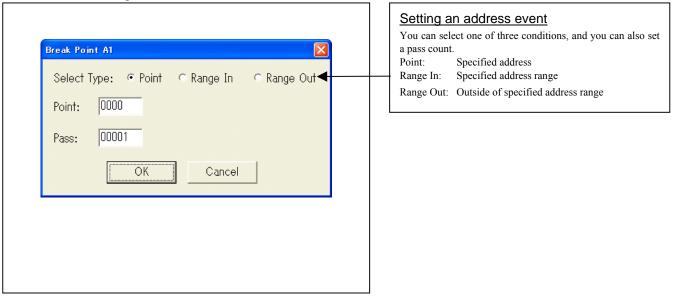
Opening the trace point setup dialog box

| Menu | Menu item | Function |
|-------|-------------|------------------------------------|
| Debug | Trace Point | Setting the trace point dialog box |

1. Setting the trace point dialog box

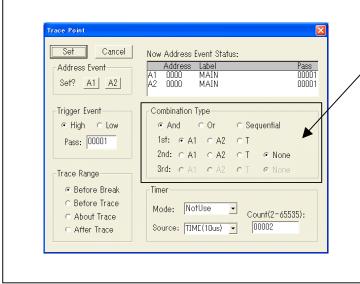


2. Address event setting window in initial state

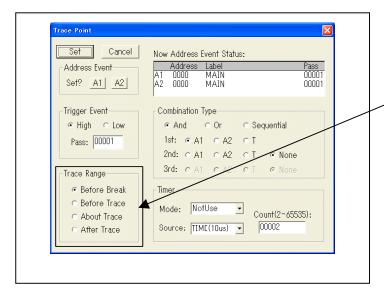


(3) Setting the combinatorial event condition

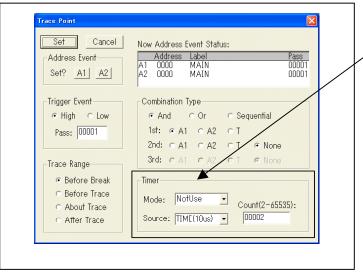
1. Window for setting the combinatorial event condition



2. Setting a break event



3. Timer setup area



Setting the combinatorial event condition

Select a combinatorial condition for A1, A2, and T. One of

the following three combinatorial conditions can be selected..

- AND
 - All of the specified conditions are met.
- OR

One of the specified conditions is met.

- Sequential

The specified conditions are met sequentially in a specified order.

| Setting a b | preak event |
|---------------|--|
| The following | g four break conditions can be set. You can set |
| two or more b | reak conditions at the same time. |
| Before break: | 32K cycles of instruction execution before the |
| 1 | user program stopped is recorded. |
| Before Trace: | 32K cycles of instruction execution before the |
| | user program stopped is recorded. |
| About Trace: | 32K cycles of instruction execution before and |
| | after a trace point condition was met is recorded. |
| After Trace: | 32K cycles of instruction execution after a trace |
| | point condition was met is recorded. |
| | |

Timer setup area

The following four operation modes of the timer can be specified.

NotUse: The timer is not used.

TimeOut: The program breaks when a breakpoint is reached within a specified time.

TimeCount: The program breaks when a specified time has elapsed after it started running.

DelayCount: The program breaks when a specified time has elapsed after a breakpoint is reached.

One of the following two can be specified as the count source for the timer.

TIME (10us): The passage of time if counted using the emulator's timer(10us)

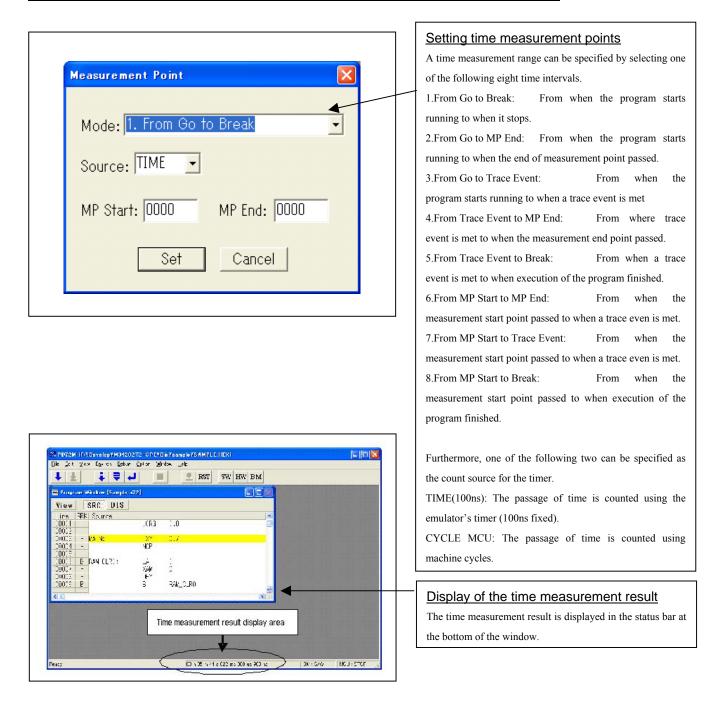
CYCLE: The passage time is counted by using MCU cycle.



3.5 Time Measurement

- (1) Trace window
- 1. Setting time measurement points

| Menu | Menu item | Function |
|-------|-------------------|---|
| Debug | Measurement Point | Sets up the time measurement points dialog box. |





4. Hardware Specifications

This chapter describes specifications of this product.

4.1 Target MCU Specifications

Table 4.1 lists the specifications of target MCUs which can be debugged with this product.

Table 4.1 Specifications of target MCUs for the M34519T2-CPE

| Item | | D | Description | | | | |
|-------------------------------|---|------------------------------|-------------------|-------------------|-------------------|------------|--|
| Applicable MCU | 4500 Series | s 4518, 4519, 4583 and 4584 | Groups | | | | |
| Evaluation MCU | M34519M | 8-001FP (mounted in the socl | ket of the emu | ator) | | | |
| | M34584M | D-001FP (included) | | | | | |
| Applicable power supply | 3.0 V ±5 % | 6 or 5.0 V ±5 % | | | | | |
| | - Available | only from the emulator, not | from the user s | system | | | |
| Maximum operating frequency | 3.0 V | | | Divided | -by 8-mode | | |
| | Set the MC | CU power supply voltage sel | ection switch | Divided | -by 4-mode | 6.0 MHz | |
| | (SW1) to 3 | V. | | Divided | -by 2-mode | | |
| | | | | Through | mode | 4.4 MHz | |
| | 5.0 V | | | Divided-by 8-mode | | | |
| | Set the MCU power supply voltage selection switch | | | | Divided-by 4-mode | | |
| | (SW1) to 5 | V. | Divided-by 2-mode | | 6.0 MHz | | |
| | | | | Through | mode | | |
| Clock supply | Main clock | (X _{IN}) | | Clock m | ounted on em | ulator | |
| | | | | (6MHz: | preinstalled, r | eplacable) | |
| Port emulation | Pin | Output type | Direction | Device | | | |
| | P00P03 | | I/O | Input | 74HC4050 | | |
| | P10P13 | N-channel open drain or | | Output | 74ALS641A | (N-ch) | |
| | D0D5 | C-MOS output | | | 74VHC126(C-MOS | | |
| | P20-P21 | N-channel open drain | I/O | I/O | 74HC4066 | | |
| Connection to the user system | Connected | by 2.54-mm-pitch 50-pin flat | t cable | | | | |



4.2 Differences between the Actual MCU and Emulator

Differences between the actual MCU and emulator are shown below. When debugging the MCU using this product, be careful about the following precautions.

| Μ | PO | RT | A٢ | JT |
|---|----|-----------|----|----|
|---|----|-----------|----|----|

Note on Differences between the Actual MCU and Emulator: • Operations of the emulator system differ from those of actual MCUs as listed below. (1) Initial values of internal resource data of an MCU at power-on With the emulator system, the ROM area at power-on in initialized to 000h (NOP instruction). (2) Voltage drop detection circuit Because the operating voltage of this product is fixed to 3V or 5V, it cannot evaluate any system using a voltage drop detection circuit. (3) Power-on reset You can reset this emulator system by the reset command of the emulator debugger M3T-PD45M, however, this emulator system cannot emulate operation at a power-on reset. Therefore, check the operation at a power-on reset using an actual MCU. (4) RESET* output Because an emulation circuit exists in pin RESET*, systems that use RESET output cannot be evaluated. (5) Internal pull-up transistor control Because this product has an emulation circuit present in ports P0 and P1, you cannot use the MCU's internal pullup transistors. Therefore, the M34519T2-CPE controls on/off of external pullup resistors $(68k\Omega)$ by decoding the pullup control register transfer instruction (TPU0A or TPU1A). (6) Unconnected pins Following pins are not connected to the user system. Xin, Xout, CNVss Note on RESET* Input: • A low input to pin RESET* from the user system is accepted only when a user program is being executed (when the RUN status LED on the emulator's upper panel is lit). • You cannot use an SRST instruction. If it is executed, it acts as a NOP instruction Notes on Operating Clock: • The clock generated on the OSC board only is usable as the operating clock, and the clocks listed below cannot be used. (1) External input clocks on the user system (2) Clocks generated by external RC oscillation circuit (3) Clocks generated by external ceramic resonators (4) Clocks generated by external crystal oscillation circuits (5) Clocks generated by internal on-chip oscillators *1 No operations are performed when the CRCK, CMCK or CYCK instruction is executed. • Clock input to the MCU is supplied from the oscillator circuit board OCS-2 in the emulator, and cannot be supplied from the oscillator circuit in the user system. If the system clock frequency needs to be changed, alter the circuit on the oscillator circuit board OCS-2 before use. For details, refer to Section 2.9.3, "Selecting Clock Supply" (page 37).

| IMPORTANT |
|---|
| Note on Watchdog Timer: ● With this product, the watchdog function cannot be used. However, the watchdog timer initialization cycle can be verified by observing the waveform at the check pin (WRST) of the emulator. For details, refer to "2.11 Watchdog Timer Initialization Cycle Check Pin" (page 41). |
| • With this product, DWDT instruction (stop of the watchdog timer function enabled) cannot be used. |
| Note on Port Electrical Characteristics: |
| • Because the following ports are configured with port emulation circuits, electrical characteristics differ from those of the actual MCU. |
| - P00P03 |
| - P10P13 |
| - P20P21 |
| - D0D5 |
| - RESET* |
| For more details, refer to "4.3 Connection Diagram" (page 60). |
| Notes on Port I/O Timings: |
| Port input timings |
| Port input timings are the same as with the actual MCUs. |
| • Port output timings |
| Port output timings Port output timings are different from those of the actual MCUs for the following ports that are configured with |
| port emulation circuits: |
| - P00-P03 |
| - P10-P13 |
| - D0D5 |
| With the actual MCUs, changes occur at the beginning of the T3 state of an output instruction. With this |
| product, changes occur at the beginning of the T2 state of the next output instruction. Figure 4.1 shows the port |
| output timing for this product. |
| The output timings excluding the indicated above are the same as with the actual MCUs. |
| Instruction X Port output instruction X Next instruction |
| Cycle - T1 - T2 - T3 - T1 - T2 - T3 - T3 - T2 - T3 - T3 - T3 - T2 - T3 - T3 |
| |
| |
| Port output of actual MCU |
| Port output of this product |
| |
| Figure 4.1 Port output timings |
| • As a pitch converter board and other devices are used between the evaluation MCU and the user system. Some |
| characteristics are slightly different from those of the actual MCU. Therefore, be sure to evaluate your system |
| with an evaluation MCU. |

RENESAS



IMPORTANT

Note on Register Operation:

• Tables 4.3 and 4.4 list the registers that can be operated from the M3T-PD45M. The "Yes" in the tables means that the register can be operated; the "No" means that the register can not be operated.

Table 4.3 Registers that can be operated when debugging 4518 and 4519 Groups MCUs

| Register | Reference | Modification | Register | Reference | Modification |
|----------|-----------|--------------|----------|-----------|--------------|
| PC | Yes | Yes | W3 | Yes | Yes |
| | | | | | |
| СҮ | Yes | Yes | W4 | Yes | Yes |
| A | Yes | Yes | W5 | Yes | Yes |
| В | Yes | Yes | W6 | Yes | Yes |
| Х | Yes | Yes | J1 | Yes | Yes |
| Y | Yes | Yes | Q1 | Yes | Yes |
| Z | Yes | Yes | Q2 | Yes | Yes |
| D | Yes | Yes | Q3 | Yes | Yes |
| Е | Yes | Yes | K0 | Yes | Yes |
| SP | Yes | No | K1 | Yes | Yes |
| V1 | Yes | Yes | K2 | Yes | Yes |
| V2 | Yes | Yes | PU0 | Yes | Yes |
| I1 | Yes | Yes | PU1 | Yes | Yes |
| I2 | Yes | Yes | FR0 | No | Yes |
| MR | Yes | Yes | FR1 | No | Yes |
| RG | No | Yes | FR2 | No | Yes |
| PA | No | Yes | FR3 | No | Yes |
| W1 | Yes | Yes | SI | Yes | Yes |
| W2 | Yes | Yes | | | |

"Yes" means that the register can be operated, "No" means that the register can not be operated. Table 4.4 Registers that can be operated when debugging 4583 and 4584 Groups MCUs

| Register | Reference | Modification | Register | Reference | Modificatio |
|----------|--------------------|---------------------|-------------------|---------------------|----------------|
| PC | Yes | Yes | W3 | Yes | Yes |
| CY | Yes | Yes | W4 | Yes | Yes |
| А | Yes | Yes | W5 | Yes | Yes |
| В | Yes | Yes | W6 | Yes | Yes |
| Х | Yes | Yes | Q1 | Yes | Yes |
| Y | Yes | Yes | Q2 | Yes | Yes |
| Ζ | Yes | Yes | Q3 | Yes | Yes |
| D | Yes | Yes | K0 | Yes | Yes |
| Е | Yes | Yes | K1 | Yes | Yes |
| SP | Yes | No | K2 | Yes | Yes |
| V1 | Yes | Yes | PU0 | Yes | Yes |
| V2 | Yes | Yes | PU1 | Yes | Yes |
| I1 | Yes | Yes | FR0 | No | Yes |
| I2 | Yes | Yes | FR1 | No | Yes |
| MR | Yes | Yes | FR2 | No | Yes |
| RG | No | Yes | | | |
| PA | No | Yes | | | |
| W1 | Yes | Yes | | | |
| W2 | Yes | Yes | | | |
| "Yes" m | eans that the regi | ster can be operate | ed, "No" means th | nat the register ca | n not be opera |

Note on Final Evaluation:

• Be sure to evaluate your system with an evaluation MCU.

4.3 Connection Diagram

Figure 4.2 shows a part of the connection diagram of the M34519T2-CPE. This connection diagram mainly shows the interface section. The circuits not connected to the user system such as the emulator's control system are omitted. The signals not shown in Figure 4.2 connect the evaluation MCU and the user system directly. Tables 4.5, 4.6, 4.7, 4.8 and 4.9 show IC electric characteristics of this product for reference purposes.

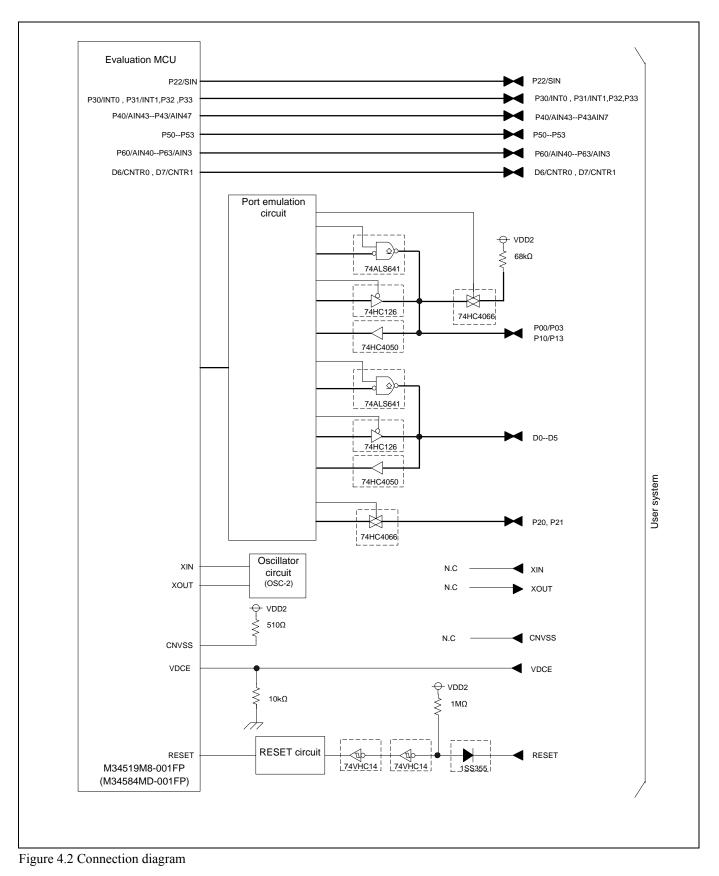




Table 4.5 Electrical characteristics of the 74HC4050

| Signal | Item | Condition | Standar | Unit | |
|--------|-----------------------------|-----------|---------|------|------|
| Signai | Itelli | Condition | Min. | Max. | Unit |
| | | Vcc=2.0V | 1.50 | - | |
| Vih | "H" level threshold voltage | Vcc=4.5V | 3.15 | - | |
| | | Vcc=6.0V | 4.20 | - | V |
| | | Vcc=2.0V | - | 0.50 | v |
| VIL | "L" level threshold voltage | Vcc=4.5V | - | 1.35 | |
| | | Vcc=6.0V | | 1.80 | |

Table 4.6 Electrical characteristics of the 74ALS641A

| Signal | Item | Condition | S | Unit | | |
|--------|--------------------|--------------------|------|----------|------|-------|
| Signai | Itelli | Condition | Min. | Standard | Max. | Ullit |
| VOL | "L" output voltage | Vcc=4.5V, IoL=24mA | - | 0.35 | 0.5 | V |
| Iol | "L" output current | | - | _ | 24 | mA |

Table 4.7 Electrical characteristics of the 74VHC126

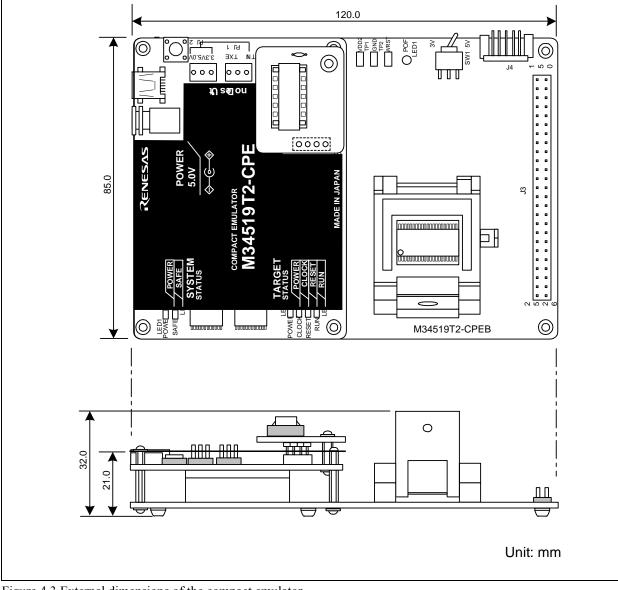
| Signal | Item | Condition | Standard values | | | Unit |
|--------|--|--------------------|-----------------|----------|------|-------|
| Signai | Item | Condition | Min. | Standard | Max. | Ullit |
| Voh | "I" autout voltage | Vcc=3.0V, IoH=-4mA | 2.58 | - | - | |
| V OH | "H" output voltage | Vcc=4.5V, IoH=-8mA | 3.94 | - | - | V |
| Vol | (⁴ I ²² automá a a lite se | Vcc=3.0V, IoL=4mA | - | - | 0.36 | v |
| | "L" output voltage | Vcc=4.5V, IoL=8mA | - | - | 0.36 | |

Table 4.8 Electrical characteristics of the 74HC4066

| Symbol | Itom | Condition | St | Unit | | |
|---------------|---------------------------------|-----------|------|----------|------|------|
| Symbol | Item | Condition | Min. | Standard | Max. | Unit |
| Ron | ON resistor | Vcc=4.5V | - | 96 | 170 | 0 |
| ΔR on | ON resistor difference | Vcc=4.5V | - | 10 | - | Ω |
| IOFF | Leak current (Off) | Vcc=12.0V | - | - | ±100 | |
| Iız | Leak current (On, output: open) | Vcc=12.0V | - | - | ±100 | μA |

Table 4.9 Electrical characteristics of the 74VHC14

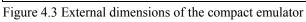
| Signal | Item | Condition | Standard | Unit | | |
|---------------------------|-----------------------------|-----------|----------|------|-------|--|
| Signai | item | Condition | Min. | Max. | Ullit | |
| Vp | | Vcc=3.0V | - | 2.20 | V | |
| | "H" level threshold voltage | Vcc=4.5V | - | 3.15 | | |
| | | Vcc=5.5V | - | 3.85 | | |
| | | Vcc=3.0V | 0.90 | - | | |
| \mathbf{V}_{N} | "L" level threshold voltage | Vcc=4.5V | 1.35 | - | | |
| | | Vcc=5.5V | 1.65 | - | | |
| $V_{\rm H}$ | | Vcc=3.0V | 0.30 | 1.20 | | |
| | Hysteresis voltage | Vcc=4.5V | 0.40 | 1.40 | | |
| | | Vcc=6.0V | 0.50 | 1.60 | | |



4.4 External Dimensions

4.4.1 External Dimensions of the Compact Emulator

Figure 4.3 shows external dimensions of the M34519T2-CPE.



4.5 Notes on Using This Product

Notes on using this product are listed below. Be sure to read these notes before debugging by using this product.

IMPORTANT

Notes on the Self-check:

- If the self-check does not result normally (excluding target status errors), the emulator may be damaged. Then contact your local distributor.
- Run the self-check with the user system not connected.

Note on Quitting the Emulator Debugger:

• To restart the emulator debugger, always shut power to the emulator module off once and then on again.

Notes on the User System (Power Supply, Order of Powering On):

- This emulator cannot supply the power to the user system. Therefore design your system so that the user system is powered separately.
- The voltage of the user system should be as follows.
 - 3.0 V \pm 5 % or 5.0 V \pm 5 %
- Do not change the voltage of the user system after turning on the power.
- Before powering on your emulator system, check that the host machine, the emulator, the converter board and user system are all connected correctly. Next, turn on the power to each equipment following the procedure below.
 - (1) Turn ON/OFF the user system and the emulator as simultaneously as possible.
 - (2) When the emulator debugger starts up, check the target status LEDs on the emulator to see if this product is ready to operate.

Is the power supplied? Check that target status LED (POWER) is ON.*1

Is the clock supplied? Check that target status LED (CLOCK) is ON.

*1 When the user system is not connected the target status LED (POWER) does not light.

| IMPORTANT | | | | |
|---|--|--|--|--|
| Note on the RAM Backup Mode: | | | | |
| • Although this emulator allows you to execute a program using POF instructions, execution of such programs is | | | | |
| subject to the following limitations: | | | | |
| (1) The POF instruction cannot be stepped and over-stepped. Therefore, do not attempt to step and step-over the POF instruction. | | | | |
| (2) No events (hardware breaks or trace points) can be set in execution cycles of the POF instruction. The | | | | |
| events set in execution cycles of the POF instruction, if any, have no effect. | | | | |
| (3) During RAM backup mode, no commands of the emulator debugger M3T-PD45M except for RESET can | | | | |
| be executed. Exit RAM backup mode by applying key-on wakeup input or reset input before executing the | | | | |
| commands. | | | | |
| Note on the MCU Status While the Program is Idle: | | | | |
| • The MCU status while the program is idle show below. | | | | |
| DI insertion mode: DI insertion mode is executed while the user program has stopped. | | | | |
| Clock stop mode: Clock stops while the user program has stopped. | | | | |
| *In this emulator, the MCU is operating even while the user program remains idle, as well as when the emulator is | | | | |
| executing a command. Therefore, the timer and other internal peripheral functions of the MCU are operating. | | | | |
| Note on a Break Operation When Skipping Instructions | | | | |
| • In cases when the next instruction is skipped by a skip instruction, if a break operation (hardware, software or | | | | |
| forcible break) in the skipped instruction is attempted, no break occurs. If a skip and a break occur at the same | | | | |
| time, the cause of the break is cleared and the program continues running until the next cause of break occurs. | | | | |
| Example: If a break operation is attempted when executing an instruction at address 0002h, the intended break | | | | |
| is canceled and the program continues running. | | | | |
| [ADDR] [CODE] | | | | |
| 0000 RC | | | | |
| 0001 SZC | | | | |
| 0002 TABP 1 : Skipped instruction 0003 TAM | | | | |
| 0004 BL 0004 : The program continues to execute | | | | |
| instruction without breaking. | | | | |
| | | | | |
| Note on a Break Operation in a Train of Successive Instructions: | | | | |
| • The program does not break in a train of successive instructions. If a break operation (hardware, software or | | | | |
| forcible break) is attempted in a train of successive instructions, the intended break occurs in an instruction at | | | | |
| which the successive instructions ended. An example is shown below. | | | | |
| Example: If a break operation is attempted while executing the instructions at addresses 00000003h, the | | | | |
| break occurs at address 0004h. | | | | |
| [ADDR] [CODE] | | | | |
| | | | | |
| 0001 LA 1 0002 LA 2 > Successive instructions | | | | |
| 0002 LA2 0000000000000000000000000000000 | | | | |
| 0004 NOP < A break occurs at this address. | | | | |
| | | | | |

5. Troubleshooting

This chapter describes how to troubleshoot when this product does not work properly.

5.1 Flowchart to Remedy the Troubles

Figure 5.1 shows the flowchart to remedy the troubles from when power to the emulator is activated until the emulator debugger starts up. Check this while the user system is disconnected. For the latest FAQs visit the Renesas Tools Homepage.

http://www.renesas.com/en/tools

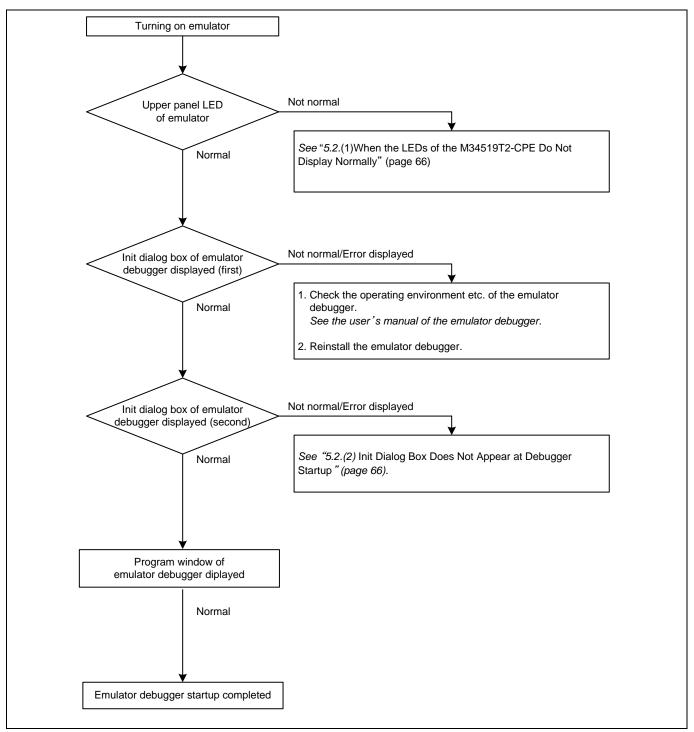


Figure 5.1 Flowchart to remedy the troubles

5.2 When the Emulator Debugger Does Not Start Up Properly

(1) When the LEDs of the M34519T2-CPE Do Not Display Normally

| Table 5.1 Errors LEDs show and their checkpoints 1 | | | | | |
|--|-------------------------------|--|--|--|--|
| Error | Connection to the user system | Checkpoint | | | |
| System Status POWER LED does not light up. | - | Check that power is properly supplied to the user system and that the user system is properly grounded. | | | |
| | | See "2.3 Connecting the Power Supply for the Emulator" (page 22). | | | |

| Target Status LED display | | Connection to | Problem & Remedy | | |
|---------------------------|-----------------------|---------------|--|---|---|
| | Blinking LOCK | OFF RESET | RUN | the user system | |
| | | | Connected | Check that power (Vcc) is properly supplied to the user system and that the user system is properly grounded. | |
| | | | | Disconnected | The emulator system is working properly. Target Status POWER LED does not light up when the user system is disconnected |
| | | | | - | The emulator system is not working properly. |
| | | | | - | Check that power is supplied to the emulator.The emulator may be damaged. Contact your local distributor. |
| | | | | - | |
| =[| 1 1 1 = 1 1 1 1 | | | - | A clock is not supplied to the emulator. Check that the oscillator circuit board (OSC-2) is attached. Check that the oscillator on the oscillator circuit board (OSC-2) or the oscillation module is oscillating properly. Refer to "2.9.3 Selecting Clock Supply" (page 37) |
| - [- | 1 1 1 = = | | | - | The MCU cannot be controlled. Check that the MCU is properly attached. Check that the oscillation frequency of the oscillator circuit board (OSC-2) is within the specified range. |
| Others | | - | The emulator system is not working properly. - The emulator may be damaged. Contact your local distributor. | | |

Table 5.2 Errors LEDs show and their checkpoints 2

(2) Init Dialog Box Does Not Appear at Debugger Startup

| Error | Checkpoint | | |
|----------------------------------|--|--|--|
| Communication error occurred. | - Check the target status LED display. If the LED is blinking, the emulator did no | | |
| Data was not sent to the target. | start up normally. | | |
| | See "2.5.5 LED Display When the Emulator Starts Up Normally" (page 26). | | |
| | - Check that the USB cable is connected properly. | | |
| | See "2.4 Connecting the Host Machine " (page 23). | | |
| | -USB device driver has been installed before the emulator debugger start up? | | |
| | See "2.2.2 Installing the USB Device Driver" (page 21) | | |
| Not compact emulator. | Check that an emulator other than the compact emulator (such as PC4701, PC7501) is | | |
| | not connected. | | |

5.3 How to Request for Support

After checking the items in "5 Troubleshooting", fill in the text file the installer of the emulator debugger generates in the following directory and email to your local distributor.

)

 $\SUPPORT\product-name\SUPPORT.TXT$

For prompt response, please specify the following information:

- (1) Operating environment
 - Operating voltage: [V] - Operating frequency: [MHz]
 - User system: Connected/Disconnected

___ [V]

- (2) Product information
 - Target MCU:
- Emulator:
- (3) Condition
 - The emulator debugger starts up/does not start up
 - The error is detected/not detected in the self-check
 - Frequency of errors: always/frequency (
- (4) Problem



6. Maintenance and Guarantee

This chapter describes how to maintenance, repair provisions and how to request for repair.

6.1 User Registration

When you purchase our product, be sure register as a user. For user registration, refer to "User registration" (page 11) of this user's manual.

6.2 Maintenance

- (1) If dust or dirt collects on any equipment of your emulation system, wipe it off with a dry soft cloth. Do not use thinner or other solvents because these chemicals can cause the equipment's surface coating to separate.
- (2) When you do not use this product for a long period, for safety purposes, disconnect the power cable from the power supply.

6.3 Guarantee

If your product becomes faulty within one year after its purchase while being used under good conditions by observing "IMPORTANT" and "Precautions for Safety" described in this user's manual, we will repair or replace your faulty product free of charge. Note, however, that if your product's fault is raised by any one of the following causes, we will repair it or replace it with new one with extra-charge:

- Misuse, abuse, or use under extraordinary conditions
- Unauthorized repair, remodeling, maintenance, and so on
- Inadequate user's system or misuse of it
- Fires, earthquakes, and other unexpected disasters

In the above cases, contact your local distributor. If your product is being leased, consult the leasing company or the owner.

6.4 Repair Provisions

(1) Repair with extra-charge

The products elapsed more than one year after purchase can be repaired with extra-charge.

(2) Replacement with extra-charge

If your product's fault falls in any of the following categories, the fault will be corrected by replacing the entire product instead of repair, or you will be advised to purchase new one, depending on the severity of the fault.

- Faulty or broken mechanical portions
- Flaw, separation, or rust in coated or plated portions
- Flaw or cracks in plastic portions
- Faults or breakage caused by improper use or unauthorized repair or modification
- Heavily damaged electric circuits due to overvoltage, overcurrent or shorting of power supply
- Cracks in the printed circuit board or burnt-down patterns
- Wide range of faults that makes replacement less expensive than repair
- Unlocatable or unidentified faults



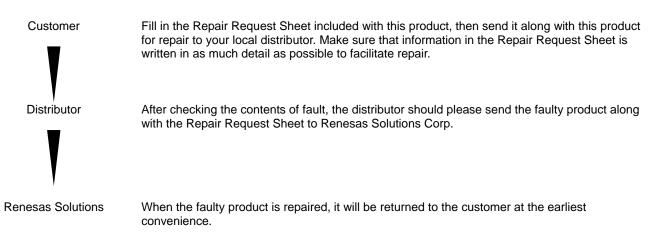
(3) Expiration of the repair period

When a period of one year elapses after the model was dropped from production, repairing products of the model may become impossible.

(4) Transportation fees at sending your product for repair Please send your product to us for repair at your expense.

6.5 How to Make Request for Repair

If your product is found faulty, follow the procedure below to send your product for repair.



Note on Transporting the Product:

• When sending your product for repair, use the packing box and cushion material supplied with this product when delivered to you and specify handling caution for it to be handled as precision equipment. If packing of your product is not complete, it may be damaged during transportation. When you pack your product in a bag, make sure to use conductive polyvinyl supplied with this product (usually a blue bag). When you use other bags, they may cause a trouble on your product because of static electricity.



Compact Emulator for 4518, 4519, 4583 and 4584 Groups M34519T2-CPE User's Manual

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