Introduction
The ISL28134SOICEVAL1Z Evaluation Board is designed to evaluate the performance of the ISL28134 Low Noise Chopper Stabilized op amp. The evaluation board contains the circuitry needed to evaluate the high performance of the ISL28134 amplifier. The ISL28134 chopper stabilized rail-to-rail input/output amplifier features a low 2.5µV maximum VOS and 15nV/°C drift over-temperature. The amplifier has no 1/f noise corner down to 0.1Hz with a 1kHz voltage noise density of 10nV/√Hz. The high open loop gain of 174dB allows a high gain single-stage DC amplifier that can operate from a 3V single cell battery while consuming 700µA of current. The ISL28134SOICEVAL1Z evaluation board can be configured as a precision high-gain (G = 1,000V/V) differential amplifier to demonstrate the level of performance possible with this amplifier.

Reference Documents
• ISL28134 Data Sheet

Evaluation Board Key Features
• Single Supply Operation: +2.25V to +6.0V
• Dual Supply Operation: ±1.125V to ±3.0V
• Singled-Ended or Differential Input Operation with High Gain (G = 1,000V/V)
• Banana Jack Connectors for Power Supply Inputs
• BNC Connectors for Op Amp Input and Output Terminals
• Convenient PCB pads for changing gain configurations, filtering and impedance loading.
• External VREF Input

Power Supplies (Figure 1)
External power connections are made through the V+, V-, and GND connections on the evaluation board. The circuit can operate from a single supply or from dual supplies. For single supply operation, the V- and GND pins are tied together to the negative or ground reference of the power supply. For split supplies, V+ and V- terminals connect to their respective supply terminals. The evaluation board power supply range is from +2.25V to +5.5V or ±1.125V to ±2.75V. De-coupling capacitors C2 and C4 provide low-frequency power-supply filtering, while additional capacitors, C1 and C5, which are connected close to the part, filter out high frequency noise. Anti-reverse diodes D1 and D2 protect the circuit in the momentary case of accidentally reversing the power supplies to the evaluation board.

![Figure 1. Power Supply Circuit](image1)

![Figure 2. Basic Differential Amplifier Configuration](image2)
Amplifier Configuration (Figure 3)

The schematic of the op amp input stage with the components supplied is shown in Figure 3, with a closed loop gain of 1,000V/V. The differential amplifier implements a high impedance differential input. The amplifier gain equation is expressed in Equation 1 as:

\[ V_{\text{OUT}} = (V_{\text{IN}^+} - V_{\text{IN}^-}) \times (R11 / R9) \]  

(EQ. 1)

For single-ended inputs the amplifier can be configured as an inverting gain \( G = -1,000 \text{V/V} \) by grounding the \( \text{IN}^+ \) input, or as a non-inverting amplifier with \( G = +1,001 \text{V/V} \) by grounding the \( \text{IN}^- \) input. The non-inverting gain is strongly dependent on any resistance from \( \text{IN}^- \) to GND. For good gain accuracy, a 0Ω resistor should be installed on the empty R1 pad.

User-Selectable Options (Figures 3 and 4)

Component pads are included to enable a variety of user-selectable circuits to be added to the amplifier inputs, outputs and the amplifier feedback loops.

The inverting and non-inverting inputs have additional resistor placements for adding input attenuation or to establish input DC offset through the VREF pin. To add a reference input without scaling it by the gain of the amplifier to the output, the circuit needs to be modified slightly. R13 and R15 form a voltage divider to the amplifier \( \text{IN}^+ \) terminal and is chosen to give the inverse of the feedback gain. In this configuration, \( R13 = R9 \) and \( R15 = R11 \) to scale the reference input by 1,000 while the feedback gain of the amplifier drives the input DC offset to the output in a 1:1 ratio. The non-inverting input signal will need to be AC-coupled into the amplifier.

The op amp output (Figure 4) also has additional resistor and capacitor placements for filtering and loading.

NOTE: Operational amplifiers are sensitive to output capacitance and may oscillate. In the event of oscillation, reduce output capacitance by using shorter cables, or add a resistor in series with the output.

High Gain Low Noise Amplifier

The high performance of the ISL28134 allow it to be configured as a very high gain amplifier with excellent DC and noise performance. The low offset voltage and low noise combined with a high gain is ideal for amplifying very small precision signals accurately for sensor applications. The eval board has been specifically configured for a gain of 1,000V/V to highlight these features of the ISL28134.

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**FIGURE 3. INPUT STAGE**

**FIGURE 4. OUTPUT STAGE**

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**TABLE 1. ISL28233SOICEVAL1Z COMPONENTS PARTS LIST**

<table>
<thead>
<tr>
<th>DEVICE #</th>
<th>DESCRIPTION</th>
<th>COMMENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>U1</td>
<td>ISL28134IBZ</td>
<td>SOIC 8 Ld Package</td>
</tr>
<tr>
<td>R11</td>
<td>100kΩ, 603, 0.125W, SMD Resistor</td>
<td>Feedback Gain</td>
</tr>
<tr>
<td>C3</td>
<td>DNP 603 SMD Capacitor</td>
<td>Feedback Filtering</td>
</tr>
<tr>
<td>R9, R12</td>
<td>100Ω, 603, 0.125W SMD Capacitor</td>
<td>Gain Set Resistors</td>
</tr>
<tr>
<td>R17</td>
<td>DNP, 603, SMD Resistor</td>
<td>Output Loading</td>
</tr>
<tr>
<td>C6</td>
<td>DNP 603 SMD Capacitor</td>
<td>Output Filtering</td>
</tr>
<tr>
<td>R1, R2, R3, R4, R6, R7, R8, R10, R14, R15, R16, R19, R20</td>
<td>DNP 603 SMD Resistor</td>
<td></td>
</tr>
<tr>
<td>C1, C2, C4, C5</td>
<td>Power Supply Decoupling</td>
<td>( C1 = C5 = 0.01\mu\text{F}; C2 = C4 = 4.7\mu\text{F} )</td>
</tr>
<tr>
<td>D1, D2</td>
<td>Reverse Polarity Protection Diode</td>
<td></td>
</tr>
</tbody>
</table>
ISL28134SOICEVAL1Z Schematic Diagram
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