



User's Manual

IMAPCAR Series Processor

1DC LANGUAGE SPECIFICATIONS

Software

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1 PREFACE

1DC (One Dimensional C) is an expanded implementation of C designed for programming parallel processor systems in which many PEs (Processing Elements) and memory blocks are linked together one dimensionally (Integrated Memory Array Processor, or IMAP, systems).

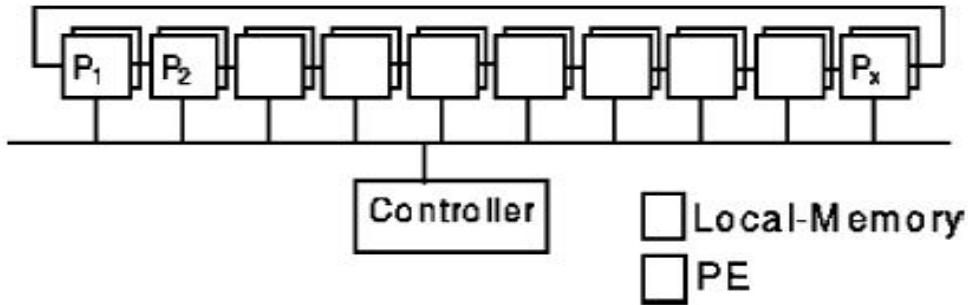


Figure 1 Example IMAP System Configuration

IMAP systems include two main operating modes: the SIMD (Single Instruction Multiple Data) mode, and the MP (Multi Processor) mode. A simple overview of how to use 1DC in each of these modes is provided below, and then the 1DC language specifications are described.

1.1 SIMD Mode

The figure below shows a configuration in which an IMAP system is used as an SIMD parallel processor. Here, *IMEM* refers to memory blocks allocated to each PE



Figure 2 IMAP System Configuration in SIMD Mode

When using an IMAP system in the SIMD mode, the processing for a certain collection of data is normally assigned to each PE. In this mode, each PE has to execute the same instructions broadcast from the controller (the control processor, or CP), but each PE can access the memory block (local memory) where the data it is in charge of is stored using high speed, unique addresses, and data can be directly exchanged between PEs by using a network (without having to wait). To the user, the SIMD mode makes it seem as though there is a working 2D memory area that has extremely little access overhead.

1DC is an IMAP system programming language designed to enable the design of parallel algorithms for operating in this memory area and to minimize the expansion of standard C specifications as much as possible. The following figure shows an operational overview of a parallel algorithm that uses the 2D memory area.

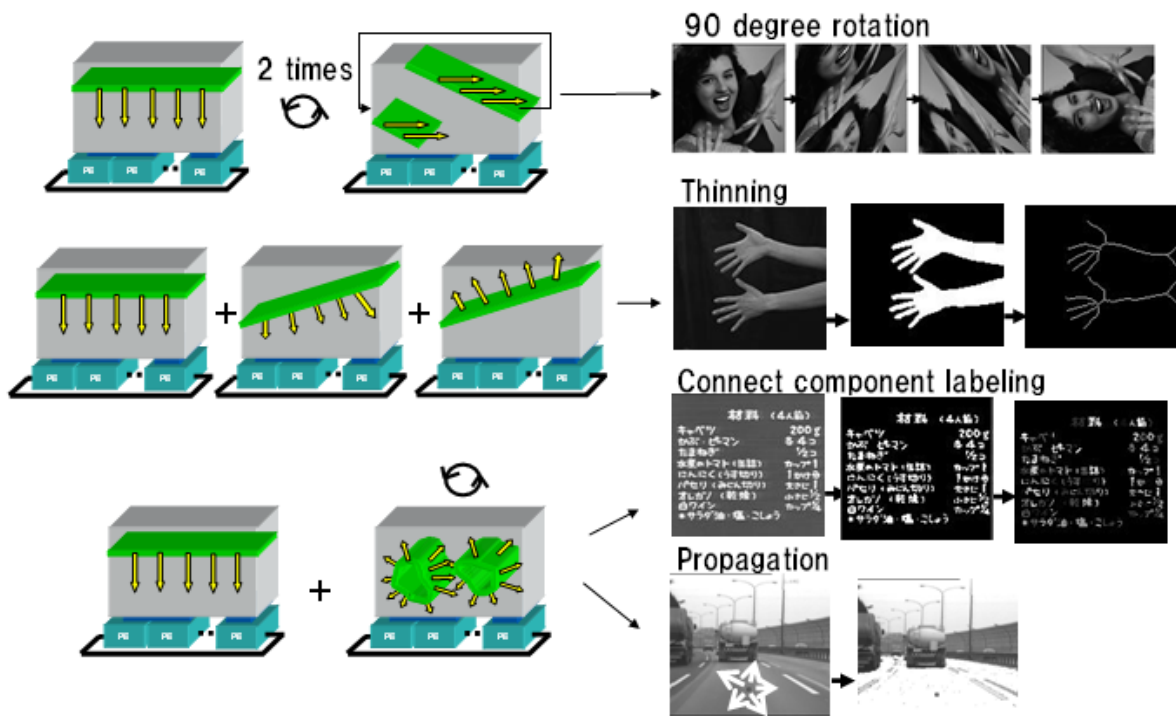


Figure 3 Operational Overview of Parallel Algorithm Using 2D Memory Area

1.2 MP Mode

When using an IMAP system in the MP mode, the system runs in a multi-processor configuration in which multiple PEs are grouped into one PU (Processing Unit). Because each PU runs on its own program counter, waiting is required when exchanging data between PUs, and message communication and non-cached external variable access methods are used, but different processing can be performed for each PU in parallel. This multi-processor parallel processing is programmed so that it is available without using the portion of the 1DC specifications outside the range of standard C.

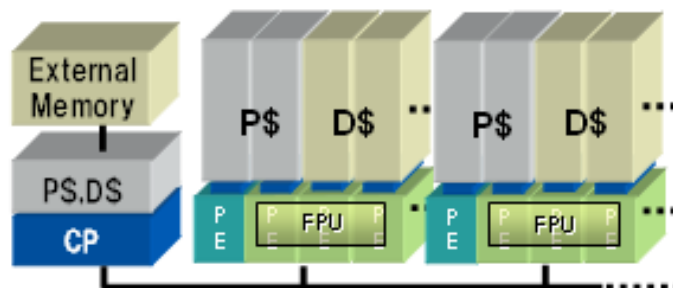


Figure 4 IMAP System Configuration in MP Mode (When Each PU Consists of 4 PEs)
1.3 Mixed mode

An IMAP system can also be used in the MIXED mode, in which some PEs run in the SIMD mode, and the rest run as multiple PUs in the MP mode. When using the MIXED mode, tasks (functions) that run in the SIMD mode are programmed using the portion of 1DC that is expanded from standard C, while those that run in the MP mode are programmed using standard C. Data exchanges between PUs running in the same operating mode are performed through the CP or through external memory.

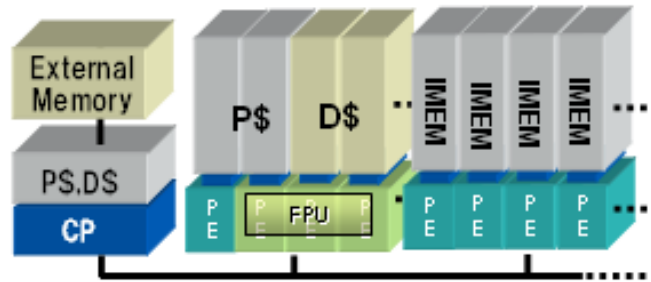


Figure 5 . IMAP System Configuration in MIXED Mode (When Each PU Consists of 4 PEs)

1.4 Hardware Configuration Example (for XC Core)

The figure below shows the configuration of an XC core, an example of an actual IMAP system. Each tile is made up of 8 PEs, which are grouped into a higher 4 PEs and a lower 4 PEs, and the user can select whether each of these groups runs as a PU or as 4 separate PEs. No 4-PE group runs as a PU in the SIMD mode, all 4-PE groups run as PUs in the MP mode, and only lower 4-PE groups run as PUs in the MIXED mode.

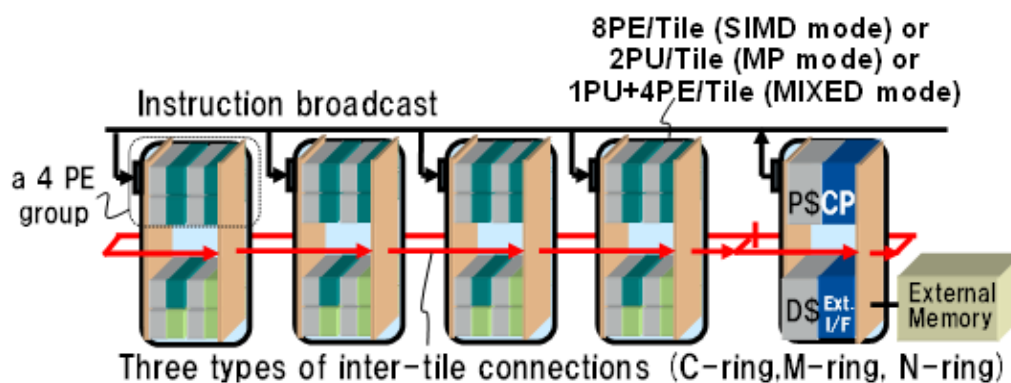


Figure 6 Example Physical Configuration of IMAP System (for XC Core)

2 1DC SPECIFICATION EXPANSION FROM C

Compared to C, the 1DC specifications have been expanded as follows.

- Additional data attributes
 - Data allocation attributes
 - uni (default)
 - multi
 - outside qualifier
 - align qualifier
 - Data value attributes
 - common (default)
 - sep (or separate)
 - Pointers to multi sep or sep data

- Additional operators
 - Operators for transferring data between PEs: :> and :<
 - Operator for selecting PEs: :[and :] (used as a pair)
 - Logical operators for PE data: :|| and :&&
 - Operator for initializing PE data: :(and :) (used as a pair)
 - PE data assignment operator: :=

- Additional syntax
 - PE selecting conditional statements 1: mifa and melsea
 - PE selecting conditional statements 2: mif and melse
 - PE selecting loop statement: mwhile
 - PE selecting loop statement: mfor
 - PE selecting loop statement: mdo

These expanded specifications are described in the above order below.

2.1 Additional Data Attributes

Data allocation attributes	
<i>uni</i> default)	<p>The <i>uni</i> attribute is the default in 1DC for data allocation. Data that has this attribute actually consists of only one element. <i>uni</i> data is assigned to the memory for the controller that controls all PEs, which enables the compiler to broadcast this data to all PEs at compile time as necessary. Therefore, the <i>uni</i> data has the same value when viewed from each PE. To give a variable the <i>uni</i> attribute, either specify the <i>uni</i> type qualifier before the variable data type when declaring the variable, or declare the variable without the type qualifier.</p> <pre> int x; // Declares the 2-byte signed integer x for the controller. uni int x; // Declares the 2-byte signed integer x for the controller (as above). </pre>
<i>multi</i>	<p>Data that has the <i>multi</i> attribute has as many elements as there are LPA PEs and is assigned to the local memory for the PE array of the LPA. To give a variable the <i>multi</i> attribute, specify the <i>multi</i> type qualifier before the variable data type when declaring the variable.</p> <p>However, if the value attribute of a variable is specified using the <i>sep</i> type qualifier described below, the variable is automatically given the <i>multi</i> attribute, so the <i>sep</i> type qualifier is normally sufficient for declaring <i>multi</i>-type data.</p>
<i>outside</i> qualifier	<p>The <i>outside</i> type qualifier is used to directly assign data to external memory. To give a variable the <i>outside</i> attribute, specify the <i>outside</i> type qualifier before the variable data type and other qualifiers as follows when declaring the variable:</p> <pre> outside sep int a[20],b; // Declares the sep array a and sep // variable b, which have the outside attribute. outside int c; // Declares a uni-type variable that has // the outside attribute. outside int d[10]; // Declares a uni-type array that has // the outside attribute. </pre> <p>All arithmetic operations can be used for <i>uni</i>-type variables and arrays that have the <i>outside</i> attribute, but declaring a <i>sep</i>-type variable or array that has the <i>outside</i> attribute only allocates memory for the data, and the data must be explicitly transferred from the external memory to the PE-local memory in order to actually reference the data. The data can be transferred using the standard <i>IxEmemrd</i> and <i>IxEmemwr</i> functions as shown in the following example:</p> <pre> outside sep int owrk[VLINES]; // sep array owrk that has the // outside attribute sep int src[VLINES]; // sep array src in the PE array memory int i; IxEmemrd(src,work*PENO,VLINES*2); // Copies the data in the // work array to the src array. for(i=0; i<VLINES; i++) src[i] = ...; // Processes the data in the src array. IxEmemwr(src,work*PENO,VLINES*2); // Writes the data in the // src array back to the work array. </pre>

<p><i>align</i> qualifier</p>	<p>The <i>align</i> type qualifier is used to allocate memory for an array, starting at the beginning of a memory page (using a hardware dependent number of bytes) and is only meaningful for a global array. To give a global array the <i>align</i> attribute, specify the <i>align</i> type qualifier before the variable data type and other qualifiers when declaring the array.</p> <pre> align sep int tbl[256]; // Declares a sep array that has // the align attribute. sep int a,idx; // Declares a sep variable. </pre>
<p>Data value attributes</p>	
<p><i>common</i> (default)</p>	<p>The <i>common</i> attribute is the default in 1DC for data values. If data that has this attribute has multiple elements, the values of each of those elements are assumed to be the same. To give a variable the <i>common</i> attribute, either specify the <i>common</i> type qualifier before the variable data type when declaring the variable, or declare the variable without the type qualifier.</p> <pre> int x; // Declares the 2-byte signed integer x for the controller. common int x; // Declares the 2-byte signed integer x for the PE array. </pre> <p>However, because IMAP Series processors can efficiently broadcast data from the PE array controller to all PEs, declaring a <i>uni</i> type <i>common</i> variable and assigning it to the controller memory, instead of declaring a <i>multi</i> type variable and assigning it to the PE array internal memory, can reduce the amount of consumed PE memory without reducing execution efficiency. Therefore, <i>multi</i> type <i>common</i> variables are not normally required.</p>
<p><i>sep</i> (separate)</p>	<p><i>sep</i> data (data that has the <i>sep</i> attribute) refers to data for which the value of each element might differ, assuming the data has multiple elements. To declare a <i>sep</i> variable, specify the <i>sep</i> or <i>separate</i> type qualifier before the variable data type when declaring the variable. <i>sep</i> data has the <i>multi</i> attribute by default, and variables to be processed by the PE array are normally declared simply as <i>sep</i> variables.</p> <pre> int x; // Declares the 2-byte signed integer x (that // has the uni attribute) for the controller. sep char y; // Declares the 1-byte sep variable y. </pre>

Pointers to data with added attributes and structure specification method	
Pointers	<p>Pointers to data that has one of the added attributes described above can be used in 1DC. The following examples show how the meaning changes depending on where the <i>sep</i> type qualifier is specified when declaring a variable:</p> <pre> sep char *p; // Declares p, a pointer to sep char data. char * sep p; // Declares p, a sep variable that stores a // pointer to char data. sep char * sep p; // Declares p, a sep variable that stores // a pointer to sep char data. sep int (*f)(); // Declares f, a pointer to a function that // returns a sep int value. </pre>
Structures	<p>Normally, the declarations for structure members and the actual data allocation attributes and value attributes that are used are specified separately. The data allocation attributes and value attributes cannot be specified when declaring the members.</p> <pre> struct ab { int a; unsigned char b[20]; }; sep struct ab sep_ab; // Declares a structure for the PE array. struct ab uni_ab; // Declares a structure for the controller. outside struct ab oui_ab; // Declares a uni-type structure for // the external memory. outside sep struct ab osep_ab; // Declares a sep-type structure // for the external memory </pre> <p>An added attribute can also be directly specified when declaring a structure as follows:</p> <pre> sep struct ab { // Declares a structure for the PE array. int a; unsigned char b[20]; } sa </pre>

2.2 Additional Operators

<p>Operators for transferring data between PEs: :> and :<</p>	<p>In the PE array of the LPA, :> is used as a unary operator to reference the sep data in the adjacent PE on the left, and :< is used similarly for the adjacent PE on the right. In addition, :> is used as a binary operator to reference the sep data in the nth PE to the left of the current PE, and :< is used similarly for the nth PE to the right. This operator actually transfers data one adjacent PE at a time, and performs n transfers to transfer data a distance of n PEs.</p> <pre> int n; sep unsigned char x, y; y = x :< n; // Assigns the x value of the PE n PEs to the // right of the current PE to y of the current PE. y = x :< (n+1); // Assigns the x value of the PE n + 1 PEs // to the right of the current PE to y of the current PE. x = x :> (n+1); // Assigns the x value of the PE n + 1 PEs // to the left of the current PE to x of the current PE. x = :< x; // Assigns the x value of the adjacent PE to // the right of the current PE to x of the current PE. </pre>
<p>Operator for selecting PEs: :[and :]</p>	<p>This operator is used to select a PE in the PE array, send sep data in the PE to the controller, and then assign that data to a specified PE or broadcast the data to all PEs.</p> <pre> sep int x; int n,y; x:[n:] = x:[2*n+1:]; // Assigns the x value of PE (2 * n + 1) to x of the nth PE. y = 2 * x:[n:]; // Multiplies the x value of the nth PE // by 2 and assigns the result to y of the controller. x = x:[1:] // Assigns the x value of the first PE to // the x of all PEs (by broadcasting to all PEs). </pre>

<p>Logical operators for PE data: : and :&&</p>	<p>These operators are used to calculate the logical disjunction (:) or the logical conjunction (:&&) of the <i>sep</i> data of unmasked (active) PEs. In general, these operations are efficiently implemented by LPA hardware that reads out the result of calculating the logical disjunction of the values of the status registers (1 bit) for each PE.</p> <pre> sep int a,b; int c; c = : (a b); // Calculates the logical disjunction of // a and b for each PE, calculates the logical // disjunction of this result for all PEs, // and then assigns the final result to c of the controller. c = :&&a; // Calculates the logical conjunction of a for // all PEs, and then assigns this result to c of the controller. </pre>
<p>Operator for initializing PE data: :(and :)</p>	<p>This operator is used to assign different <i>sep</i> data constants to PEs. This operator can be used to easily specify <i>sep</i> constants. Only constants can be specified between :(and :), and multiple constants must be separated with commas. If the constant <i>c0</i> is followed by : and another constant <i>c1</i>, <i>c0</i> is assigned to <i>c1</i> PEs. If less constants are specified between :(and :) than the number of PEs, 0 is automatically assigned to the remaining PEs. However, if a comma (,) is specified after the last constant <i>cn</i>, <i>cn</i> is automatically assigned to the remaining PEs instead. Constant propagation is performed for this operator at compile time.</p> <p>For the following examples, it is assumed that there are 8 PEs and that the system <i>sep</i> constant <i>PENUM</i>, which stores the PE number of each PE (0 for the leftmost PE), has been defined:</p> <pre> sep int x = :(0,1,2,3,4 :); // The operator can also be used when initializing a //sep variable during declaration. x = :(0,1,2,3,4 :); // Equivalent to x = :(0,1,2,3,4,0,0,0 :) x = :(0,1,2,3,4, :) // Equivalent to x = :(0,1,2,3,4,4,4,4 :) x = :(0:2, 1:3, 4 :); // Equivalent to x = :(0,0,1,1,1,4,0,0 :) x = PENUM; </pre>

<p>PE data assignment operator: :=</p>	<p>This assignment operator specifies that a value be assigned to <i>sep</i> data for all PEs regardless of the current context. This operator is used to explicitly assign <i>sep</i> data to all PEs. For details about how the context is defined, see the next section.</p> <pre style="text-align: center;"> sep int a,b; a := b; // Assigns the b value of all PEs in the PE array // to the a of all PEs. </pre>																																
<p>Order of operations</p>	<p>The order of operations for 1DC operators is based on the order of operations for C. The following shows the order of operations for 1DC operators and their associativity.</p>																																
	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">Operators</th> <th style="text-align: center;">Associativity</th> </tr> </thead> <tbody> <tr> <td>:(:) :[:] () [] -> .</td> <td>Left to right</td> </tr> <tr> <td>! ~ ++ -- - (type) * & sizeof < > : :&&</td> <td>Right to left</td> </tr> <tr> <td>* / %</td> <td>Left to right</td> </tr> <tr> <td>+ -</td> <td>Left to right</td> </tr> <tr> <td><< >> < ></td> <td>Left to right</td> </tr> <tr> <td>< <= > >=</td> <td>Left to right</td> </tr> <tr> <td>== !=</td> <td>Left to right</td> </tr> <tr> <td>&</td> <td>Left to right</td> </tr> <tr> <td>^</td> <td>Left to right</td> </tr> <tr> <td> </td> <td>Left to right</td> </tr> <tr> <td>&&</td> <td>Left to right</td> </tr> <tr> <td> </td> <td>Left to right</td> </tr> <tr> <td>?:</td> <td>Right to left</td> </tr> <tr> <td>= += -= := etc.</td> <td>Right to left</td> </tr> <tr> <td>,</td> <td>Left to right</td> </tr> </tbody> </table>	Operators	Associativity	:(:) :[:] () [] -> .	Left to right	! ~ ++ -- - (type) * & sizeof < > : :&&	Right to left	* / %	Left to right	+ -	Left to right	<< >> < >	Left to right	< <= > >=	Left to right	== !=	Left to right	&	Left to right	^	Left to right		Left to right	&&	Left to right		Left to right	?:	Right to left	= += -= := etc.	Right to left	,	Left to right
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2.3 Additional Syntax

<p>PE selecting conditional statements: <i>mif</i> and <i>melse</i></p>	<p>The syntax of <i>mif</i> and <i>melse</i> statements is the same as that of C <i>if</i> and <i>else</i> statements: <pre><i>mif</i> (expression-1) <i>statement-block-1</i> <i>melse</i> <i>statement-block-2</i></pre></p> <p>Remark The <i>melse</i> statement is optional</p> <p>However, the value of <i>expression-1</i> must have the <i>sep</i> attribute, <i>statement-block-1</i> is executed only for the group of PEs (or PE context) for which the value of <i>expression-1</i> is not zero, and <i>statement-bloc-2</i> is executed only for the group of PEs (or PE context) for which the value of <i>expression-1</i> is zero. Note that, when these statements are used in a function, the PE context that is used is calculated by finding the logical conjunction of the PE context for the caller of the function and the PE context for <i>expression-1</i>.</p>
<p>PE selecting conditional statements: <i>mifa</i> and <i>melsea</i></p>	<p>The syntax of <i>mifa</i> and <i>melsea</i> statements is the same as that of the previously described <i>mif</i> and <i>melse</i> statements: <pre><i>mifa</i> (expression-1)<i>statement-block1</i> <i>melsea</i> <i>statement-block2</i></pre></p> <p>Remark The <i>melsea</i> statement is optional</p> <p>However, the value of <i>expression-1</i> must have the <i>sep</i> attribute. <i>mifa</i> and <i>melsea</i> differ from <i>mif</i> and <i>melse</i> in that, while <i>mif</i> and <i>melse</i> use the context calculated by finding the logical conjunction of the current context and the context for <i>expression-1</i>, <i>mifa</i> and <i>melsea</i> use the context for <i>expression-1</i> regardless of the current context. The <i>mifa</i> statement generates code that is more efficient than that of the <i>mif</i> statement because <i>mifa</i> does not consider nesting. Therefore, using <i>mifa</i> is faster if there is no need to nest masking conditions</p>
<p>PE selecting loop statement: <i>mwhile</i></p>	<p>The syntax of the <i>mwhile</i> statements is the same as that of C <i>while</i> statements: <pre><i>mwhile</i> (expression-1) <i>statement-block-1</i></pre></p> <p>However, the value of <i>expression-1</i> must have the <i>sep</i> attribute, and <i>statement-block-1</i> is executed only for the group of PEs (or PE context) for which the value of <i>expression-1</i> is not zero. For the <i>mwhile</i> statement, <i>statement-block-1</i> is executed in the context based on the (truth) value of <i>expression-1</i> during the first iteration, and, during the <i>n</i>th iteration (<i>n</i> = 2...), all operations for <i>statement-block-1</i> and <i>expression-1</i> (except assignment operations using :=) are performed only in the context determined according to the value of <i>expression-1</i> during the previous iteration.</p>
<p>PE selecting loop statement: <i>mfor</i></p>	<p>The syntax of <i>mfor</i> statements is the same as that of C <i>for</i> statements: <pre><i>mfor</i> (<i>expression-1</i>; <i>expression-2</i>; <i>expression-3</i>) <i>statement-block-1</i></pre></p> <p>This is equivalent to the following <i>mwhile</i> statement: <pre><i>expression-1</i>; <i>mwhile</i> (<i>expression-2</i>) { <i>statement-block-1</i> <i>expression-3</i>; }</pre></p>
<p>PE selecting loop statement: <i>mdo</i> and <i>mwhile</i></p>	<p>The syntax of <i>mdo</i> and <i>mwhile</i> statements is the same as that of C <i>do</i> and <i>while</i> statements: <pre><i>mdo</i> <i>statement-block-1</i> <i>mwhile</i> (<i>expression-1</i>);</pre></p> <p>However, the value of <i>expression-1</i> must have the <i>sep</i> attribute, and <i>statement-block-1</i> is executed during the first iteration regardless of the context determined using <i>expression-1</i>.</p>

3 Revision history

Version	Date	Document Number	Description
1.0	Sept 2009	U20036EE1V0UM00	First version

The following revision list shows all functional changes compared to the previous version.

Chapter	Page	Description