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Renesas Electronics Corporation

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(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.

Restrictions on Use of the SH-4A, SH4AL-DSP E200F Emulator

Please note that there are the following restrictions on use of the SH-4A, SH4AL-DSP E200F emulator.

1. MFI Trace and MFI Extended Monitor of the I/O Analyzer Function

- (1) If the 68 interface is used for booting in extended through mode, the extended monitor may display “Standard through mode in MFI boot”.
- (2) “DMA_MFRAM access” in the MFI trace is displayed only when MFRAM is accessed after setting bits LOCKW, DWTW, and WT of MFISCR and MFISCRW via the MFI bus. If the user program is written so that bits LOCKW, DWTW, and WT of MFISCR and MFISCRW are set by the SH-Mobile, the display is turned to “MFI Register access” when MFRAM is accessed next time.
- (3) In some cases, MFIDREQ is not reflected to the extended monitor when MFIDREQ is used for a transfer to MFRAM. This is because MFIDREQ cannot be sampled at the timing of updating the extended monitor due to the short assertion timing of MFIDREQ.

2. Corrections

(1) SH-4A, SH4AL-DSP E200F Emulator User's Manual

Additional document, Supplementary Information on Using the SH7318

Additional document, Supplementary Information on Using the SH7323

Additional document, Supplementary Information on Using the SH7345

There was an incorrect description in the note of (2) Using the function to trace the data transfer via MFI in section 3.2, Using the MFI Trace Functions (section 3, I/O Analyzer Function).

Timestamp information can be acquired even during a break.

(2) SH-4A, SH4AL-DSP E200F Emulator User's Manual

Additional document, Supplementary Information on Using the SH7318

Additional document, Supplementary Information on Using the SH7323

Additional document, Supplementary Information on Using the SH7345

There was an incorrect description in (c) To view the MFI access during execution of the user program of (2) Using the function to trace the data transfer via MFI in section 3.2, Using the MFI Trace Functions (section 3, I/O Analyzer Function).

The start of user program execution is not synchronized with the start of trace acquisition. To start trace acquisition, select [Restart].