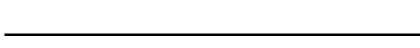
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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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## **Evaluation Board Information**

# 2SC5508/NE662M04 1.9 GHz LNA Evaluation Board (NF optimized)

- Evaluation Board Pattern Layout
- Circuit Description
- Noise Figure and Associated Gain
- Input and Output Return Loss Data
- 1 dB Gain Compression Output Power Data
- Reference Performance

Document No. PU10348EJ01V0EB (1st edition)
Date Published March 2003 CP(K)

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This document outlines general applications for this product. The application circuits and circuit constants provided in this document are simply examples and should not be used for mass production design. Be aware also that there is no intention to standardize the restrictions and characteristics of these application circuits.

The characteristics of high-frequency devices in particular vary depending on the external components and mounting pattern used.

Customers are requested to confirm all characteristics when designing a system based in part or wholly on the information in this document.

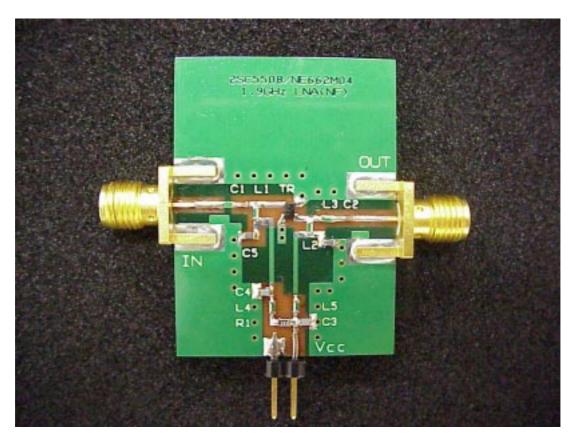
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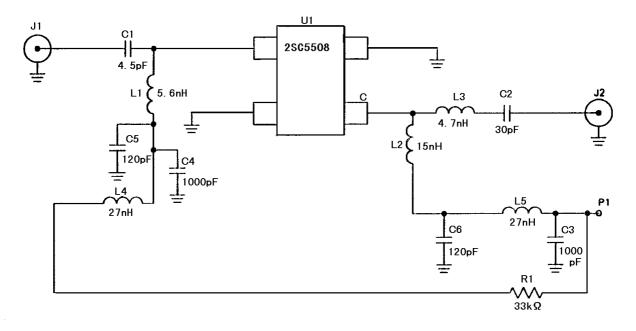
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M8E 00.4-0110

# **Evaluation Board Pattern Layout**

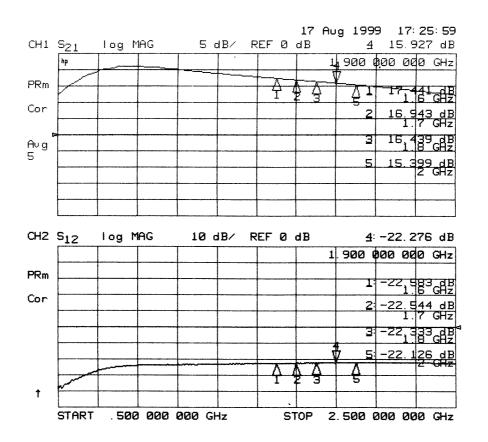


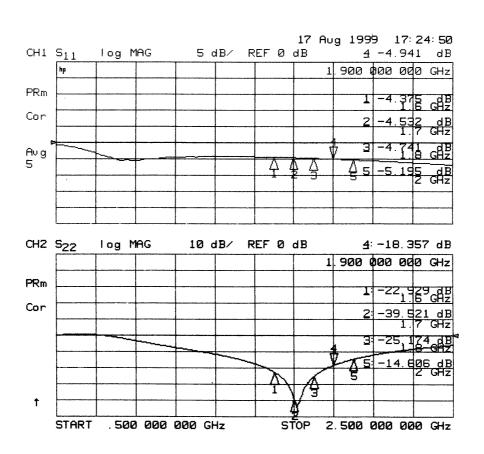
# **Circuit Description**

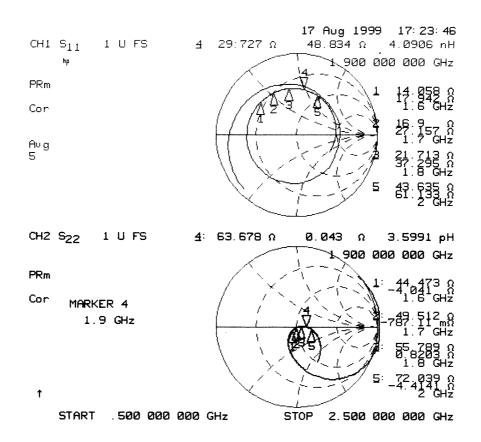


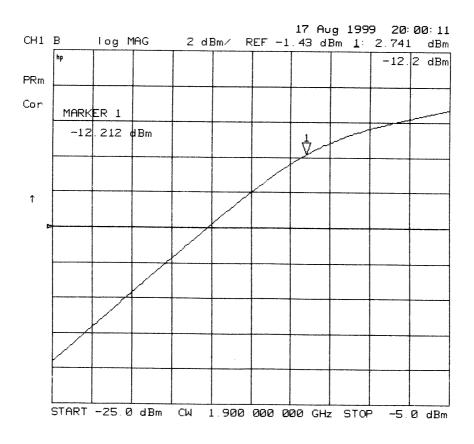
## 2SC5508/NE662M04 (TA = +25°C, VcE = 2 V, Ic = 5.5 mA, unless otherwise specified)

Parameter	Symbol	Data	Unit	Test Conditions
Noise Figure	NF	1.28	dB	f = 1.9 GHz
Associated Gain	Ga	16	dB	f = 1.9 GHz
Input Return Loss	RLin	4.9	dB	f = 1.9 GHz
Output Return Loss	RLout	18.4	dB	f = 1.9 GHz
1 dB Gain Compression Output Power	P <sub>(1 dB)</sub>	2.7	dBm	f = 1.9 GHz









## ▶ For further information, please contact

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