

RAA215300 Evaluation GUI

This manual provides installation procedures, features, and user information for the graphical user interface (GUI) evaluation software used by the RAA215300 and evaluation board.

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1. System Requirements

Following are the minimum system requirements for the evaluation software.

- Operating System: PC or virtual Windows machine with Windows 7 or greater
- .NET Framework: 4.5 or greater, and 3.5 (CLR 2)
- RAM: 100MB RAM
- Hard Disk: 10MB free disk space

Note: No special drivers are required for the Renesas USB Dongle (ISLUSBMINIZEVAL1Z), it operates as a Human Interface Device (HID).

2. Software Installation

The following steps describe installing the PC software.

Note: This evaluation software is a single evaluation tool for all RAA215300 evaluation boards.

- 1. Download the installer file from the website or from the USB drive.
- 2. The installer file named RAA215300_Installer_vX.Y.Z.exe, where X.Y.Z is the version number of the evaluation software, is downloaded into the system or user designated download folder.
- 3. Run the installer file.
- 4. Follow the installation wizard to install the evaluation software.
- 5. Read the license agreement, choose the accept option, and click **Next** to continue with the evaluation software installation.

LI	cense Agreement
	Please read the following important information before continuing.
	Please read the following License Agreement. You must accept the terms of this agreement before continuing with the installation.
	EVALUATION TOOL LICENSE AGREEMENT
	IT IS IMPORTANT THAT YOU CAREFULLY READ AND
	UNDERSTAND THIS AGREEMENT. BY CLICKING THE "I
	ACCEPT" BUTTON, YOU AGREE TO BE LEGALLY
	BOUND BY THE TERMS OF THIS AGREEMENT. IF YOU
	DO NOT AGREE WITH ALL THE TERMS OF THIS
	○ I accept the agreement
	● I do not accept the agreement

Figure 1. License Agreement



6. The default directory is C:\Program Files (x86)\Renesas\RAA215300.

🔀 Setup - RAA215300 Evaluation Software	_		×
Select Destination Location Where should RAA215300 Evaluation Software be installed?		(
Setup will install RAA215300 Evaluation Software into the f	ollowing	folder.	
To continue, click Next. If you would like to select a different folder,	click Bro	owse.	
C:\Program Files (x86)\Renesas\RAA215300	Br	owse	
At least 4.1 MB of free disk space is required.			
< Back Nex	t >	Car	ncel

Figure 2. Select Evaluation Software Installation Folder

7. To change the default location, click the **Browse** button. Click **Next** to continue to the Select Start Menu Folder. Create the Renesas folder in the Start Menu. Click **Next** to continue to Ready to Install.

10 octob	AA215300 Evaluation Software		_		\times
Select St	art Menu Folder			Ē	
Where	should Setup place the program's sho	ortcuts?		Ċ	
\$	Setup will create the program's sho	ortcuts in the follow	ing Start Mer	nu folder.	
To con	tinue, click Next. If you would like to	select a different fo	older, click Br	owse.	
Renes	as\RAA215300		в	rowse	

Figure 3. Select Start Menu Folder



8. Click **Install** to complete the installation process.

🐻 Setup - RAA215300 Evaluation Software —		×
Ready to Install Setup is now ready to begin installing RAA215300 Evaluation Software on your computer.		Ð
Click Install to continue with the installation, or click Back if you want to review change any settings.	or	
Destination location: C:\Program Files (x86)\Renesas\RAA215300		^
Start Menu folder: Renesas/RAA215300		
<	>	/
< Back Install	Ca	ncel

Figure 4. Ready to Install

9. Click **Finish** to exit setup. If the **Launch RAA215300** check box is selected, the evaluation software automatically runs.



Figure 5. Exit Setup



3. Using the Software

3.1 Open Software

Open (run) the evaluation software from the Windows Start menu. Unless changed during the installation process, the program resides under the Renesas folder.

3.2 Evaluation Software Initialization

During initialization, an attempt is made to establish connection to the HID dongle (ISLUSBMINIEVAL1Z). If the connection is successful, the main GUI form opens and the HID Status button turns green.



Figure 6. Default Evaluation Software GUI

Figure 7 shows the Renesas HID dongle.



Figure 7. Renesas ISLUSBMINIZEVAL1Z HID Dongle



If the connection attempt is unsuccessful, a warning message appears (see Figure 8) before the main GUI form is displayed.



Figure 8. No HID Dongle Error Message

After the dongle is connected to the PC, click **Retry**; if successful, the default GUI form displays (see Figure 6). Alternatively, clicking **Cancel** closes the warning message and opens the main GUI form to run in off-line mode, where no serial communication can occur until the HID connection is successfully (re)established.

3.3 Device/Slave Address

The first step after installing the evaluation software is to click **Find Slave Address**. The find slave address routine searches the potential address(es) for the IC and sets the found valid address(es) to the GUI (see Find Device/Slave Address(es)). When the routine completes, a message box appears to report the detected slave address (see Figure 9), or to indicate that no address was found (see Figure 10).



Figure 9. Slave Address Found

RAA215300	×
RAA215300 I2C slave address Not found.	
Verify IC And interface connections.	
ОК	

Figure 10. Slave Address Not Found



3.4 RAA215300 Specific GUI Tabs and User Controls

3.4.1 Buck1-6 Tabs

The GUI tabs, titled accordingly, display the output voltage targets and the configuration settings for each buck regulator. The GUI layout is identical (see Figure 11) across all Buck tabs for consistency and ease of use.



Figure 11. Buck1 Tab

The output voltage target is set using a sliding track bar for intuitive voltage increase/decrease adjustment. The track bar can be adjusted by clicking and dragging the bar. Likewise, it also supports mouse wheel scroll (the size of steps are determined by the Windows mouse scroll settings), PgUp/PgDn keys for a large change ($\pm 0x10$), and arrow Up/Down keys for small change ($\pm 0x1$). The output voltage code setting can also be adjusted directly in the relative numeric up/down text box.

The **Auto write** check box determines if output voltage changes are automatically written to the IC (checked), or the user needs to manually click **Write** after making an adjustment to the voltage target(s) (unchecked, default).

3.4.2 LDO1-3 Tabs

The LDO regulator's output voltage targets and configuration settings are contained on two GUI tabs (LDO1 Tab for LDO 1, and LDO2/3 tab for both LDO2 and LDO3 configuration settings).

Buck 1 Buck 2 Buck 4 Buck 5 Buck 6 LDO 1 LDO 2/3 Fault 1/2/3 Fault 4/5/6 Fault 4/5/6 <t< th=""><th>Slew Rate (Reg 0x4E) Soft Start Tims ~ SR Shut Down Tims ~ DVS SRup BmV/us ~ DVS SRup BmV/us ~ Read Write</th><th>LD01/2/3 Config (Reg 0x58) Active Normal LC ~ Active LD03 Discharge LD02 Discharge Medium ~ Discharge Medium ~ Discharge Medium ~ Discharge Medium ~</th></t<>	Slew Rate (Reg 0x4E) Soft Start Tims ~ SR Shut Down Tims ~ DVS SRup BmV/us ~ DVS SRup BmV/us ~ Read Write	LD01/2/3 Config (Reg 0x58) Active Normal LC ~ Active LD03 Discharge LD02 Discharge Medium ~ Discharge Medium ~ Discharge Medium ~ Discharge Medium ~
	TCOL	1000 THE

Figure 12. LDO1 Tab



3.4.3 Fault Configuration and Reporting Tabs

All fault configuration and status reporting reside on three GUI tabs (Fault1/2/3 Tab, Fault4/5/6, and Fault ECC).

			INT Mask				us 2 (Reg0x60)	INT Mask	FAULT Latched Sta				INT Mask
	LATCHED LIV		(0x64)		LATCHED	LIVE	Config (0x6A)	(0x65)		LATCHED	LIVE	Config (0x6B)	(0x66)
Reserved	LOW LO	W Shutdow ~	Unmask 🗸	Reserved	LOW	LOW	Shutdow \sim	Unmask 🗸	Reserved	LOW	LOW	Shutdow \sim	Unmask 🗸
Reserved	LOW LO	W Shutdow ~	Unmask 🗸	Reserved	LOW	LOW	Shutdow $ \smallsetminus $	Unmask 🗸	VREFIN UVLO Dis	LOW	LOW	Shutdow \sim	Unmask 🗸
Buck6 UV	LOW	W Shutdow ~	Unmask 🗸	Reserved	LOW	LOW	Shutdow \sim	Unmask 🗸	Buck6 OV	LOW	LOW	Shutdow $ \smallsetminus $	Unmask v
Buck5 UV	LOW	W Shutdow ~	Unmask 🗸	Reserved	LOW	LOW	Shutdow $ \sim $	Unmask 🗸	Buck5 OV	LOW	LOW	Shutdow $ \smallsetminus $	Unmask 🚿
Buck4 UV	LOW	W Shutdow ~	Unmask 🗸	VIO Pgood	LOW	LOW	Shutdow \sim	Unmask 🗸	Buck4 OV	LOW	LOW	Shutdow $ \smallsetminus $	Unmask 🕓
Buck3 UV	LOW	W Shutdow ~	Unmask 🗸	LDO3 UV	LOW	LOW	Shutdow $ \sim $	Unmask 🗸	Buck3 OV	LOW	LOW	Shutdow \sim	Unmask 🕓
Buck2 UV	LOW LO	W Shutdow ~	Unmask 🗸	LDO2 UV	LOW	LOW	Shutdow \sim	Unmask 🗸	Buck2 OV	LOW	LOW	Shutdow \sim	Unmask 🗸
Buck1 UV	LOW	W Shutdow ~	Unmask 🗸	LDO1 UV	LOW	LOW	Shutdow $ \sim $	Unmask 🗸	Buck1 OV	LOW	LOW	Shutdow $ \smallsetminus $	Unmask 🚿
Green='0'=OK Red='1'=Fault	Read	ad Read	Read	Green='0'=OK Red='1'=Fault	Read	Read	Read	Read	Green='0'=OK Red='1'=Fault	Read	Read	Read	Read
	Wr '1'	Write	Write		Wr '1'		Write	Write		Wr '1'		Write	Write

Figure 13. Fault1/2/3 Tab

Fault status reporting features both textual labels and color feedback for quick view/assessment. Green = Good/normal, Red = Bad/fault. Figure 14 shows how the GUI would report an active and uncleared undervoltage (UV) fault on Buck1. For faults that feature a Write 1 to clear mechanism, the user can click to toggle HIGH/LOW the required fault bit and click the respective **Wr** '1' button to write the register.



Figure 14. Example - Buck1 UV Fault



3.4.4 MPIO Tab

All the multiple purpose input-output (MPIO) configuration settings are all located on the MPIO Tab.

ck1 Buck 2 Buck 3 Buck 4 Buck 5 Buck 6 LDO 1 LDO 2/3 Fault 1/2/3 Fault MPIO0 Config (Reg0x8A)	4/5/6 Fault ECC MPIO Config1/Status Co MPIO0 Power On (Reg0x)		Off (Reg0x73)	MPIOx Assertion (Reg0x80
	Read Write Pwr On Delay 70ms ~	Pwr Off		MPIO0 Assertion Enabled: A re:
MPIO1 Config (Reg0x8B)	MPIO1 Power On (Reg0x			
nvert Active Low V Type Open drain, NI V Function Reset output V R	Read Write Pwr On Delay 20ms ~	Read Write Pwr Off Delay 0ms	∨ Read Write	MPIO1 Assertion Enabled: A re:
MPIO2 Config (Reg0x8C)	MPIO2 Power On (Reg0x			MPIO2
nvert Active High V Type Open drain, NI V Function PGood output V R	Read Write Pwr On Delay 'AND' of all reç v	Read Write Pwr Off Delay Oms	∨ Read Write	Assertion Enabled: A re:
MPIO3 Config (Reg0x8D)	MPIO3 Power On (Reg0x			MPIO3 Enabled: A res
nvert Active High V Type Disabled (high V Function Disabled V R	Read Write Pwr On Oms ~	Read Write Pwr Off Oms Oms	✓ Read Write	Assertion
MPIO4 Config (Reg0x8E)	MPIO4 Power On (Reg0x	A) MPIO4 Power	Off (Reg0x7B)	MPIO4 Enabled: A re
nvert Active Low \checkmark Type Disabled (high \checkmark Function Disabled \checkmark R	Read Write Pwr On Delay Oms ~	Read Write Pwr Off Oms Delay	✓ Read Write	Assertion
MPIO5 Config (Reg0x8F)	MPIO5 Power On (Reg0x			MPIO5 Assertion Enabled: A re
nvert Active Low V Type Disabled (high V Function Disabled V R	Read Write Pwr On Delay Oms ~	Read Write Pwr Off Oms	✓ Read Write	Read Write

Figure 15. MPIO Tab

3.4.5 Config1/Status and Config2 Tabs

The general IC status and information registers reside on the Config1/Status Tab.

Buck 1 Buck 2 Buck 3 Buck 4 Buck 5 Buck 6 LDO 1 LDO 2/3 Fault 1/2/3 Fault 4/5/6 Fault ECC MPIO Config1/Status Config 2 RTC General



Figure 16. Config1/Status Tab

All remaining IC configuration settings reside on the Config2 Tab.

ock Enable (Re	g (0x6C)	Config 1 (Reg 0x	:6F)	Config 2 (Reg 0	x 70)	Config 3 (Reg (x71)	Spr	read Spectru	ım 1 (Reg 0x86) —	Spread Spectru	m 2 (Reg 0x87)	Spread Spectru	m 3 (Reg 0x88
leep State	~	Charge Current Level	60uA ~	VCCBAT	3.3V ~	VIO Timeout	1ms 🗸	Fre	eq SS	17.5kHz 🗸	Buck4 PFM AM	-1, -1, 0, 0 🗸	SS PWM MOD	Triangular
TC C Charger	~	Temp Warning Threshold	105C ~	Sleep to DPFM	1ms v	Cold Reset	255ms ~	PW	VM AM	Disabled 🗸	Buck3 PFM AM	-1, -1, 0, 0 🗸	Read	Write
CBAT Comp	Enabled ~	Buck 1 High Current Thresh	6A ~	Delay		Delay			ck6	-1, -1, 0, 0 🗸	Buck2	-1, -1, 0, 0 🗸		
VD VD PD	Enabled V	AVDD UVPD Config	2.7V ~	PWRON Hold Period	1s ~	Read	Write		M AM	-1, -1, 0, 0 🗸	PFM AM	-1, -1, 0, 0 🗸	PWRON Config	g (Reg 0x90)
VD RST	Enabled 🗸	PWRON Config	Long Pust 🗸			Shutdown (Reg) (0x89)		ck5 M AM	-1, -1, 0, 0 ∨	Buck1 PFM AM	-1, -1, 0, 0 ∨	PWRON Polarity	Active higl
RST Fault	Enabled \checkmark	Read	Write	Read	Write	Shutdown Option LDO	Shut dowr $ \sim $		Read	Write	Read	Write	Read	Write
Read	Write					Shutdown	Shut dowr v							
						Option Buck								
						Read	Write							

Figure 17. Config2 Tab



3.4.6 RTC Tab

One tab is dedicated to the real-time clock (RTC) timing and configuration settings. All registers on this tab use the I^2C RTC slave/device address to communicate with the IC.

Buck 1	Buck 2	Buck 3	Buck 4	Buck 5	Buck 6	LDO 1	LDO 2/3	Fault 1/2/3	Fault 4/5/6	Fault ECC	MPIO	Config1/Status	Config 2	RTC	General

RTC Time (HH:MM:SS)		RTC Date (Day-DD-MM-YY)	RTC Setup (Reg 0x07)
Hours (Reg 0x02) (AM/PM) (12/24 hr) 0 0 0 1 AM v 12hr v	Minutes (Reg 0x01) Seconds (Reg 0x0 0 0 0 0		Disable V No Alarm V Int XT Oscillator Battery Backup Rea
Read Write	Read Write Read Write		Vite Read Write Enable V No V Write RTC Reg Write RTC Failure
Alarm Time (HH:MM:SS)		Alarm Date (Day-DD-MM)	User1 (Reg 0x12) Disable V OK V
Hours (Reg 0x0E)	Minutes (Reg 0x0D) Seconds (Reg 0x1 0 + 0 + 0 + 0 +	Day (Reg 0x11) Date (Reg 0x0F) Month (Reg 0x0F) Day1 0 0 0 0 0 0 0 0 0 0 0	
Disable ~ 12hr ~ (12/24 hr)	Disable V Disable V	Disable v Disable v Disable	✓ User2 (Reg 0x13) Single ever √ Enable √ 0 ← Aarm Function Freq Output Read
Read Write Analog Trim (Reg 0x0A)	Read Write Read Write	Read Write Read Write Read W	Vite 0 + Write Alam Function Freq Output Rea
BM ATR OpF V ATR	12.5pF V Read Write	DTR Oppm V Read Write Sync RTC to Sys Clk	✓ Sync Date Read All Write All Low Power Mode ✓ 24hr format RTC Normal osc ∨

Figure 18. RTC Tab

To sample the current computer system clock time and set to the RTC registers, click Sync RTC to Sys Clk.

The GUI notifies the user if there is a suspected conflict in the RTC settings (for example, entered or read-back time data conflicts with the selected 12/24hr setting); however, it does not prevent the reading and displaying of incorrect times.

Note: To start and communicate to the RTC timing registers, ensure the **Int XT Oscillator** and **RTC Reg Write** settings in reg0x07 are enabled.

3.4.7 Auxiliary Controls

Various auxiliary IC controls are located in the bottom right vicinity of the GUI window.

Software Reset (Reg 0x6D)	
Reset Normal V Write	System Control (Reg 0x95) Write
I2C Trigger Power-Off (Reg 0x6E) Key (hex)	vvile
Power-Off A8 - Read	MPIO Config Lock* Unlocked V Read
EEPROM Control (Reg 0xFF)	*Once this bit is set to '1' (locked), it cannot be set back to '0'(unlocked) until POR
Write Memory Recall Memory	
White Memory Recail Memory	



3.4.7.1 Software Reset

The software reset control provides selection for the type of reset to be triggered when the Write button is clicked.

3.4.7.2 I²C Trigger Power-Off

The I²C Trigger Power-Off control provides the ability to read-back the currently set I²C key value, in addition to choosing a different key. The key is sent to the IC when **Power-Off** is clicked.

3.4.7.3 EEPROM Control

Click **Write Memory** to initiate IC non-volatile EEPROM programming operations, where all current register values in the IC are copied to EEPROM.

Click **Recall Memory** to reload stored EEPROM values to the IC volatile registers. After a Recall Memory operation, Renesas recommends (re)synchronizing the GUI display with the IC registers, see Read All.

3.4.7.4 System Control - MPIO Lock

The system control provides access to the MPIO configuration lock feature. When the lock is activated, the IC prevents MPIO configuration register settings from subsequent changes during the current IC power-on period. To reset (clear) the lock, the IC must be power cycled to trigger a power-on reset (POR).

3.5 Supplementary Support Features

The evaluation software has supplementary features such as data-logging and script execution, read and write of all registers, finding or changing a slave address, and others as shown in Figure 20.



Figure 20. Supplementary Features

3.5.1 File Menu

3.5.1.1 Save

The **Save** option saves all the register settings, as currently shown in the GUI, into a text (load) file (see Figure 21).

File	Help		
	Save	Ctrl+S	
	Open	Ctrl+0	
	Exit	Ctrl+X	

Figure 21. File > Save

The text file generated has 8-bit data values in hexadecimal format for the 0x00 to 0xFF register space. Each line of the text file corresponds to register address (n - 1). For example, line one of the text file contains data from register address 0, and line 256d contains data from register address 255d (0xFF).

It is acceptable to manually add notes or comments to any line in a text load file, placed after the 8-bit hexadecimal data value (two characters). **Note:** No special delimiters are required.

3.5.1.2 Open

The **Open** option (re)loads all the register settings from a properly formatted text load file into the evaluation software (see Figure 22) and updates the GUI display.

File	Help		
	Save	Ctrl+S	
	Open	Ctrl+0	
	Exit	Ctrl+X	
-			

Figure 22. File > Open

Note: By default the evaluation software does not automatically write the data loaded from the text file into the IC registers. To write all register data, click **Write All** after **File > Open**. See Auto Write Selection.

3.5.1.3 Exit

The **Exit** options closes the application window and exits the program. Functionally, this is the same as clicking the X in the upper right of the GUI window to close the program.

3.5.2 HID Status and Connection

The HID Status displays the state of the connection between the software/PC and Renesas HID dongle. The software automatically detects removal and insertion of the HID dongle to the PC. The **CONNECT** button is green when the HID dongle is connected, and red when disconnected. Clicking **CONNECT** resets and attempt to re-establish the HID connection.

If the dongle is disconnected while the evaluation software is running, a message box appears (see Figure 23). Click **Retry** to manually attempt to reconnect, or click **Cancel** to return to the GUI.



Figure 23. No HID Dongle

3.5.3 I²C ACK/NACK

Successful I²C communication contains acknowledge bits (ACK) at the end of each byte. No special notification or action is provided by the GUI for an ACK. However, the GUI monitors for unsuccessful I²C communication in the form of a not acknowledge (NACK) bit. If a NACK is encountered, the GUI notifies the user with a message box (see Figure 25), and generally terminates the current operation.

Renesas		×
	Please check connections. I2C NACK occured.	
	ОК	

Figure 24. I²C NACK

3.5.4 Find Device/Slave Address(es)

This feature searches for an RAA215300 device on the I^2C bus and sets the correct slave addresses to be used by the GUI. Click **Find Slave Address** to initiate the search. It searches for an I^2C ACK by attempting I^2C commands with incrementing slave address from 0x00 to 0x7F. If an address is ACK'd, the routine does a command sequence to verify an RAA215300 device.

If an RAA215300 device is located, it reads back the programmable slave addresses setting registers and set those values to the GUI, and a message box with the I²C slave addresses appears (see Figure 9). If the evaluation software cannot find the RAA215300 device on the bus, a message box with an error message appears (see Figure 10).

3.5.4.1 Address Change

It is possible to manually change the slave address for the RAA215300 Main or RTC address. Choose the new address from the pull-down list and click the respective write button to send a command to the IC to set the new address. If the command is ACK'd (successful), the GUI is updated to use the newly selected address. If the command is NACK'd (not successful), the GUI continues to use the current address.

This feature is not commonly used, but may be useful in system testing and debug.

3.5.5 Write All

The **Write All** button writes data to all the valid registers of the IC based on the settings shown on the evaluation software GUI. The currently selected slave address(es) in the GUI are used for the operation. If an I²C NACK is encountered the routine terminates immediately.

A check box option is provided to enable the I²C slave address search before executing the write all operation (see Figure 25).



Figure 25. Enable Save Address Search for Read/Write All

3.5.6 Read All

The **Read All** button reads all the valid registers and updates the evaluation software GUI settings based on the read-back data from the IC registers, which synchronizes the IC/device under test with the GUI display. It is always recommended to read-all whenever the evaluation software is first run and/or a device under test is (re)started (such as power cycled, reset, or memory recalled). The currently selected slave address(es) in the GUI are used for the operation. If an I²C NACK is encountered the routine terminates immediately.

A check box option is provided to enable the I²C slave address search before executing the read all operation (see Figure 25).



3.5.7 I²C Log and Script

The evaluation software has a live I^2C log that updates when an I^2C read or write transaction is performed (see Figure 26).

Log							
Sr N		R/W	Address	Data	Status	Timestamp	
9	0x08	Write	0xF8	0x00	1	I2C::2022/03/15 15:59:03	^
10	0×09	Write	0xF8	0x00	1	I2C::2022/03/15 15:59:03	
11	0x0A	Write	0xF8	0×00	1	I2C::2022/03/15 15:59:03	
12	0×0B	Write	0xF8	0×00	1	I2C::2022/03/15 15:59:03	
13	0x0C	Write	0xF8	0×00	1	I2C::2022/03/15 15:59:03	
14	0×0D	Write	0xF8	0x00	1	I2C::2022/03/15 15:59:03	
15	0×0E	Write	0xF8	0×00	1	I2C::2022/03/15 15:59:03	
16	0×0F	Write	0xF8	0×00	1	I2C::2022/03/15 15:59:03	
17	0x10	Write	0xF8	0×00	1	I2C::2022/03/15 15:59:03	
18	0x11	Write	0xF8	0×00	1	I2C::2022/03/15 15:59:03	
19	0x12	Write	0xF8	0x00	0	I2C::2022/03/15 15:59:03	~
_		User Notes:					
	Save Log	1) Redo a Cor	nmand: select a	in item/row usi	ng mouse click	orarrow up/dn key, and	
		Press <enter></enter>	to redo the cor	nmand	-		
		2) Press <spa< td=""><td>ce> to select th</td><td>e newest (bott</td><td>om) item of log li</td><td>st</td><td></td></spa<>	ce> to select th	e newest (bott	om) item of log li	st	
	Clear Log	If saving the	e Log, the file fo	rmat is valid to	be loaded/run	as a Script	



To redo a given command select the row using a mouse click or using the arrow up/down keys and press **<Enter>**. Press **<Space>** to jump to the most recently logged item (bottom of the list).

3.5.7.1 Save Log

All the transactions in the log are saved into a specifically formatted text file using the **Save Log** option.

3.5.7.2 Clear Log

The transactions in the log can be cleared by using the Clear Log option.

3.5.7.3 Load/Run Script

A saved log file (see Save Log) can be loaded and executed as a script. The I²C transactions execute in sequential order as defined by the log file. Both I²C read and write is supported. **Note**: If the log file has an incorrect slave address or line format, the script routine is terminated and a message box notifies the user of the error encountered (see Figure 27).

Renesas		Х
	Got I2C NACK. Script routine terminated at line::2	
	ОК	

Figure 27. Script Termination



3.6 General Tab

The General tab contains ancillary controls to help user interaction with the IC, dongle, and GUI (see Figure 28). The following sections describe the available controls.

SB Dongle Setup		Manual Transaction b7 b6 b5 b4 b3 b2 b1 b0 Reg Val (HEX)
uC: I2C Frequency	Set to Enable I2C repeat start for read-back sequence	Address(Hex)
400kHz 1MHz	for read-back sequence	0 0 0 0 0 0 0 0 0 Fegister Address (HEX)
HID Info		WRITE REG READ Copy Addr
Get DLL Info Get HID Info uC: GPIO Static Set	Clear	Dump Read all registers, and save values to dumpfile Reg Dump
P1.7 P1.6 P1.5 P1.4 P1.:	Read	16-bit Converter Tool HEX-to-t 0
P2.7 P2.6 P2.5 P2.4 P2.		No Auto Write: GUI controls updated to match load file settings, no registers written Auto Write Reg: Ubdate GUI controls and write Registers only

Figure 28. General Tab

3.6.1 Manual Transaction

The General tab includes an independent manual transaction section that is used to perform a user-configured, standard I²C transaction, see Figure 29. This feature operates independently from the main (IC specific) GUI tabs and controls. Register data read and written though this mechanism is not automatically reflected in the main GUI controls. Specific GUI register control(s) can subsequently be updated (synchronized) by a register specific read or read-all operation on the main GUI, see Read All.

The manual transaction is a useful debug tool and can be used to communicate with the Renesas device under test, and/or other I²C compliant devices on the bus.





3.6.1.1 7-Bit I²C Address Text Box

The slave address of the target IC is entered into the **7-bit I²C Address** box. Valid entries are hexadecimal 0 to 7F, other values or characters are not accepted.

3.6.1.2 Register Address Text Box

The 8-bit register address is entered into the **Register Address** box. Valid entries are hexadecimal 0 to FF, other values or characters are not accepted.

3.6.1.3 Reg Val (HEX) Numeric Text Box

The 8-bit register value is entered into **Register Val** box. Valid entries are hexadecimal 0 to FF, other values or characters are not accepted. The **Reg Val (HEX)** box works bi-directionally with the bit buttons.

3.6.1.3.1 Bit Buttons

The **Bit** buttons beneath each b7~b0 labels show 0 or 1 and gray or green, respectively, to visually represent the value shown in the **Reg Val (HEX)** box. The user may also click the bit buttons to toggle the bit values, and the updated total register value is updated accordingly in the **Reg Val (HEX)** box.

3.6.1.4 Copy Addr Button

The **Copy Addr** button copies the currently selected slave address from the Device Address (Main) and sets it to the **7-bit I²C Address** box.

3.6.1.5 Read Button

The **Read** button performs an I²C read transaction from the register address entered into the **Register Address** box and populates the value in the **Reg Val** box. The slave address for the transaction is used from the **7-bit I²C Address** box. A message box appears if the I²C transaction encounters NACK (see Figure 24).

3.6.1.6 Write Reg Button

The **Write Reg** button performs an I²C write transaction to the register address entered into the **Register Address** box with the value in the **Reg Val** box. The slave address for the transaction is used from the **7-bit I²C Address** box. A message box appears if the I²C transaction encounters NACK (see Figure 24).

3.6.2 USB Dongle Setup

The USB Dongle Setup section, located on the General tab, provides setup and control of the microcontroller (MCU) on the HID dongle board (see Figure 30).



Figure 30. USB Dongle Setup

3.6.2.1 I²C Frequency Radio Button

The I²C frequency radio buttons allows changing the SCL frequency.

3.6.2.2 Enable I²C Repeat Start Check Box

The dongle supports both I²C Stop-Start and Repeated Start read transactions. Checking the box (default) enables the repeated start option.

3.6.2.3 HID Info

This is an informational section that reports the Renesas DLL and HID dongle firmware details.

3.6.2.4 GPIO Static Set

The MCU has general purpose open-collector output port pins with weak pull-up to 3.3V. These output pins are controlled manually from the GPIO Static Set GUI controls. A checked box for a pin (or pins) chooses logic HIGH. An unchecked box for a pin (or pins) chooses logic LOW. To apply the check box selections to the HID dongle click the **Write** button. The **Read** button reads back the current state of all the dongle pins and updates the GUI. **Note**: More than one pin can be checked or unchecked at the same time.

Only general purpose ports P1.0 to P1.7, P2.0 and P2.1 are branched out from the dongle connector (see Figure 31).



Figure 31. ISLUSBMINIZEVAL1Z HID Dongle Pin Assignments

3.6.3 16-Bit Converter Tool

This tool converts 16-bit hexadecimal and decimal numbers as shown in Figure 32. The maximum hexadecimal allowed is 0xFFFF, and the maximum decimal allowed is 65536.



3.6.4 Register Dump

The register dump button reads back all necessary registers from the IC and creates a text file with a dump of register data. A register dump does not update the GUI controls.

The dump file format is compatible with the **File > Open** operation (see File Menu), therefore, it can be (re)loaded to the GUI.

Dump
Read all registers, and
save values to dumpfile
Reg Dump
neg bump

Figure 33. Register Dump

3.6.5 Auto Write Selection

The GUI provides a selection for how to behave when a text load file is opened and settings are loaded to the GUI. The default operation only updates the GUI selections. The user must manually click each register write button, or click **Write AII** after **File > Open**, to write all the registers in the IC.

An optional GUI setting offloads the user task of writing all registers. When selected, it automatically attempts a **Write All** register operation after the text load file has been read and the GUI settings updated.

Load File - Auto Write Selection	
No Auto Write: GUI controls updated to match load file settings, no registers with the setting of the settin	written
○ Auto Write Reg: Update GUI controls and write Registers only	

Figure 34. Auto-write Behavior Selection



4. Software Uninstall Process

The evaluation software can be uninstalled in a few simple steps. Navigate to the program in the Windows Start menu under the Renesas folder. Right-click on the program name and choose **Uninstall**. This opens the Windows Control Panel **Programs and Features**, where the program can be selected an uninstalled.

5. Troubleshooting Tips

The following are common troubleshooting tips for users experiencing issues while installing or using the evaluation software.

- Issues with installing or running program:
 - Try running installer and/or opening the program with administrative rights (right-click and Run As Administrator).
 - If Run As Administrator does not resolve the issue, the issue could be related to a third-party restriction such as corporate security policy or anti-virus. Contact your local IT administrator for further help.
 - Ensure the proper .NET frameworks are installed on the machine. Both .NET common runtime language (CLR) 4 and CLR 2 are required. This assembly targets .NET 4.5. .NET framework 4.5 or greater is required, which inherently runs CLR 4. Additionally, newer OS (such as Windows 10) also need to have .NET framework 3.5 enabled.
- Evaluation software text is enlarged, cut-off, or missing:
 - The evaluation software does not support non-native scaling or dynamic resizing, therefore, verify that the PC display settings are set to a 100% scale factor. This setting is in the Display Settings of a Windows PC.
 - The evaluation software is sized optimally for display resolution of 1366×768 or higher.
- Evaluation software cannot connect to the Renesas HID dongle:
 - The USB cable is not connected to the PC USB port and/or Renesas HID dongle. Connect the cable to both the PC USB port and Renesas HID dongle.
 - The USB cable is damaged. Replace the USB cable.
 - Bad USB port. Ensure the USB device is recognized by the machine. This can be checked in the Windows Device Manager. The connection/cable and port are good if different audible tones can be heard from PC when plugging the cable in and out.
- Another Renesas HID dongle is already connected to the machine.
- Issues with updating to newer version of the evaluation software:
 - Uninstall the existing software as described in the Software Uninstall Process; next, reinstall the newer version.
- Evaluation software text looks different than the images in the manual:
 - The evaluation software was developed in Windows 10 with a default system font for the English (United States) language setting. Other regions might use a different default system font for English, which affects the appearance of the evaluation software but not its operation.
- HID communication error message:
 - The GUI can report various HID USB communication errors using a message box (see Figure 35). The message box reports an error code number. Check all physical connections, restart the IC, and exit/reopen the GUI.
 - If the issue persists, contact Renesas to help diagnose the problem.





Figure 35. HID Communication Error Example

6. Revision History

Revision	Date Description	
1.01	Aug 29, 2022	Updated System Requirements section. Added another bullet in the Troubleshooting Tips section.
1.00	Jun 30, 2022	Initial release.

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