



VersaClock[®] LP Evaluation Board Setup Guide

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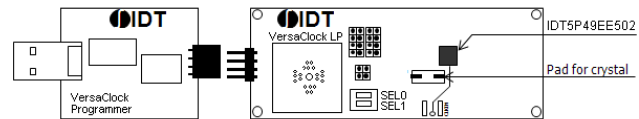
VersaClock® LP Eval Board Getting Started Guide

1 Download and Install the Software

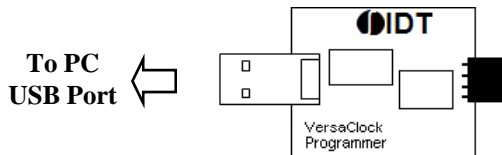
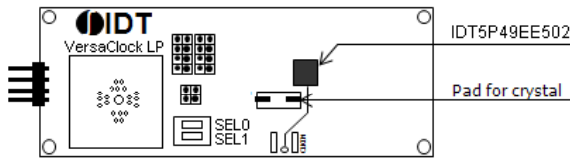
- Download the software from www.idt.com/go/versaclock3
- Install USB Driver
- Install VersaClock III Application

2 Set Jumpers and DIP Switches Correctly

- See next page



3 Insert Eval Board into PC USB



4 Launch VersaClock III Application

- If the application is already launched, please close the application and re-launch it again

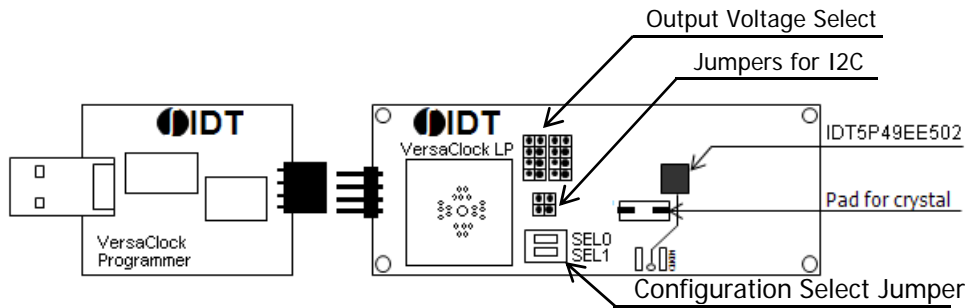
5 Configure the Frequencies

- Refer to “Configuring a Device” section in VersaClock III User Guide

6 Program The Device

- Refer to “Programming a Device” section in VersaClock III User Guide

To Configure the Board



- ❑ To Program with USB Interface
 - USB interface will translate into I2C interface
 - Short I2C jumpers for SCLK and SDAT (JP1)



- ❑ To Select I/O voltages
 - 1.8V, 2.5V and 3.3V power supplies are available
 - Register bits PS[1:0] selects output voltage to apply to an output clock
 - VDDO1 is the highest output voltage

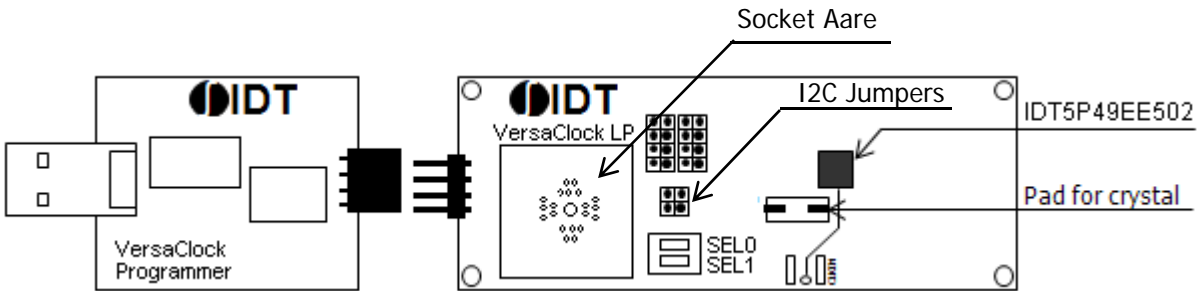


- ❑ To Select Clock Source
 - Connect external clock source to CLKIN, or mount a crystal
 - ONXTALB bit = 0 selects crystal
 - No external CLKSEL pin for clock source select
- ❑ To Select Configuration
 - Up to 3 configurations are supported: SEL[1:0] = 01, 10 and 11.
 - Note SEL1 = 0 and SEL0 = 0 sets the part in Sleep mode. Part will not be programmed



- ❑ To Enable Outputs
 - Output enable by configuration, no OE pin

To Program Devices Using the Socket Board



□ Steps of Use

- Install the socket in Socket area;
- Remove (disconnect) I2C jumpers in JP1
- Place an IDT5P49EE502 part in the socket. Pay attention to part orientation
- Plug in the board cumbo (board + programmer) into your PC and program the part is socket as previously described

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