

**RZ/V2MA Contents Guide**

 Note : (NDA) content requires advance application to the secure site.  
 If you click the link before applying, an error screen will be displayed.

Step	Launch of board and evaluation environment	Contents Type	Contents Name	RZ/V2MA Link
1	User's Manual	Document	User's Manual: Hardware	<a href="#">Link</a>
		Document	(NDA) User's Manual: Hardware	<a href="#">Link</a>
2	How to get Evaluation Board Kit	Board	RZ/V2MA Evaluation Board Kit (chip1stop)	<a href="#">Link</a>
		Board	RZ/V2MA Evaluation Board Kit (SHIMAFUJI)	<a href="#">Link</a>
		Document	RZ/V2MA Evaluation Board Kit Booklet	<a href="#">Link</a>
3	Evaluation environment start-up method	Document	Linux Package Release Note	<a href="#">Link</a>
		Document	Linux Package Component List	<a href="#">Link</a>
		Document	Linux BSP Manual Set	<a href="#">Link</a>
		Document	Linux Software Manual	<a href="#">Link</a>
		Document	Linux Package Start-Up Guide	<a href="#">Link</a>
		Document	Video Codec integration Guide	<a href="#">Link</a>
		Document	OpenCV Accelerator User's Manual	<a href="#">Link</a>
4	How to get various software	Software	Linux Package	<a href="#">Link</a>
		Software	Linux Open Source Package	<a href="#">Link</a>
		Software	DRP-AI Support Pacakge	<a href="#">Link</a>
		Software	Video Codec Pacakge	<a href="#">Link</a>
		Software	OpenCV Accelerator Support Package	<a href="#">Link</a>
5	How to get various tools	Tool	DRP-AI TVM	<a href="#">Link</a>
		Tool	DRP-AI Translator Installer	<a href="#">Link</a>
		Document	DRP-AI Translator Release Note	<a href="#">Link</a>
		Document	DRP-AI Translator User's Manual	<a href="#">Link</a>

Step	Custom board development	Contents Type	Contents Name	RZ/V2MA Link
	PCB Design Guide for High Speed Serial Interfaces	Document	(NDA) High-Speed Interface PCB Design Guidelines	<a href="#">Link</a>
	LPDDR4 Initialization Procedure Guide	Document	(NDA) LPDDR4 Initialization Procedure Guide	<a href="#">Link</a>
	Reference Design Data	Design Data	(NDA) Reference Design Data	<a href="#">Link</a>
	Thermal Design Guide	Document	(NDA)Thermal Design Guide	<a href="#">Link</a>
	Signal Interity (including IBIS), Power Integrity	Model + Document	(NDA) S parameter, IBIS	*

\*: Contact a Renesas Electronics sales representative.

Step	Information for engineers	Contents Type	Contents Name	RZ/V2MA Link
	RZ/V Series 64-bit MPU (Wiki)	Link summary	Introduction,Boards,Software and Documentation	<a href="#">Link</a>
	FAQ	Link summary	Knowledgebase / FAQ	<a href="#">Link</a>
	Contact Support	QA Ticket	Technical Q&A	<a href="#">Link</a>