

RZ/V2L Easy Download Guide

Step	Launch of board and evaluation environment	Contents Type	Contents Name	RZ/V2L Link
1	User's Manual			
		Document	User's Manual: Hardware	Link
2	How to start up the board			
		Document	SMARC Module Board User's Manual	Link
		Document	SMARC Carrier Board User's Manual	Link
		Document	Evaluation Kit(EVK) Start Up Guide	Link
3	Board information / how to get	Info/purchase	Evaluation Kit WEB Page	Link
4	Evaluation environment start-up method (Linux-OS for Cortex-A55)			
		Document	Release Note for RZ/V Verified Linux Package	Link
		Document	RZ/V Verified Linux Package Component List	Link
5	How to get various software (Linux-OS for Cortex-A55)			
		Software	RZ/V Verified Linux Package [5.10-CIP]	Link
		Software	RZ/V2L DRP-AI Support Package	Link
		Software	RZ/V2L ISP Support Package	Link
		Software	Graphics Library	Link
		Software	Codec Library	Link
		Software	AI Demo Binary(Object Detection, Pose Estimation, Classification)	
6	Evaluation environment start-up method (RTOS for Cortex-M33)			
		Document	Release Note for Multi-OS Package	Link
		Document	Getting Started with Flexible Software Package	Link
		Document	Setting GPIO with Flexible Software Package	Link
		Document	FSP Documentation	Link
		Document	Release Note for Flexible Software Package	Link
7	How to get various software (RTOS for Cortex-M33)	Software	CM33 Multi-OS Package	Link
8	How to get various tools			
		Tool	DRP-AI Translator	Link
		Tool	e² studio 2022-01 installer for Windows	Link
		Tool	Smart Configurator	Link
9	How to launch various tools			
		Document	Release Note for DRP-AI Translator	Link
		Document	Release Note for e² studio	Link
		Document	Smart Configurator for RZ User's Guide	Link

Step	Custom board development	Contents Type	Contents Name	RZ/V2L Link
	PCB design checklist when designing a custom board	Document	Check List for PCB Design	Link
	PCB Design Guide for High Speed Serial Interfaces	Document	PCB Design Guide for Serial Interface	Link
	PCB Design Guide for DDR memory	Document	PCB Design Guide for DDR memory	Link
	PCB Verification Guide for Core	Document,Model	PCB Verification Guide for Core	Link
	PCB Verification Guide for DDR memory	Document,Model	PCB Verification Guide for DDR memory	Link
	Reference Desing Data(Schamtics,Layout,BOM)	Design Data	Reference Design Data(Schamtics,Layout,BOM)	Link
	IBIS(For LSI)	Model	IBIS	Link
	Boundary Scan Description Language file	File	BSDL	Link
	Power Delivery Network model	Model	PDN	Link
	Mechanical Handling Guideline	Document	Mechanical Handling Guideline	Link
	Thermal Management Guide	Document	Thermal Management Guide	Link
	Reference Power consumption	Document	Reference Power consumption	Link
	Reflow Soldering conditions	Document	Reflow Soldering conditions	Link
	Baking conditions	Document	Baking conditions	Link
	Lifetime estimation guide	Document	Lifetime estimation guide	Link
	Power Management IC installed in EVK	Device Info	Power Management IC	Link
	Clock Generator IC installed in EVK	Device Info	Clock Generator IC	Link

Step	Information for engineers	Contents Type	Contents Name	RZ/V2L Link
	RZ/V Series 64-bit MPU (Renesas info)	Link summary	Introduction, Boards, Software and Documentation	Link
	FAQ	Link summary	Knowledgebase / FAQ	Link
	Contact Support	QA Ticket	Technical Q&A	Link

Step	Information for engineers	Contents Type	Contents Name	RZ/V2L Link
	Pre-Trained Free AI App Library			
		Link to GitHub	Free, open source-based library of pre-trained AI applications	Link

[Supplementary information] Renesas recomnds using latest Verified Linux Package (Linux Kernel v5.10).

Step	Information for engineers	Contents Type	Contents Name	RZ/V2L Link
	Old Version Software & Tools			
		Software	RZ/V Verified Linux Package 3.0.0 update2	Link
		Software	RZ/V2L DRP-AI Support Package v7.20	Link
		Software	RZ/V2L ISP Support Package v1.20	Link
		Software	Graphics Library v1.21	Link
		Software	Codec Library v0.58	Link
		Software	CM33 Multi-OS Package v1.02	Link
		Tool	DRP-AI Translatorv1.80	Link