

**RZ/A3UL Easy Download Guide**

This document regroups all links necessary for the user to develop an application

Mouseover 'L' in 'Link'

Step	Information for engineers	Contents Type	Contents Name	RZ/A3UL Link	Note
	RZ/A Series (Wiki)	Link summary	Introduction,Boards,Software and Documentation	<a href="#">Link</a>	-
	Contact Support	QA Ticket	Technical Q&A	<a href="#">Link</a>	-

  

Step	Introduction	Contents Type	Contents Name	RZ/A3UL Link	Note
	RZ Family Brochure	Document	RZ Family Microprocessors	<a href="#">Link</a>	-
	Flyer	Document	RENESAS RZ/A3UL GROUP	<a href="#">Link</a>	-
	Demo Video	Video	GuilianiSDK for RZA3UL Tutorial - How to run	<a href="#">Link</a>	-
	Demo Video	Video	RZ/A Flexible Software Package - Getting Started for RZ/A3UL	<a href="#">Link</a>	-
	Demo Video	Video	RZ/A3UL Display demonstration with Free GUI tool( just 1 minute)	<a href="#">Link</a>	-
	Demo Video	Video	RZ/A FSP Azure RTOS Project - Getting Started	<a href="#">Link</a>	-

  

Step	Launch of board and evaluation environment	Contents Type	Contents Name	RZ/A3UL Link	Note
1	User's Manual	Document	User's Manual: Hardware	<a href="#">Link</a>	-
		Document	Technical Update(Support Interface for SD channel 1)	<a href="#">Link</a>	-
2	How to start up the board	Document	SMARC Module Board User's Manual	<a href="#">Link</a>	-
		Document	SMARC Carrier Board User's Manual	<a href="#">Link</a>	-
3	Board information / how to get	Info/purchase	Evaluation Kit web page	<a href="#">Link</a>	-
4	Evaluation environment start-up method	web page	RZ/A Start-up	<a href="#">Link</a>	SLA*2
		web page	RZ/A3UL Software Package	<a href="#">Link</a>	SLA*2
		Document	RZ/A3UL Release Note for RZ/A Flexible Software Package	<a href="#">Link</a>	-
		Document	RZ/A3UL Getting Started with RZ/A Flexible Software Package	<a href="#">Link</a>	-
		Document	RZ/A3UL Release Note for RZ/A Initial Program Loader	<a href="#">Link</a>	-
5	How to get various software	Document	RZ/A3UL EVK Example Project Bundle	<a href="#">Link</a>	-
		Software	RZ/A FSP(Flexible Software Package) Packas	<a href="#">Link</a>	SLA*2
6	How to get various tools	Software	RZ/A3UL EVK Example Project Bundle	<a href="#">Link</a>	Disclaimer*3
		Tool	e <sup>2</sup> studio (IDE)	<a href="#">Link</a>	SLA*2
7	How to launch various tools	Tool	GUI development tools (Guiliani EvalKits)	<a href="#">Link</a>	-
		Document	Release Note for e <sup>2</sup> studio	<a href="#">Link</a>	-

  

Step	Custom board development	Contents Type	Contents Name	RZ/A3UL Link	Note
	PCB design checklist when designing a custom board	Document	Check List for PCB Design	<a href="#">Link</a>	-
	PCB Design Guide for High Speed Serial Interfaces	Document	PCB Design Guide for Serial Interface	<a href="#">Link</a>	-
	PCB Design Guide for DDR memory, DDR Config Generation Tool	Document, Tool	PCB Design Guide for DDR memory, DDR Config Generation Tool	<a href="#">Link</a>	-
	PCB Verification Guide for Core	Document	PCB Verification Guide for Core	<a href="#">Link</a>	-
	PCB Verification Guide for DDR memory	Document, Model	PCB Verification Guide for DDR memory	<a href="#">Link</a>	-
	RZ/G2UL Type-1, RZ/A3UL and RZ/Five Pin Compatibility Guide	Document	RZ/G2UL Type-1, RZ/A3UL and RZ/Five Pin Compatibility Guide	<a href="#">Link</a>	-
	Reference Desing Data (SMARC/QSPI Edition)	Design Data	Reference Desing Data(Schamtics,Layout,BOM)	<a href="#">Link</a>	Disclaimer*3
	Reference Desing Data (SMARC/OCTAL Edition)	Design Data	Reference Desing Data(Schamtics,Layout,BOM)	<a href="#">Link</a>	Disclaimer*3
	Reference Desing Data (Carrier Board)	Design Data	Reference Desing Data(Schamtics,Layout,BOM)	<a href="#">Link</a>	Disclaimer*3
	IBIS(For LSI)	Model	IBIS	<a href="#">Link</a>	Disclaimer*3
	Boundary Scan Description Language file	File	BSDL	<a href="#">Link</a>	Disclaimer*3
	Power Delivery Network model	Model	PDN	<a href="#">Link</a>	Disclaimer*3
	Mecanical Handling Guideline	Document	Mecanical Handling Guideline	<a href="#">Link</a>	-
	Thermal Management Guide	Document	Thermal Management Guide	<a href="#">Link</a>	-
	Reference Power consumption	Document	Reference Power consumption	<a href="#">Link</a>	-
	Reflow Soldering conditions	Document	Reflow Soldering conditions	<a href="#">Link</a>	-
	Baking conditions	Document	Baking conditions	<a href="#">Link</a>	-
	Lifetime estimation guide	Document	Lifetime estimation guide	<a href="#">Link</a>	-
	Power Management IC on EVK	Device Info	Power Management IC	<a href="#">Link</a>	-
	Clock Generator IC on EVK	Device Info	Clock Generator IC	<a href="#">Link</a>	-
	Semiconductor Package Mount Manual	Document	Semiconductor Package Mount Manual	<a href="#">Link</a>	-
	Reliability report	Document	Reliability report	<a href="#">Link</a>	-
	Winning combinations/ RTOS-Based RZ/A3UL HMI SMARC SoM	web page	RTOS-Based RZ/A3UL HMI SMARC SoM	<a href="#">Link</a>	-
	Winning combinations/ IoT Communications Gateway Hub	web page	IoT Communications Gateway Hub	<a href="#">Link</a>	-

\*1: Letter of Acknowledgement

\*2: Software Lisence Agreement

\*3: Disclaimer Letter