

RZ/A3UL Easy Download Guide

This document regroups all links necessary for the user to develop an application

Mouseover 'L' in 'Link'

Step	Information for engineers	Contents Type	Contents Name	RZ/A3UL Link	Note
	RZ/A Series (Wiki)	Link summary	Introduction,Boards,Software and Documentation	Link	-
	Contact Support	QA Ticket	Technical Q&A	Link	-

Step	Introduction	Contents Type	Contents Name	RZ/A3UL Link	Note
	RZ Family Brochure	Document	RZ Family Microprocessors	Link	-
	Flyer	Document	RENEAS RZ/A3UL GROUP	Link	-
	Demo Video	Video	GullianSDK for RZA3UL Tutorial - How to run	Link	-
	Demo Video	Video	RZ/A Flexible Software Package - Getting Started for RZ/A3UL	Link	-
	Demo Video	Video	RZ/A3UL Display demonstration with Free GUI tool(just 1 minute)	Link	-
	Demo Video	Video	RZ/A FSP Azure RTOS Project - Getting Started	Link	-

Step	Launch of board and evaluation environment	Contents Type	Contents Name	RZ/A3UL Link	Note
1	User's Manual	Document	User's Manual: Hardware	Link	-
2	How to start up the board	Document	SMARC Module Board User's Manual	Link	-
		Document	SMARC Carrier Board User's Manual	Link	-
3	Board information / how to get	Info/purchase	Evaluation Kit web page	Link	-
4	Evaluation environment start-up method	web page	RZ/A Start-up	Link	SLA*2
		web page	RZ/A3UL Software Package	Link	SLA*2
		Document	RZ/A3UL Release Note for RZ/A Flexible Software Package	Link	-
		Document	RZ/A3UL Getting Started with RZ/A Flexible Software Package	Link	-
		Document	RZ/A3UL Release Note for RZ/A Initial Program Loader	Link	-
		Document	RZ/A3UL EVK Example Project Bundle	Link	-
5	How to get various software	Software	RZ/A FSP(Flexible Software Package) Packas	Link	SLA*2
		Software	RZ/A3UL EVK Example Project Bundle	Link	Disclaimer*3
6	How to get various tools	Tool	e ² studio (IDE)	Link	SLA*2
		Tool	GUI development tools (Gulliani EvalKits)	Link	-
7	How to launch various tools	Document	Release Note for e ² studio	Link	-

Step	Custom board development	Contents Type	Contents Name	RZ/A3UL Link	Note
	PCB design checklist when designing a custom board	Document	Check List for PCB Design	Link	-
	PCB Design Guide for High Speed Serial Interfaces	Document	PCB Design Guide for Serial Interface	Link	-
	PCB Design Guide for DDR memory, DDR Config Generation Tool	Document, Tool	PCB Design Guide for DDR memory, DDR Config Generation Tool	Link	-
	PCB Verification Guide for Core	Document	PCB Verification Guide for Core	Link	-
	PCB Verification Guide for DDR memory	Document, Model	PCB Verification Guide for DDR memory	Link	-
	RZ/GZUL Type-1, RZ/A3UL and RZ/Five Pin Compatibility Guide	Document	RZ/GZUL Type-1, RZ/A3UL and RZ/Five Pin Compatibility Guide	Link	-
	Reference Desing Data (SMARC/QSPI Edition)	Design Data	Reference Desing Data(Schamtics,Layout,BOM)	Link	Disclaimer*3
	Reference Desing Data (SMARC/OCTAL Edition)	Design Data	Reference Desing Data(Schamtics,Layout,BOM)	Link	Disclaimer*3
	Reference Desing Data (Carrier Board)	Design Data	Reference Desing Data(Schamtics,Layout,BOM)	Link	Disclaimer*3
	IBIS(For LSI)	Model	IBIS	Link	Disclaimer*3
	Boundary Scan Description Language file	File	BSDL	Link	Disclaimer*3
	Power Delivery Network model	Model	PDN	Link	Disclaimer*3
	Mechanical Handling Guideline	Document	Mechanical Handling Guideline	Link	-
	Thermal Management Guide	Document	Thermal Management Guide	Link	-
	Reference Power consumption	Document	Reference Power consumption	Link	-
	Reflow Soldering conditions	Document	Reflow Soldering conditions	Link	-
	Baking conditions	Document	Baking conditions	Link	-
	Lifetime estimation guide	Document	Lifetime estimation guide	Link	-
	Power Management IC on EVK	Device Info	Power Management IC	Link	-
	Clock Generator IC on EVK	Device Info	Clock Generator IC	Link	-
	Semiconductor Package Mount Manual	Document	Semiconductor Package Mount Manual	Link	-
	Reliability report	Document	Reliability report	Link	-
	Winning combinations/ RTOS-Based RZ/A3UL HMI SMARC SoM	web page	RTOS-Based RZ/A3UL HMI SMARC SoM	Link	-
	Winning combinations/ IoT Communications Gateway Hub	web page	IoT Communications Gateway Hub	Link	-

*1: Letter of Acknowledgement

*2: Software Licence Agreement

*3: Disclaimer Letter