

General Configuration

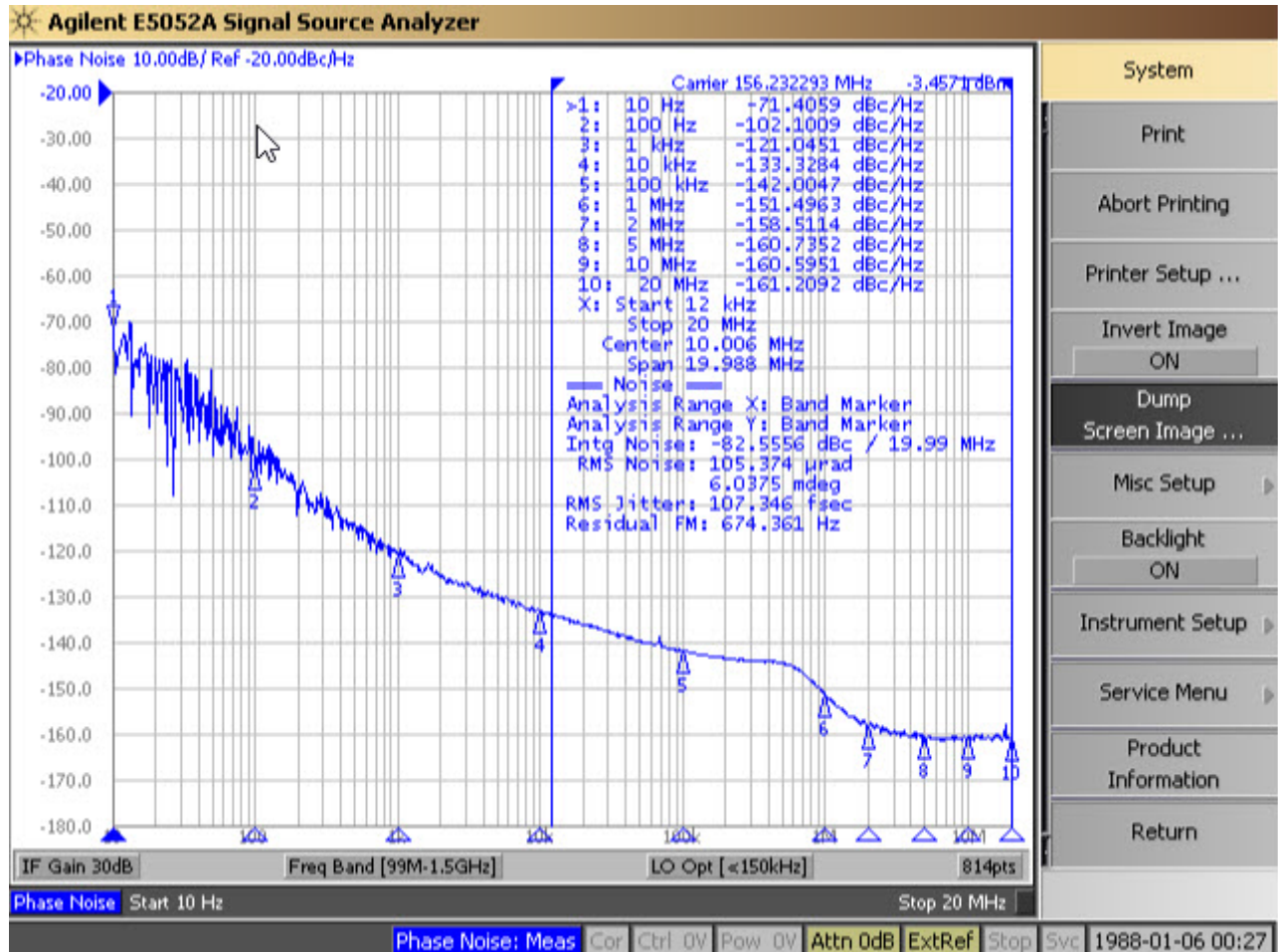
Parameter	Value
Device Version	XP version (150fs phase jitter)
Package	7.0mm × 5.0mm
Output Frequency	156.250000MHz
Frequency Stability	±50ppm
VDD Voltage	3.3V
Output Type	CML
Output Enable/Disable (E/D) Position	Pin 1
Temperature Grade	-40°C to +85°C

Pin Assignments

Number	Name	Description
1	E/D	Enable/Disable ¹ (0 = output disable).
2	NC	No connect.
3	GND	Connect to ground.
4	OUT	Output.
5	OUT2	Complementary output.
6	V _{DD}	Supply voltage.

¹ Pulled high internally.

Phase Noise Plot



Revision History

Revision Date	Description of Change
June 7, 2018	Initial release.

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(Rev.1.0 Mar 2020)

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