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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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MOS FIELD EFFECT TRANSISTOR μ PA1804

N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The $\mu PA1804$ is a switching device which can be driven directly by a 4.5 V power source.

The μ PA1804 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- Can be driven by a 4.5 V power source
- · Low on-state resistance

RDS(on)1 = 23 m Ω MAX. (Vgs = 10 V, ID = 4.0 A)

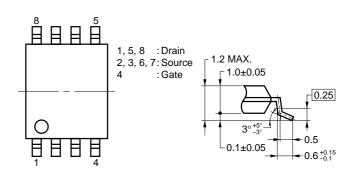
 $R_{DS(on)2} = 32 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = 4.5 \text{ V, ID} = 4.0 \text{ A)}$

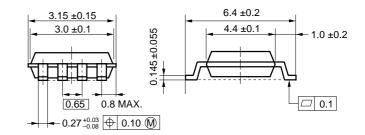
• Built-in G-S protection diode against ESD

ORDERING INFORMATION

PART NUMBER	PACKAGE
μPA1804GR-9JG	Power TSSOP8

PACKAGE DRAWING (Unit: mm)

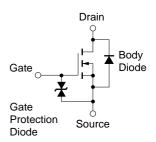




ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage	VDSS	30	V
Gate to Source Voltage	Vgss	±20	V
Drain Current (DC)	ID(DC)	±8.0	Α
Drain Current (pulse) Note1	ID(pulse)	±32	Α
Total Power Dissipation Note2	Рт	2.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

EQUIVALENT CIRCUIT



Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Mounted on ceramic substrate of 5000 mm² x 1.1 mm

Remark

The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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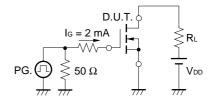
ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Ipss	Vps = 30 V, Vgs = 0 V			10	μΑ
Gate Leakage Current	lgss	Vgs = ±16 V, Vps = 0 V			±10	μΑ
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.0	2.1	2.5	V
Forward Transfer Admittance	yfs	V _{DS} = 10 V, I _D = 4.0 A	3	8.7		S
Drain to Source On-state Resistance	RDS(on)1	Vgs = 10 V, ID = 4.0 A		18	23	mΩ
	RDS(on)2	V _G S = 4.5 V, I _D = 4.0 A		24	32	mΩ
Input Capacitance	Ciss	Vps = 10 V		761		pF
Output Capacitance	Coss	V _G S = 0 V		258		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		99		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 15 V		24		ns
Rise Time	tr	ID = 4.0 A		83		ns
Turn-off Delay Time	td(off)	V _{GS(on)} = 10 V		46		ns
Fall Time	tf	$R_G = 10 \Omega$		29		ns
Total Gate Charge	Q _G	Vps = 24 V		13.5		nC
Gate to Source Charge	Qgs	ID = 8.0 A		2.4		nC
Gate to Drain Charge	Q _{GD}	V _{GS} = 10 V		3.7		nC
Diode Forward Voltage	V _F (S-D)	IF = 8.0 A, Vgs = 0 V		0.86		V
Reverse Recovery Time	trr	IF = 8.0 A, Vgs = 0 V		27		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		16		nC

TEST CIRCUIT 1 SWITCHING TIME

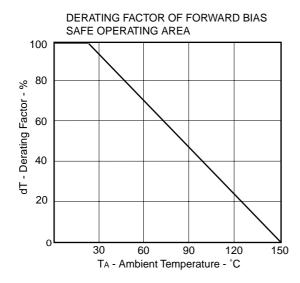
PG. $\bigcap_{RG} R_G = 10 \ \Omega$ $V_{GS} \bigvee_{Wave Form} V_{GS} \bigvee_{Wave Form} V_{GS(on)} \bigvee_{VGS(on)} 90 \%$ $V_{GS} \bigvee_{Wave Form} V_{GS(on)} \bigvee_{VGS(on)} V_{GS(on)} \bigvee_{VG$

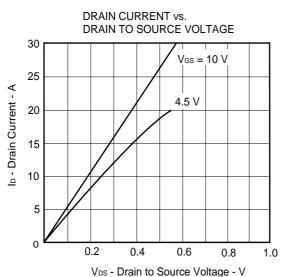
TEST CIRCUIT 2 GATE CHARGE

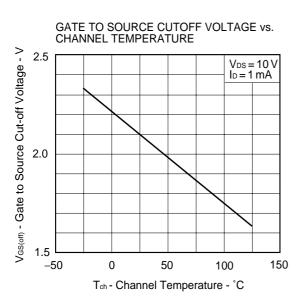


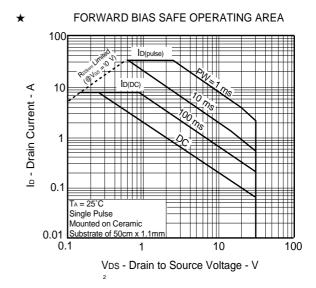


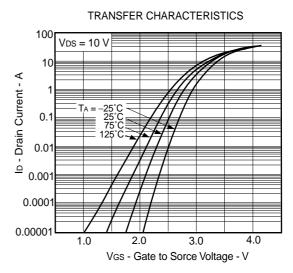
TYPICAL CHARACTERISTICS (TA = 25°C)

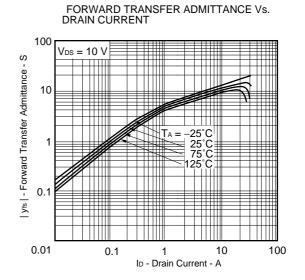




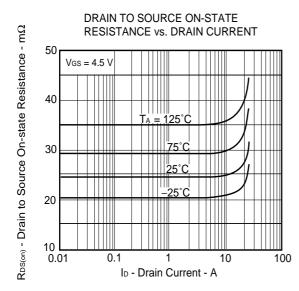


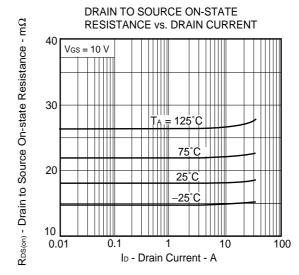


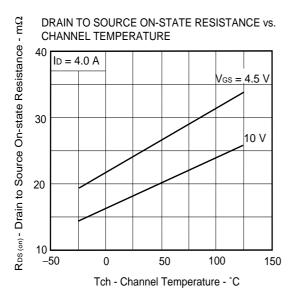


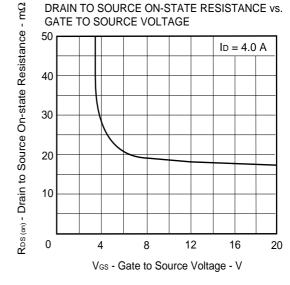


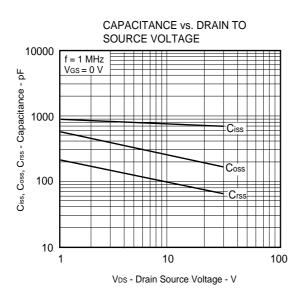
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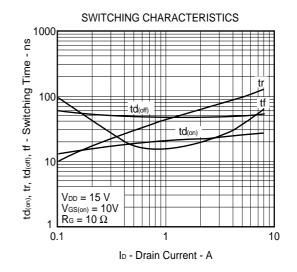


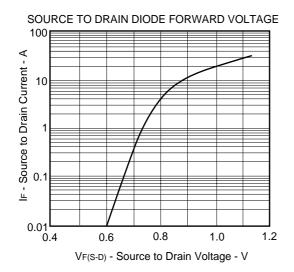


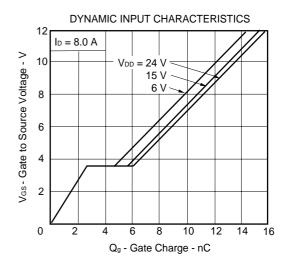


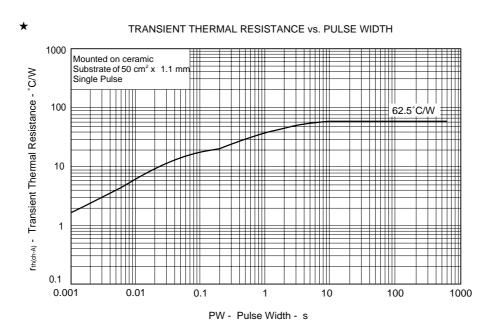












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[MEMO]

NEC μ PA1804

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