

# Dual 4.5 A Load Switch with Reverse Current Blocking

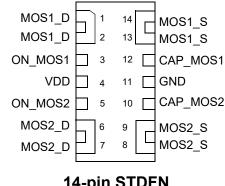
### **General Description**

The SLG59M1606V is designed for load switching application. The part comes with two 4.5 A rated MOSFETs switched on by two ON control pins. Each MOSFETs turn on time is independently adjusted by an external capacitor.

#### **Features**

- Two 4.5 A independent MOSFETs with Reverse Current Blocking
- · Two Integrated VGS Charge Pumps
- · Independent Ramp Control
- · Protected by thermal shutdown
- Pb-Free / RoHS Compliant
- · Halogen-Free
- STDFN 14L, 1 x 3 x 0.55 mm

### **Pin Configuration**

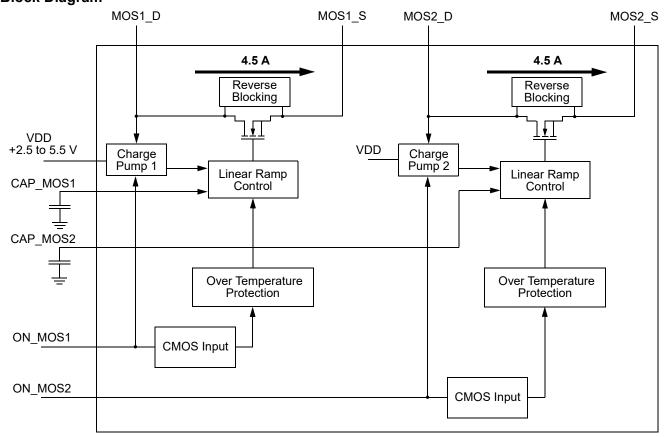


**14-pin STDFN** (Top View)

### **Applications**

- Ideal for switching ON and OFF S0 +5.0 and 3.3V power rails with associated support circuitry
- · Ideal for switching ON and OFF power rails 5 V or less.
- Can use either channel up to 5.5 A with combined maximum current of 8.5 A
- Maximum load capacitance of 1000 μF for each Channel Source terminal.

### **Block Diagram**







# **Pin Description**

Pin#	Pin Name	Туре	Pin Description
1	MOS1_D	MOSFET	Drain of MOSFET1
2	MOS1_D	MOSFET	Drain of MOSFET1 (fused with pin 1)
3	ON_MOS1	Input	Turns on MOS1 (4 MΩ pull down resistor)
4	VDD	VDD	+5VDD Power
5	ON_MOS2	Input	Turns on MOS2 (4 MΩ pull down resistor)
6	MOS2_D	MOSFET	Drain of MOSFET2
7	MOS2_D	MOSFET	Drain of MOSFET2 (fused with pin 6)
8	MOS2_S	MOSFET	Source of MOSFET2 (fused with pin 9)
9	MOS2_S	MOSFET	Source of MOSFET2
10	CAP_MOS2	Input	Sets ramp and turn on time for MOSFET2
11	GND	GND	Ground
12	CAP_MOS1	Input	Sets ramp and turn on time for MOSFET1
13	MOS1_S	MOSFET	Source of MOSFET1 (fused with pin 14)
14	MOS1_S	MOSFET	Source of MOSFET1

# **Ordering Information**

Part Number	Туре	Production Flow
SLG59M1606V	STDFN 14L	Industrial, -40 °C to 85 °C
SLG59M1606VTR	STDFN 14L (Tape and Reel)	Industrial, -40 °C to 85 °C

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### **Absolute Maximum Ratings**

Parameter	Description	Conditions		Тур.	Max.	Unit
V <sub>D</sub>	Power Supply				6	V
T <sub>S</sub>	Storage Temperature		-65		150	°C
ESD <sub>HBM</sub>	ESD Protection	Human Body Model	2000			V
W <sub>DIS</sub>	Package Power Dissipation			-	1.2	W
IDS <sub>MAX</sub>	Max Operating Current				4.5	Α
MOSFET IDS <sub>PK</sub>	Peak Current from Drain to Source	For no more than 10 continuous seconds out of every 100 seconds			6	А

Note: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

### **Electrical Characteristics**

 $T_A$  = -40 °C to 85 °C (unless otherwise stated)

Parameter	Description	Conditions	Min.	Тур.	Max.	Unit
$V_{DD}$	Power Supply Voltage		2.5		5.5	V
	Power Supply Current when OFF			0.1	1	μΑ
I <sub>DD</sub>	Power Supply Current ON_MOS_1 & ON_MOS_2 (Steady State)			50	100	μА
		T <sub>A</sub> 25°C MOSFET1 @100 mA		16.0	19.8	mΩ
		T <sub>A</sub> 70°C MOSFET1 @100 mA		18.7	24.2	mΩ
DDC	ON Periotopes	T <sub>A</sub> 85°C MOSFET1 @100 mA		19.8	25.3	mΩ
RDS <sub>ON</sub>	ON Resistance	T <sub>A</sub> 25°C MOSFET2 @100 mA		16.0	19.8	mΩ
		T <sub>A</sub> 70°C MOSFET2 @100 mA		18.7	24.2	mΩ
		T <sub>A</sub> 85°C MOSFET2 @100 mA		19.8	25.3	mΩ
MOSFET IDS	Current from Drain to Source for each MOSFET	Continuous, each channel			4.5	Α
IDC	IDS Leakage	$V_S$ = 1.0 V to 5.0 V, $V_{DD}$ = $V_D$ = 0 V, ON_MOS = LOW, 0 to 85 °C, each channel		0.5	1.5	μΑ
IDS <sub>LKG</sub>	(Reverse Blocking enabled)	$V_S$ = 1.0 V to 5.0 V, $V_{DD}$ = $V_D$ = 0 V, ON_MOS = LOW, -40 to 0 °C, each channel		3	5	μА
$V_{D}$	Drain Voltage		0.85	5.0	$V_{DD}$	V
T <sub>ON_Delay</sub>	ON pin Delay Time	50% ON to Ramp Begin, R <sub>L</sub> = 20 $\Omega$ , C <sub>L</sub> = 10 $\mu$ F	0	300	500	μs
		50% ON to 90% V <sub>S</sub>	Co	onfigurable	e <sup>1</sup>	ms
T <sub>Total_ON</sub>	Total Turn On Time	Example: CAP = 4 nF, $V_{DD} = V_D = 5$ V, Source_Cap = 10 $\mu$ F, $R_L = 20 \Omega$		2.0		ms
		10% V <sub>S</sub> to 90% V <sub>S</sub>	Co	onfigurable	e <sup>1</sup>	V/ms
T <sub>SLEWRATE</sub>	Slew Rate	Example: CAP = 4 nF, $V_{DD} = V_D = 5$ V, Source_Cap = 10 $\mu$ F, $R_L = 20 \Omega$		3.0		V/ms
CAP <sub>SOURCE</sub>	Source Cap	Source to GND			1000	μF
ON_V <sub>IH</sub>	High Input Voltage on ON pin		0.85		V <sub>DD</sub>	V
ON_V <sub>IL</sub>	Low Input Voltage on ON pin		-0.3	0	0.3	V
THERM <sub>ON</sub> <sup>2</sup>	Thermal shutoff turn-on temperature			125		°C

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# **SLG59M1606V**

### $T_A$ = -40 °C to 85 °C (unless otherwise stated)

Parameter	Description	Conditions	Min.	Тур.	Max.	Unit
THERM <sub>OFF</sub>	Thermal shutoff turn-off temperature			100		°C
THERM <sub>TIME</sub>	Thermal shutoff time				1	ms
T <sub>OFF_Delay</sub>	OFF Delay Time	50% ON to $V_S$ Fall, $V_{DD}$ = $V_D$ = 5 V, $R_L$ = 20 $\Omega$ , no $C_L$			15	μs

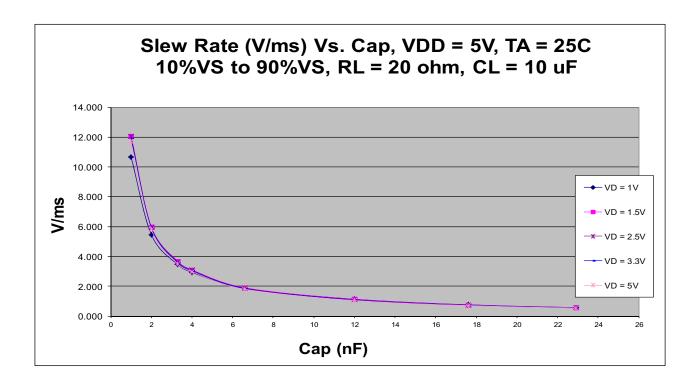
### Notes:

- 1. Refer to table for configuration details.
- 2. When device enters thermal shutdown, both channels will turn off.

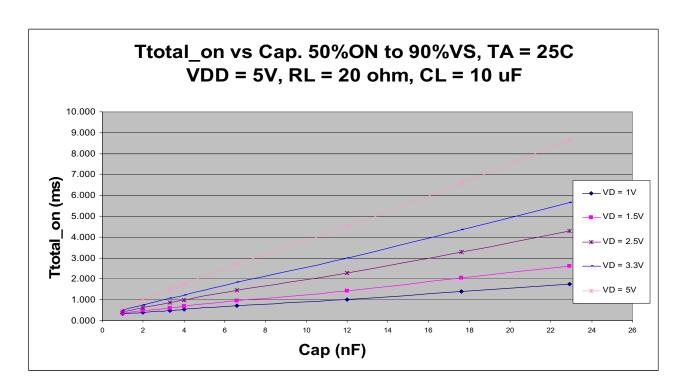
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T<sub>SLEW</sub> vs. CAP



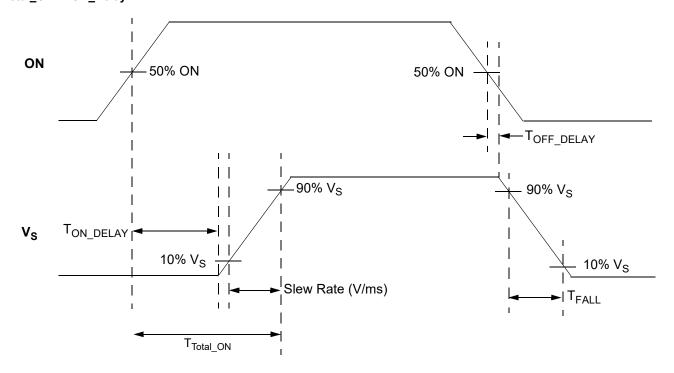
 $T_{TOTAL\_ON}$  vs. CAP



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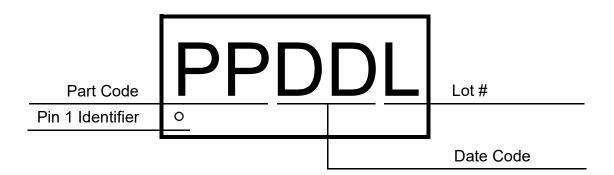
# $\rm T_{Total\_ON}, \rm T_{ON\_Delay}$ and Slew Rate Measurement



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## **Package Top Marking System Definition**

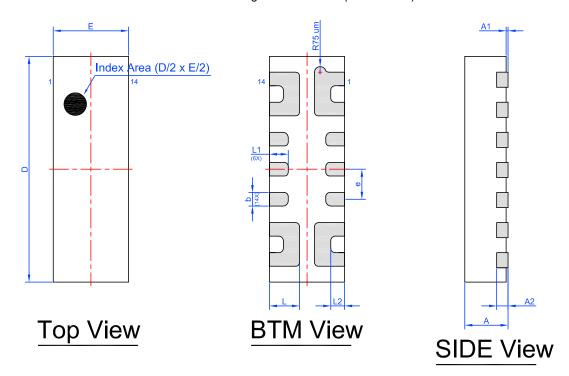


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## **Package Drawing and Dimensions**

14 Lead STDFN Package 1 mm x 3 mm (Fused Lead)



# Unit: mm

Symbol	Min	Nom.	Max	Symbol	Min	Nom.	Max
Α	0.50	0.55	0.60	D	2.95	3.00	3.05
A1	0.005	-	0.050	Е	0.95	1.00	1.05
A2	0.10	0.15	0.20	L	0.35	0.40	0.45
b	0.13	0.18	0.23	L1	0.20	0.25	0.30
е	(	0.40 BSC	,	L2	0.06	0.11	0.16

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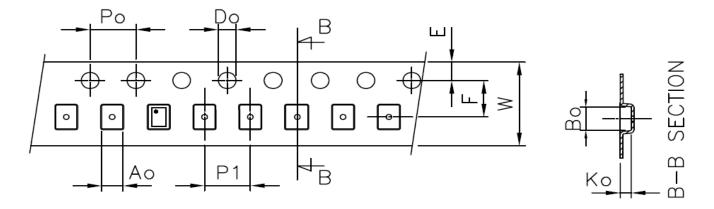


# **Tape and Reel Specifications**

Package	# of	Nominal	Unitsper	Max	Reel &	-	er A	Lead	ler B	Pocket Ta	ape (mm)
Type	Pins	Package Size	Reel		Hub Size (mm)	Pockets	Length (mm)	Pockets	Length (mm)	Width	Pitch
STDFN 14L	14	1x3x0.55mm	3000	3000	178/60	100	400	100	400	8	4

# **Carrier Tape Drawing and Dimensions**

Package Type	PocketBTM Length [mm]	PocketBTM Width [mm]	Pocket Depth [mm]	Index Hole Pitch [mm]	Pocket Pitch [mm]	Index Hole Diameter [mm]	Index Hole to Tape Edge [mm]		Tape Width [mm]
	A0	В0	K0	P0	P1	D0	E	F	W
STDFN 14L	1.15	3.15	0.7	4	4	1.5	1.75	3.5	8



# **Recommended Reflow Soldering Profile**

Please see IPC/JEDEC J-STD-020: latest revision for reflow profile based on package volume of 1.65 mm<sup>3</sup> (nominal). More information can be found at www.jedec.org.

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# **Revision History**

Date	Version	Change
2/4/2022	1.02	Updated Company name and logo Fixed typos
10/8/2015	1.01	Updated Block Diagram Updated VD min to 0.85 V
9/9/2015	1.00	Production Release Updated Electrical Characteristics conditions

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