

# Dual 4.5A GreenFET Load Switch

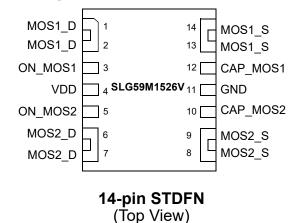
## **General Description**

The SLG59M1526V is designed for load switching application. The part comes with two 4.5A rated MOSFETs switched on by two ON control pins. Each MOSFETs turn on time is independently adjusted by an external capacitor.

## Features

- Two 4.5A independent MOSFETs
- Two Integrated VGS Charge Pumps
- Two internal discharges per channel for gate and source
- Independent Ramp Control
- · Protected by thermal shutdown with current limit
- Pb-Free / RoHS Compliant
- · Halogen-Free
- STDFN 14L, 2 x 3 x 0.55 mm

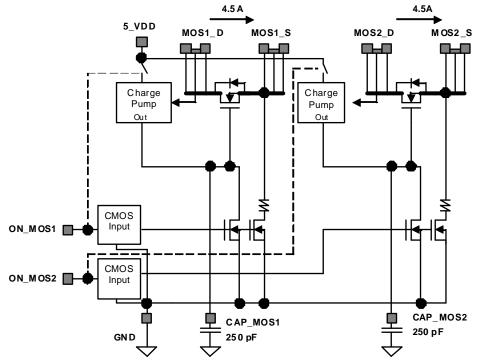
## Pin Configuration



Applications

- Ideal for switching ON and OFF S0 +5.0 and 3.3 V power rails with associated support circuitry discharges.
- Ideal for switching ON and OFF power rails 5 V or less.
- · Can use either channel up to 5.5 A with combined maximum current of 8.5 A
- Maximum load capacitance of 1000 μF for each Channel Source terminal.

## **Block Diagram**



Do not probe CAP\_MOS1 (PIN 12) or CAP\_MOS2 (PIN 10) with low impedance probe.



## **Pin Description**

Pin #	Pin Name	Туре	Pin Description
1	MOS1_D	MOSFET	Drain of MOSFET1
2	MOS1_D	MOSFET	Drain of MOSFET1 (fused with pin 1)
3	ON_MOS1	Input	Turns on MOS1 (4 M $\Omega$ pull down resistor)
4	VDD	VDD	+5VDD Power
5	ON_MOS2	Input	Turns on MOS2 (4 M $\Omega$ pull down resistor)
6	MOS2_D	MOSFET	Drain of MOSFET2
7	MOS2_D	MOSFET	Drain of MOSFET2 (fused with pin 6)
8	MOS2_S	MOSFET	Source of MOSFET2 (fused with pin 9)
9	MOS2_S	MOSFET	Source of MOSFET2
10	CAP_MOS2	Input	Sets ramp and turn on time for MOSFET2
11	GND	GND	Ground
12	CAP_MOS1	Input	Sets ramp and turn on time for MOSFET1
13	MOS1_S	MOSFET	Source of MOSFET1 (fused with pin 14)
14	MOS1_S	MOSFET	Source of MOSFET1

## **Ordering Information**

Part Number	Туре	Production Flow
SLG59M1526V	STDFN 14L	Industrial, -40 °C to 85 °C
SLG59M1526VTR	STDFN 14L (Tape and Reel)	Industrial, -40 °C to 85 °C



## **Absolute Maximum Ratings**

Parameter	Description	Conditions	Min.	Тур.	Max.	Unit	
V <sub>D</sub>	Power Supply			-	6	V	
Τ <sub>S</sub>	Storage Temperature		-65	-	150	°C	
ESD <sub>HBM</sub>	ESD Protection	Human Body Model	2000	-		V	
W <sub>DIS</sub>	Package Power Dissipation			-	1.2	W	
IDS <sub>MAX</sub>	Max Operating Current				4.5	А	
MOSFET IDS <sub>PK</sub>	Peak Current from Drain to Source	For no more than 10 continuous seconds out of every 100 seconds			6	A	
Note: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.							

### **Electrical Characteristics**

 $T_A$  = -40 °C to 85 °C (unless otherwise stated)

Parameter	Description	Conditions	Min.	Тур.	Max.	Unit
V <sub>DD</sub>	Power Supply Voltage		2.5		5.5	V
	Power Supply Current when OFF			0.1	1	μA
I <sub>DD</sub>	Power Supply Current ON_MOS_1 & ON_MOS_2 (Steady State)			50	75	μA
		T <sub>A</sub> 25°C MOSFET1 @100 mA		14.5	18	mΩ
		T <sub>A</sub> 70°C MOSFET1 @100 mA		17	22	mΩ
RDS <sub>ON</sub>	ON Resistance	T <sub>A</sub> 85°C MOSFET1 @100 mA		18	23	mΩ
KD3 <sub>ON</sub>	ON Resistance	T <sub>A</sub> 25°C MOSFET2 @100 mA		14.5	18	mΩ
		T <sub>A</sub> 70°C MOSFET2 @100 mA		17	22	mΩ
		T <sub>A</sub> 85°C MOSFET2 @100 mA		18	23	mΩ
MOSFET IDS	Current from Drain to Source for each MOSFET	Continuous			4.5	А
V <sub>D</sub>	Drain Voltage		0.9		V <sub>DD</sub>	V
T <sub>ON_Delay</sub>	ON pin Delay Time	50% ON to Ramp Begin	0	300	500	μs
		50% ON to 90% V <sub>S</sub>	Configurable <sup>1</sup>			ms
T <sub>Total_ON</sub>	Total Turn On Time	Example: CAP = 4 nF, $V_{DD} = V_D = 5$ V, Source_Cap = 10 $\mu$ F, R <sub>L</sub> = 20 $\Omega$		2.0		ms
		10% $V_{S}$ to 90% $V_{S}$	Configurable <sup>1</sup>			V/ms
T <sub>SLEWRATE</sub>	Slew Rate	Example: CAP = 4 nF, $V_{DD} = V_D = 5$ V, Source_Cap = 10 $\mu$ F, R <sub>L</sub> = 20 $\Omega$	3.0			V/ms
CAP <sub>SOURCE</sub>	Source Cap	Source to GND			1000	μF
R <sub>DIS</sub>	Discharge Resistance		100	150	300	Ω
ON_V <sub>IH</sub>	High Input Voltage on ON pin		0.85		V <sub>DD</sub>	V
$ON_V_{IL}$	Low Input Voltage on ON pin		-0.3	0	0.3	V
l <b>.</b>	Active Current Limit	MOSFET will automatically limit current when $V_S$ > 250 mV		6.0		А
I <sub>LIMIT</sub>	Short Circuit Current Limit	MOSFET will automatically limit current when $V_S$ < 250 mV		0.5		А
THERMON	Thermal shutoff turn-on temperature			125		°C
THERMOFF	Thermal shutoff turn-off temperature			100		°C

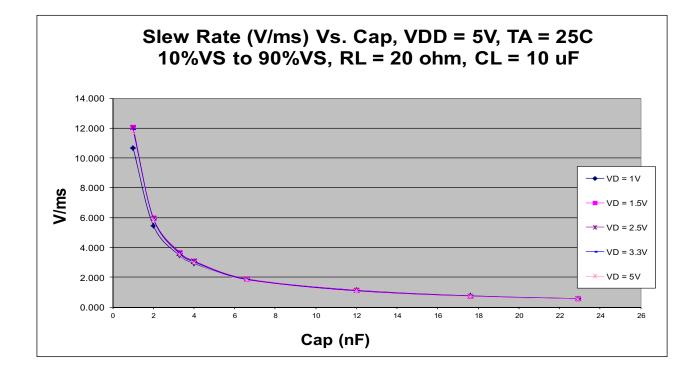


 $T_A$  = -40 °C to 85 °C (unless otherwise stated)

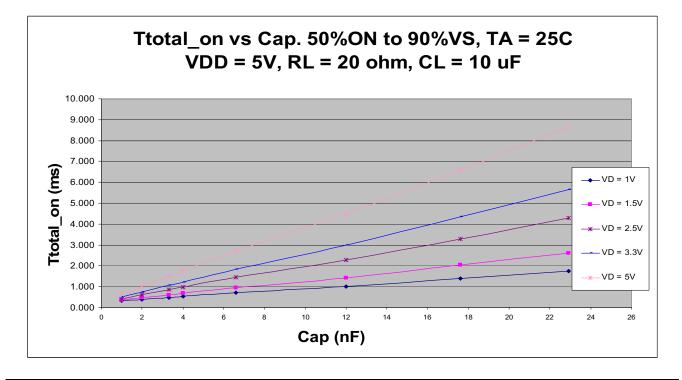
Parameter	Description	Conditions	Min.	Тур.	Max.	Unit	
THERM	Thermal shutoff time				1	ms	
T <sub>OFF_Delay</sub>	OFF Delay Time	50% ON to $V_S$ Fall, $V_{DD}$ = $V_D$ = 5 V			15	μs	
Notes: 1. Refer to table for configuration details.							



## T<sub>SLEW</sub> vs. CAP

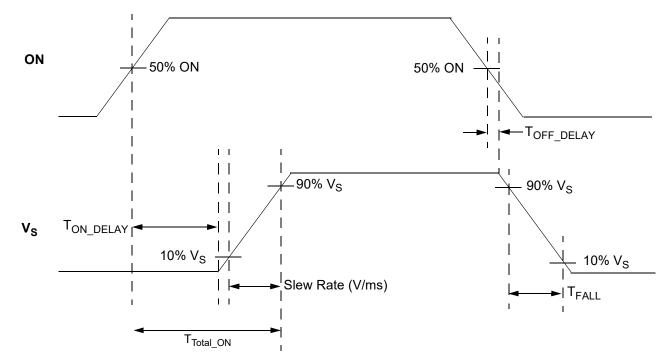


# T<sub>TOTAL\_ON</sub> vs. CAP





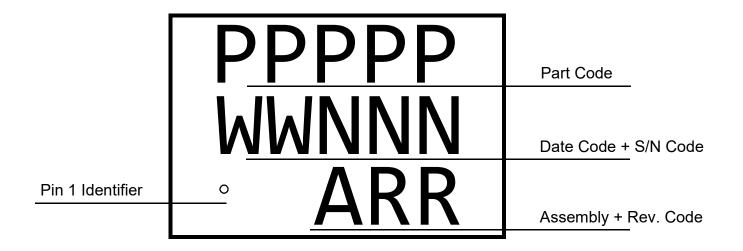
 $T_{Total\_ON},\,T_{ON\_Delay}$  and Slew Rate Measurement



More vias for thermal dissipation is better.



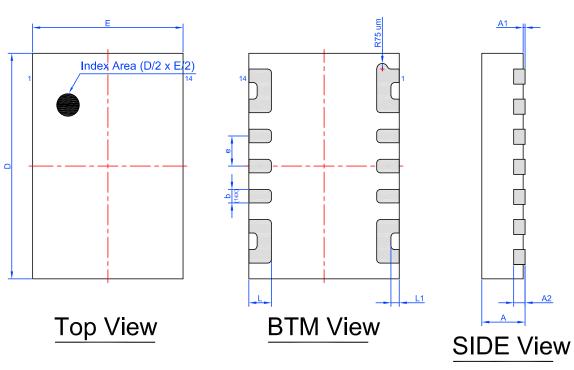
## Package Top Marking System Definition





## **Package Drawing and Dimensions**

14 Lead STDFN Package 2 mm x 3 mm (Fused Lead)



Unit: mn	n						
Symbol	Min	Nom.	Max	Symbol	Min	Nom.	Max
Α	0.50	0.55	0.60	D	2.95	3.00	3.05
A1	0.005	-	0.050	E	1.95	2.00	2.05
A2	0.10	0.15	0.20	L	0.25	0.30	0.35
b	0.13	0.18	0.23	L1	0.06	0.11	0.16
е	(	).40 BSC	•				

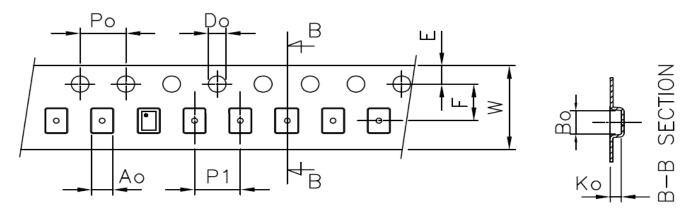


## **Tape and Reel Specifications**

Package	# of	Nominal	Nominal	Nominal	Nominal	Nominal	Nominal	Nominal				Unitsper	Max	Reel &	Trail	er A	Lead	er B	Pocket Ta	ape (mm)
Туре	Pins	Package Size	kage Pool	Units per Box	Hub Size (mm)	Pockets	Length (mm)	Pockets	Length (mm)	Width	Pitch									
STDFN 14L 2x3 mm 0.4P FC Green	14	2x3x0.55mm	3000	3000	178/60	100	400	100	400	8	4									

## **Carrier Tape Drawing and Dimensions**

Package Type	PocketBTM Length [mm]	PocketBTM Width [mm]	Pocket Depth [mm]	Index Hole Pitch [mm]	Pocket Pitch [mm]	Index Hole Diameter [mm]	Index Hole to Tape Edge [mm]	Index Hole to Pocket Center [mm]	Tape Width [mm]
	A0	B0	K0	P0	P1	D0	Е	F	w
STDFN 14L 2x3 mm 0.4P FC Green	2.2	3.15	0.76	4	4	1.5	1.75	3.5	8



## **Recommended Reflow Soldering Profile**

Please see IPC/JEDEC J-STD-020: latest revision for reflow profile based on package volume of 3.3 mm<sup>3</sup> (nominal). More information can be found at www.jedec.org.



## **Revision History**

Date	Version	Change
2/4/2022	1.03 Updated Company name and logo Fixed typos	
10/8/14	1.02	Updated VD Min from 1.0 V to 0.9 V
7/15/14	1.01	Fixed Part Marking Diagram
4/21/14	1.0	Production Release

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