

RQJ0602EGDQA

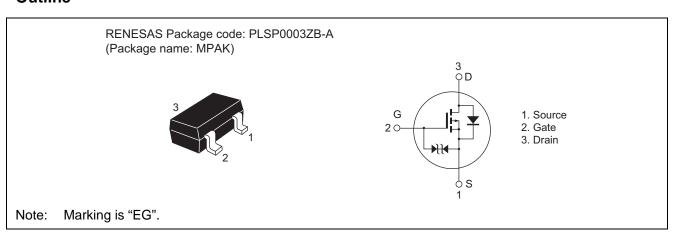
Silicon P Channel MOS FET Power Switching

R07DS0299EJ0600 Rev.6.00 Jan 10, 2014

Features

- Low on-resistance $R_{DS(on)} = 490 \text{ m}\Omega \text{ typ } (V_{GS} = -10 \text{ V}, I_D = -0.55 \text{ A})$
- Low drive current
- High speed switching
- 4.5 V gate drive

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-60	V
Gate to source voltage	V _{GSS}	+10 / -20	V
Drain current	I _D	-1.1	Α
Drain peak current	I _{D(Pulse)} Note1	-3	Α
Body - drain diode reverse drain current	I _{DR}	-1.1	Α
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. When using the glass epoxy board (FR-4: $40 \times 40 \times 1$ mm)

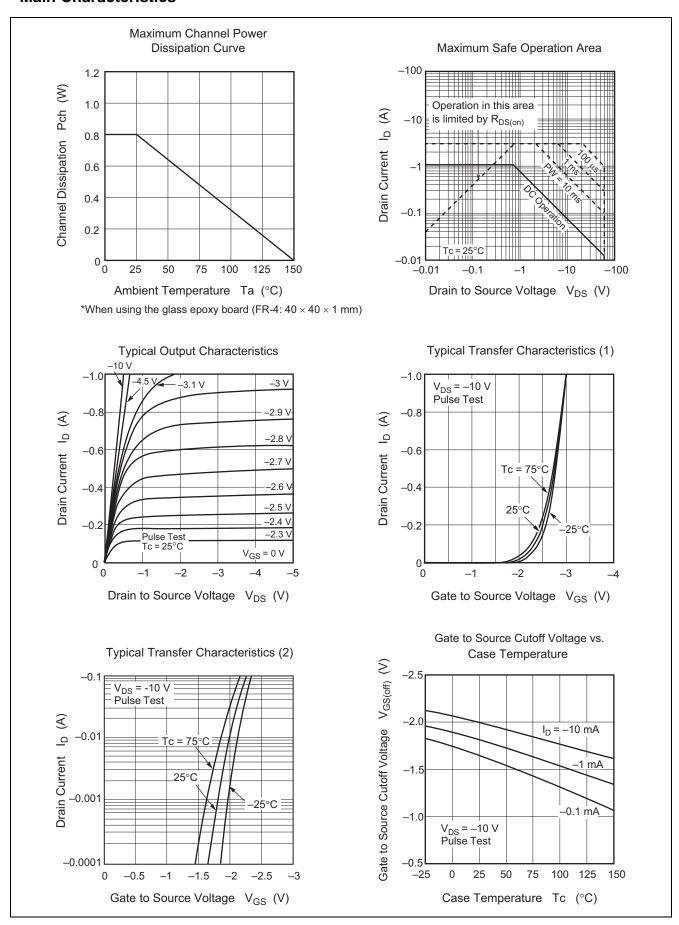
Electrical Characteristics

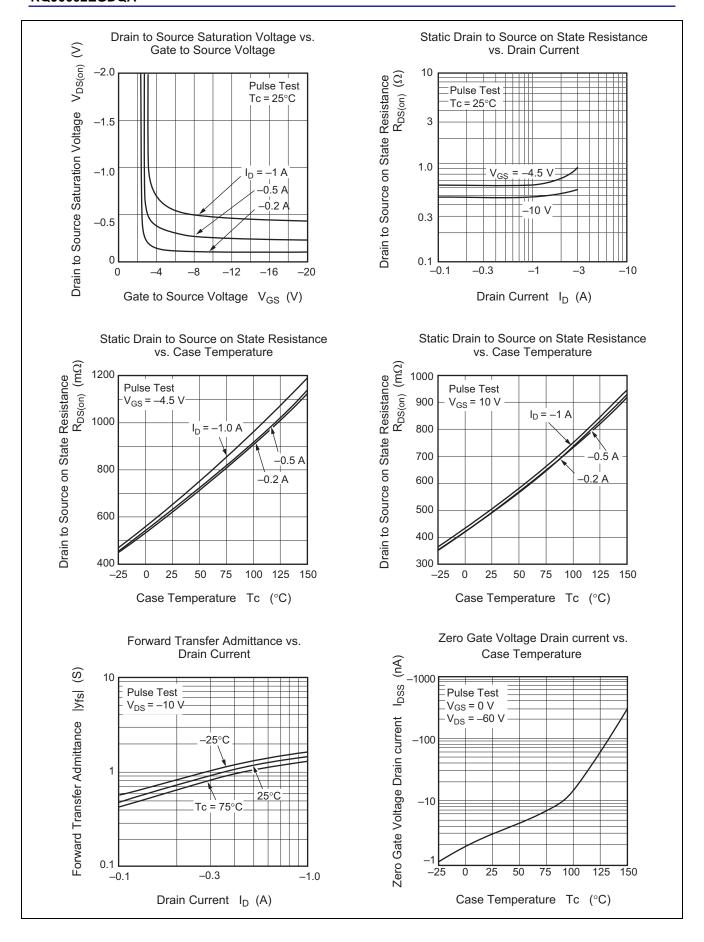
 $(Ta = 25^{\circ}C)$

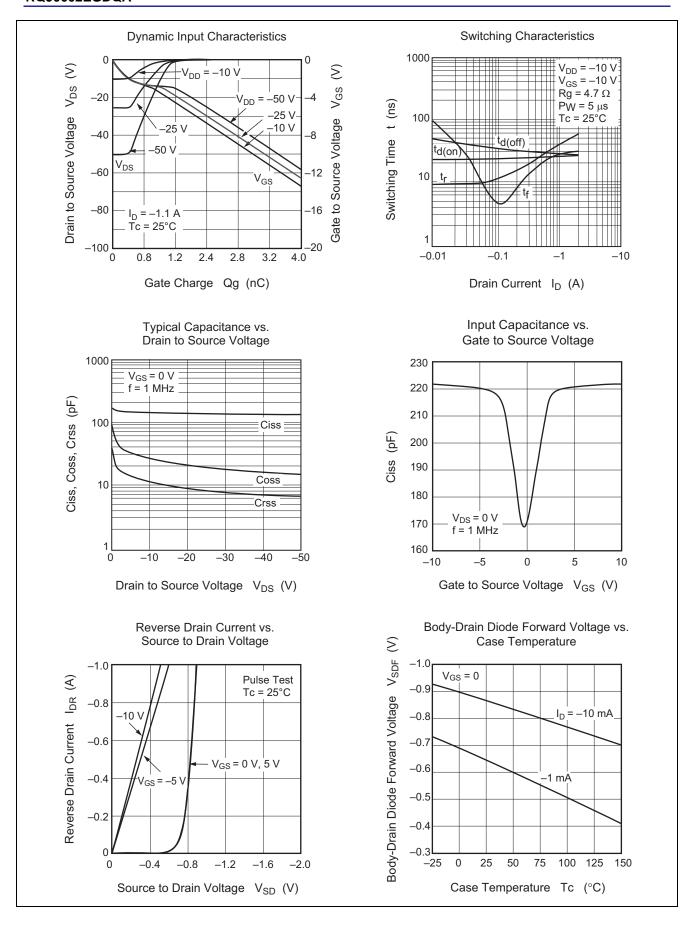
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	+10	_	_	V	$I_G = +100 \mu\text{A}, V_{DS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	-20	_	_	V	$I_G = -100 \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	+10	μΑ	$V_{GS} = +8 \text{ V}, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	-10	μΑ	$V_{GS} = -16 \text{ V}, V_{DS} = 0$
Drain to source leak current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.0	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Drain to source on state resistance	R _{DS(on)}	_	490	613	mΩ	$I_D = -0.55 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note3}}$
	R _{DS(on)}	_	613	854	mΩ	$I_D = -0.55 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y _{fs}	0.7	1.2	_	S	$I_D = -0.55 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	145	_	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	24	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	11	_	pF	
Turn - on delay time	t _{d(on)}	_	23	_	ns	$I_D = -0.5 \text{ A}, V_{GS} = -10 \text{ V},$
Rise time	t _r	_	24	_	ns	$R_L = 20 \Omega$, $Rg = 4.7 \Omega$
Turn - off delay time	t _{d(off)}	_	28	_	ns	
Fall time	t _f	_	19	_	ns	
Total gate charge	Qg	_	3.0	_	nC	$V_{DD} = -10 \text{ V}, V_{GS} = -10 \text{ V},$
Gate to source charge	Qgs	_	0.5	_	nC	$I_D = -1.1A$
Gate to drain charge	Qgd	_	0.6	_	nC	
Body - drain diode forward voltage	V_{DF}	_	-0.9	_	V	$I_F = -1.5 \text{ A}, V_{GS} = 0^{\text{Note3}}$

Notes: 3. Pulse test

Main Characteristics

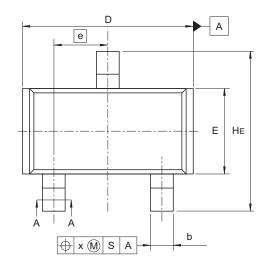


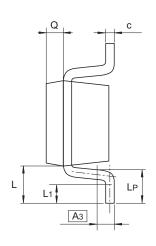


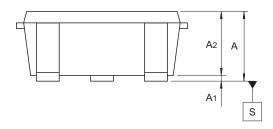


Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-59A	PLSP0003ZB-A	MPAK(T) / MPAK(T)V	0.011









Reference	Dimensions in millimeters		
Symbol	Min	Nom	Max
Α	1.0		1.3
A ₁	0		0.1
A ₂	1.0	1.1	1.2
A ₃		0.25	_
b	0.35	0.4	0.5
С	0.1	0.16	0.26
D	2.7	_	3.1
E	1.35	1.5	1.65
е	_	0.95	_
HE	2.2	2.8	3.0
L	0.35		0.75
L ₁	0.15	_	0.55
Lp	0.25		0.65
Х	_	_	0.05
Q		0.3	

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Ordering Information

Orderable Part Number	Quantity	Shipping Container
RQJ0602EGDQATL-H	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +44-1628-651-709, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd. 7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China Tel: +86-10-2035-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 LanGao Rd., Putuo District, Shanghai, China
Tel: +86-21-2226-088, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 161F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2886-9318, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei, Taiv Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd. 12F., 234 Teheran-ro, Gangnam-Gu, Seoul, 135-080, Korea Tel: +82-2-558-3737, Fax: +82-2-558-5141

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