

RJP65T43DPQ-A0

650V - 30A - IGBT Application: Power Factor Correction circuit

R07DS1376EJ0101 Rev. 1.01 Jul 11, 2017

Features

- Low collector to emitter saturation voltage $V_{CE(sat)} = 1.8 \text{ V typ.}$ (at $I_C = 20 \text{ A}$, $V_{GE} = 15 \text{ V}$, $Ta = 25^{\circ}\text{C}$)
- Trench gate and thin wafer technology (G7H series)
- High speed switching

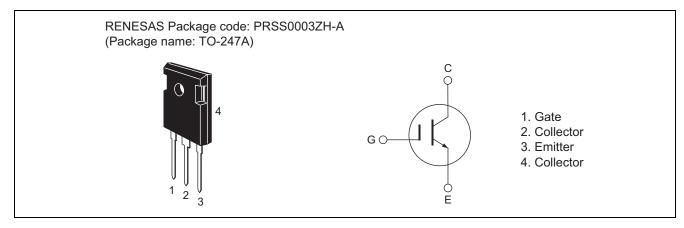
 $t_{\rm f}=45~\text{ns}$ typ. (at $V_{\rm CC}=400\rm V,\,V_{\rm GE}=15\rm V,\,IC=20\rm A,\,Rg=10\Omega,\,Ta=25^{\circ}C,\,Inductive\ load)$

- Operation frequency (20kHz ≤ f < 100kHz)
 Rating of collector current I_C = 30A (at Tc = 100°C)
- Not guarantee short circuit withstand time

Key Nominal Performance

Туре	Vces	lc	V _{CE(sat)} , Ta=25°C	Tj	Marking	Package
RJP65T43DPQ-A0	650V	20A	1.8V	175°C	RJP65T43	TO-247A

Outline



Absolute Maximum Ratings

 $(Tc = 25^{\circ}C)$

Item		Symbol	Ratings	Unit
Collector to emitter voltage		Vces	650	V
Gate to emitter voltage		V _{GES}	±30	V
Collector current	Tc = 25 °C	Ic	60	Α
	Tc = 100 °C	Ic	30	Α
Collector peak current		ic(peak) Note1	150	Α
Collector dissipation		Pc	150	W
Junction to case thermal resistance		θј-с	1.0	°C/W
Junction temperature		Tj Note2	175	°C
Storage temperature		Tstg	-55 to +150	°C

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it are within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Electrical Characteristics

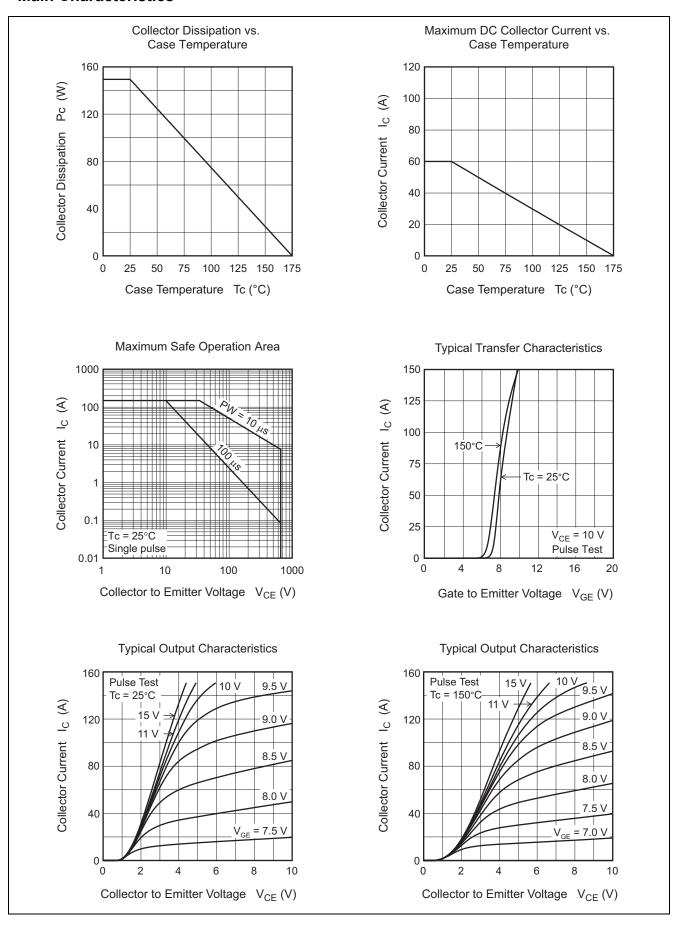
 $(Ta = 25^{\circ}C)$

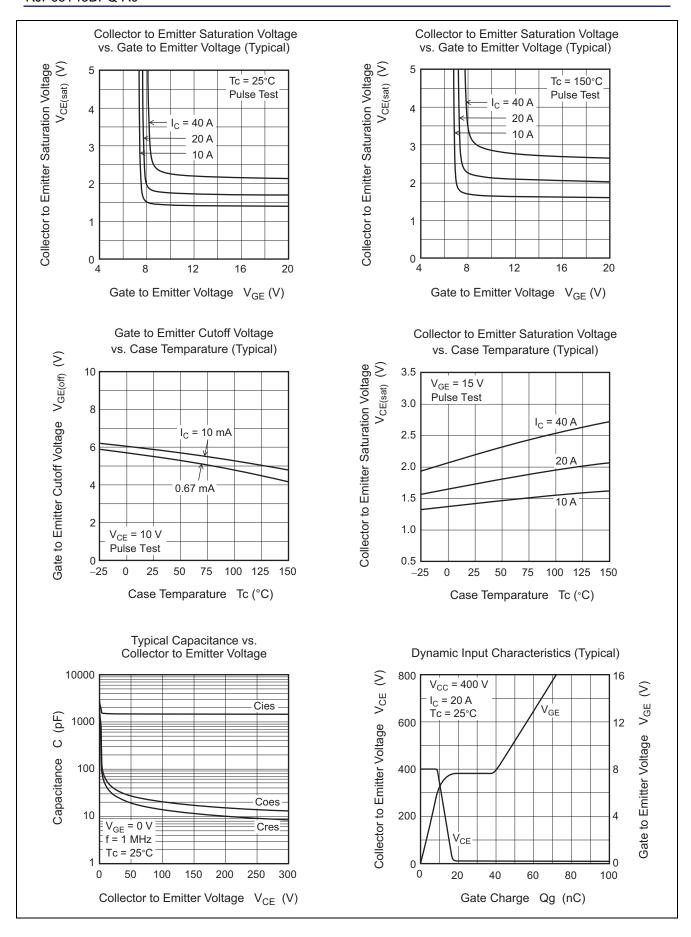
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Zero gate voltage collector current	Ices	_	_	1	μΑ	V _{CE} = 650 V, V _{GE} = 0	
Gate to emitter leak current	Iges	_		±1	μΑ	$V_{GE} = \pm 30 \text{ V}, V_{CE} = 0$	
Gate to emitter cutoff voltage	V _{GE(off)}	4.0		7.0	V	$V_{CE} = 10V, I_{C} = 0.67 \text{ mA}$	
Collector to emitter saturation voltage	V _{CE(sat)}	_	1.8	2.4	V	I _C = 20 A, V _{GE} = 15V Note3	
Total gate charge	Qg	_	69		nC	V _{CE} = 400 V	
Gate to emitter charge	Qge	_	10		nC	V _{GE} = 15V	
Gate to collector charge	Qgc	_	30		nC	Ic= 20A	
Input capacitance	Cies	_	1550		pF	V _{CE} = 25 V	
Output capacitance	Coes	_	37		pF	$V_{GE} = 0$	
Reveres transfer capacitance	Cres	_	26	_	pF	f = 1 MHz	
Turn-on delay time	t _{d(on)}	_	35		ns	V _{CC} = 400 V	
Rise time	tr	_	20	_	ns	$V_{GE} = 15 \text{ V}, I_{C} = 20 \text{ A}$	
Turn-off delay time	$t_{\sf d(off)}$	_	105	_	ns	Rg = 10 Ω , T _C = 25 °C	
Fall time	t _f	_	45	_	ns	Inductive load Note4	
Turn-on loss energy	Eon	_	0.17		mJ		
Turn-off loss energy	E _{off}	_	0.13		mJ		
Turn-on delay time	$t_{\sf d(on)}$	_	32	_	ns	V _{CC} = 400 V	
Rise time	tr	_	20	_	ns	$V_{GE} = 15 \text{ V}, I_{C} = 20 \text{ A}$	
Turn-off delay time	$t_{\sf d(off)}$	_	115	_	ns	Rg = 10 Ω, T_C = 150°C	
Fall time	t _f		45		ns	Inductive load Note4	
Turn-on loss energy	Eon		0.28		mJ		
Turn-off loss energy	E _{off}	_	0.53	_	mJ		

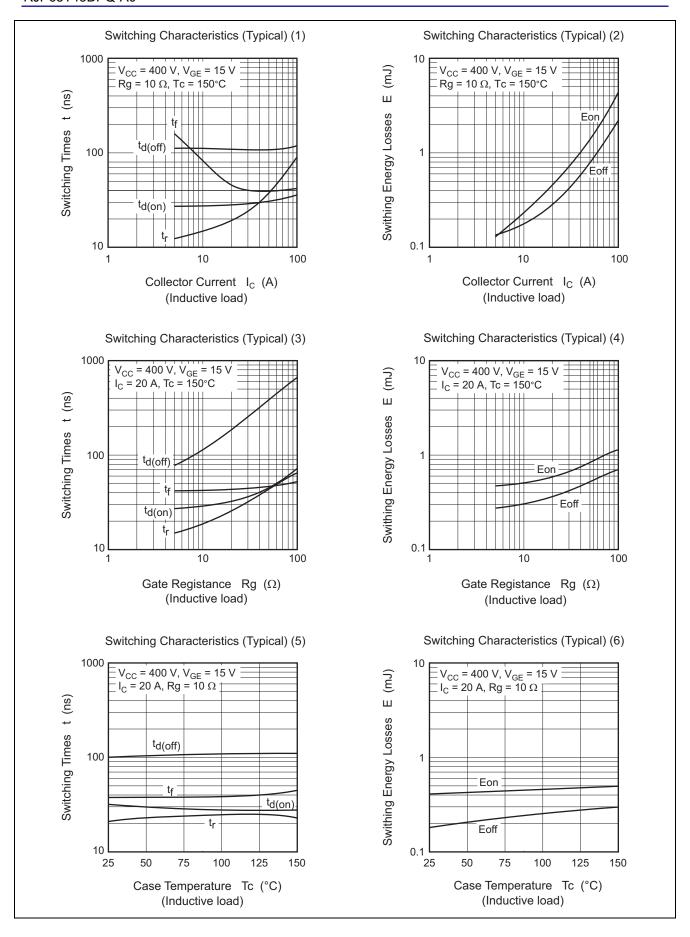
Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

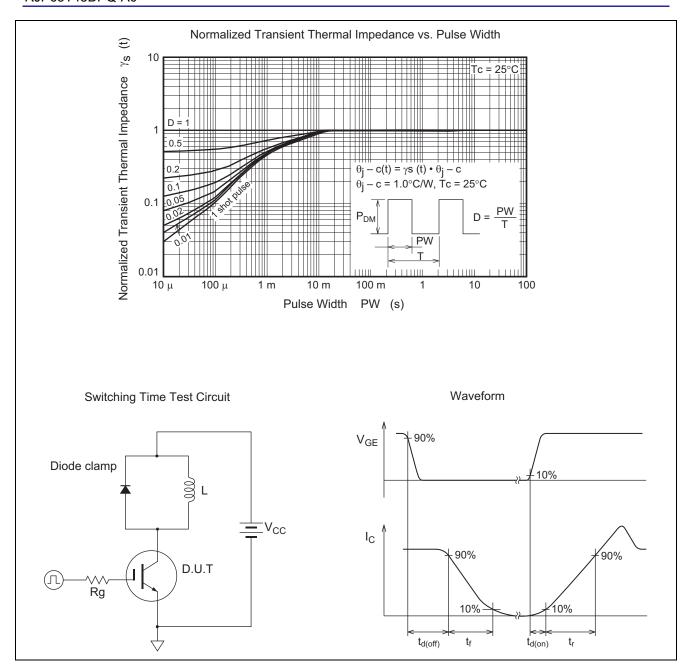
- 2. Please use this device in the thermal conditions which the junction temperature does not exceed 175°C. Renesas IGBT Application Note is disclosed about reliability test and application condition up to 175°C.
- 3. Pulse test
- 4. Switching time test circuit and waveform are shown below.

Main Characteristics

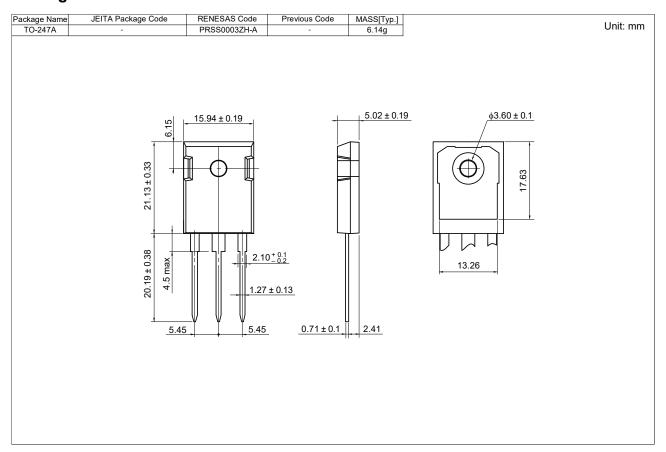








Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJP65T43DPQ-A0#T2	240 pcs	Box (Tube)

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