

# RJK5002DJE

500V - 2.4A - MOS FET  
High Speed Power Switching

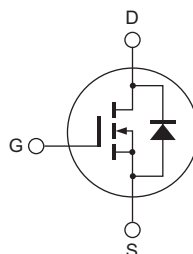
R07DS0844EJ0100  
Rev.1.00  
Jul 05, 2012

## Features

- Low on-state resistance  
 $R_{DS(on)} = 3.83 \Omega$  typ. (at  $I_D = 1.2 A$ ,  $V_{GS} = 10 V$ ,  $T_a = 25^\circ C$ )
- High speed switching

## Outline

RENESAS Package code: PRSS0003DC-A  
(Package name: TO-92 Mod)



1. Source
2. Drain
3. Gate

## Absolute Maximum Ratings

( $T_a = 25^\circ C$ )

Item	Symbol	Value	Unit
Drain to source voltage	$V_{DSS}$	500	V
Gate to source voltage	$V_{GSS}$	$\pm 30$	V
Drain current	$I_D$ <sup>Note1</sup>	2.4	A
Drain peak current	$I_{D(pulse)}$ <sup>Note3</sup>	4.8	A
Body-drain diode reverse drain current	$I_{DR}$ <sup>Note1</sup>	2.4	A
Body-drain diode reverse drain peak current	$I_{DR(pulse)}$ <sup>Note3</sup>	4.8	A
Channel dissipation	$P_{ch}$ <sup>Note 2</sup>	0.9	W
Channel to ambient thermal Impedance	$\theta_{ch-a}$	139	$^\circ C/W$
Channel temperature	$T_{ch}$	150	$^\circ C$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ C$

- Notes: 1. Limited by  $T_{ch}$  max.  
2. Value at  $T_c = 25^\circ C$   
3. Pulse width limited by safe operating area.

## Electrical Characteristics

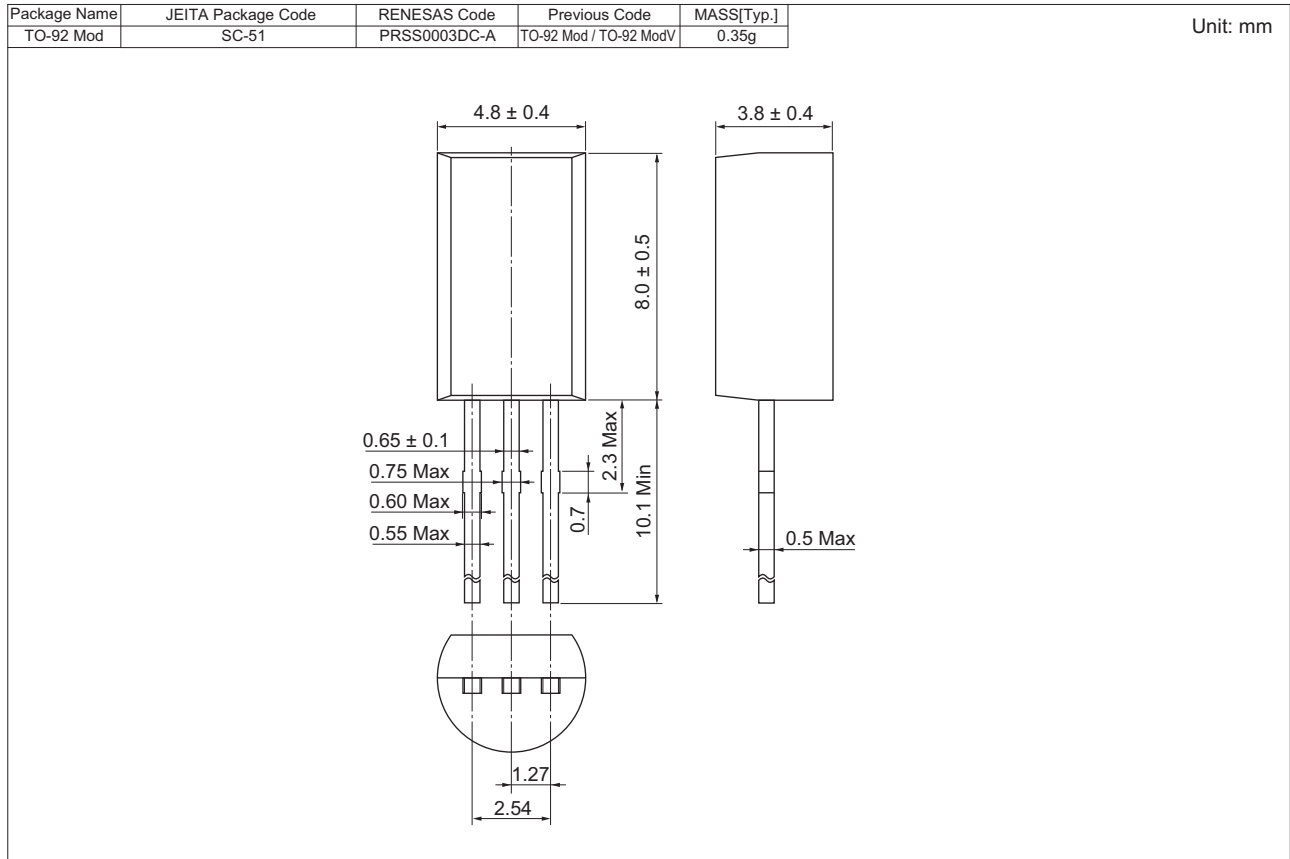
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	500	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	1	$\mu\text{A}$	$V_{DS} = 500 \text{ V}$ , $V_{GS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	$\pm 0.1$	$\mu\text{A}$	$V_{GS} = \pm 30 \text{ V}$ , $V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	3.0	—	4.5	V	$V_{DS} = 10 \text{ V}$ , $I_D = 1 \text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	3.83	5.00	$\Omega$	$I_D = 1.2 \text{ A}$ , $V_{GS} = 10 \text{ V}$ <sup>Note 4</sup>
Input capacitance	$C_{iss}$	—	165	—	pF	$V_{DS} = 25 \text{ V}$
Output capacitance	$C_{oss}$	—	21	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	$C_{rss}$	—	2.6	—	pF	$f = 1 \text{ MHz}$
Turn-on delay time	$t_{d(on)}$	—	11	—	ns	$I_D = 1.2 \text{ A}$
Rise time	$t_r$	—	12.5	—	ns	$V_{GS} = 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	22	—	ns	$R_L = 208 \Omega$
Fall time	$t_f$	—	22	—	ns	$R_g = 10 \Omega$
Body-drain diode forward voltage	$V_{DF}$	—	0.9	1.5	V	$I_F = 2.4 \text{ A}$ , $V_{GS} = 0$ <sup>Note 4</sup>
Body-drain diode reverse recovery time	$t_{rr}$	—	235	—	ns	$I_F = 2.4 \text{ A}$ , $V_{GS} = 0$ $V_{DD} = 400 \text{ V}$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Note: 4. Pulse test

5. Since this device is equipped with high voltage FET chip ( $V_{DSS} \geq 500 \text{ V}$ ), high voltage may be supplied. Therefore, please be sure to confirm about electric discharge between drain terminal and other terminal.
6. This device is sensitive to electrostatic discharge. It is recommended to adopt appropriate cautions when handling this product.

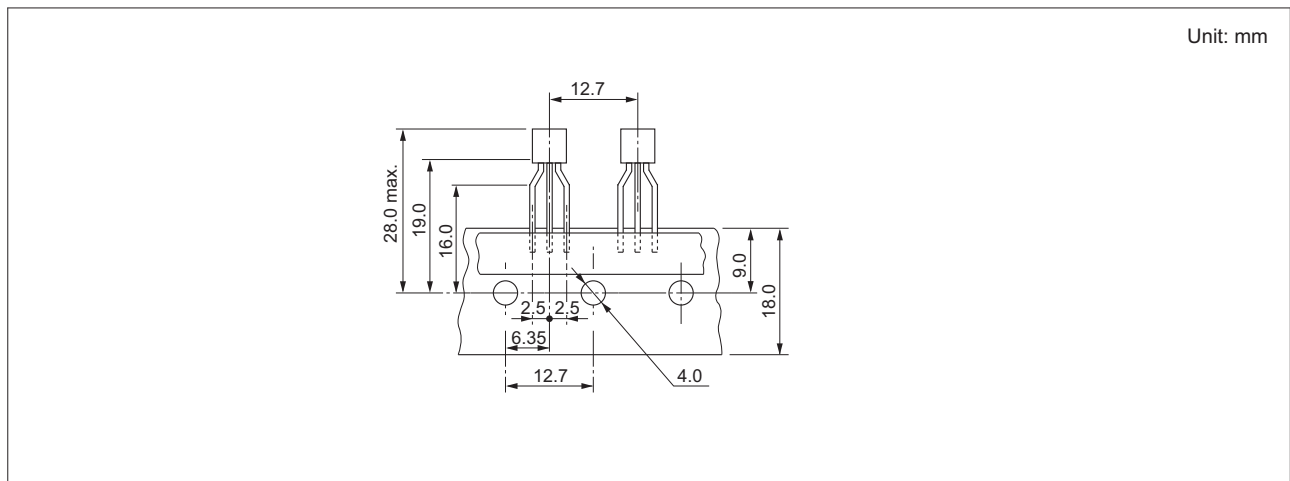
### Package Dimensions



### Ordering Information

Orderable Part No.	Quantity	Shipping Container
RJK5002DJE-00#Z0	2500 pcs	Hold Box, Radial Taping

Note: Leads is forming applied as following figure.



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**Renesas Electronics America Inc.**  
2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A.  
Tel: +1-408-588-6000, Fax: +1-408-588-6130

**Renesas Electronics Canada Limited**  
1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada  
Tel: +1-905-898-5441, Fax: +1-905-898-3220

**Renesas Electronics Europe Limited**  
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.  
Tel: +44-1628-585-100, Fax: +44-1628-585-900

**Renesas Electronics Europe GmbH**  
Arcadiastrasse 10, 40472 Düsseldorf, Germany  
Tel: +49-211-65030, Fax: +49-211-6503-1327

**Renesas Electronics (China) Co., Ltd.**  
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China  
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

**Renesas Electronics (Shanghai) Co., Ltd.**  
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China  
Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

**Renesas Electronics Hong Kong Limited**  
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong  
Tel: +852-2886-9318, Fax: +852 2886-9022/9044

**Renesas Electronics Taiwan Co., Ltd.**  
13F, No. 363, Fu Shing North Road, Taipei, Taiwan  
Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

**Renesas Electronics Singapore Pte. Ltd.**  
1 HarbourFront Avenue, #06-10, Keppel Bay Tower, Singapore 098632  
Tel: +65-6213-0200, Fax: +65-6278-8001

**Renesas Electronics Malaysia Sdn.Bhd.**  
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia  
Tel: +60-3-7955-3390, Fax: +60-3-7955-9510

**Renesas Electronics Korea Co., Ltd.**  
11F., Samik Laved' or Bldg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea  
Tel: +82-2-558-3737, Fax: +82-2-558-5141