

# READ4304G

## High Drivability & High Slew Rate, Input Output Full Range CMOS Quad Operational Amplifier

$V_{IO} \leq \pm 6mV$ ,  $SR = 8V/\mu s$ ,  $GBW=6MHz$

### Description

The READ4304G is input and output full range quad CMOS Operational Amplifier realizing high drivability and high slew rate. This IC can be used in minimum operating supply voltage from 2.5V, and in wide ambient temperature range from -40°C to +105°C.

Available in ultra-small 14 pins TSSOP package.

### Features

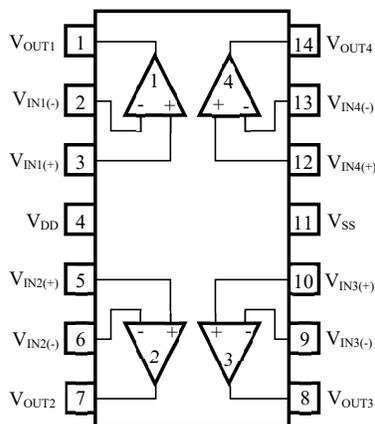
- Low voltage single supply operation  $V_{DD} = 2.5V$  to  $5.5V$
- Low input offset voltage  $V_{IO} \leq \pm 6.0mV$
- Low input bias current  $I_B \leq (1pA)$ .
- Wide output voltage range  $V_{OUT} : V_{SS}+0.1V$  to  $V_{DD}-0.1V(@I_o=5mA)$
- Supply current (per channel)  $I_{DD} = 0.75mA$  Typ.
- High slew rate  $SR = 8V/\mu s$  Typ.

( ) reference value of design

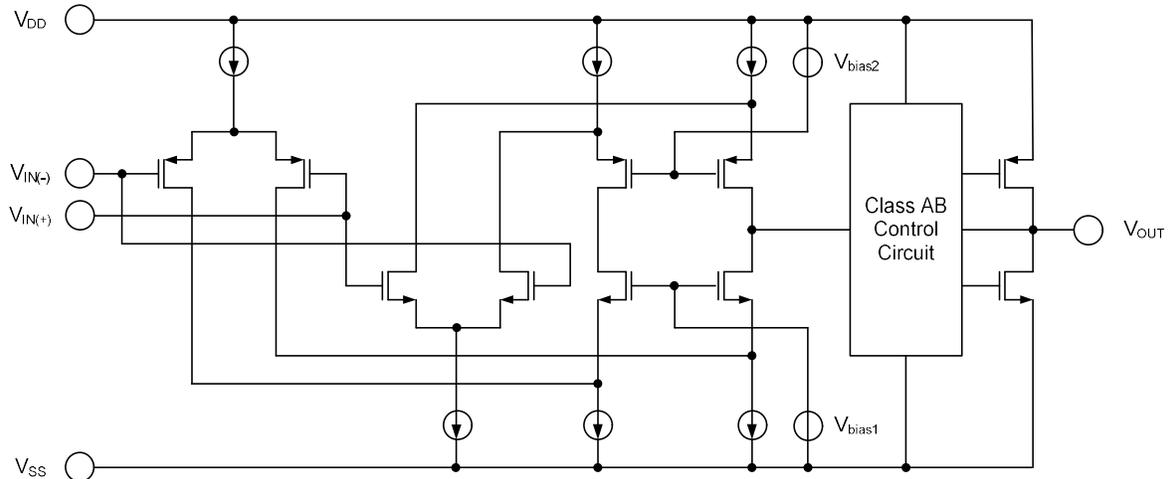
### Product Line-up

Type name	Product type quality level	Package
READ4304GSP	High slew rate with Normal quality level	14 pins plastic TSSOP

### Pin Arrangement



Equivalent Circuit (per one channel)



Absolute Maximum Ratings

<T<sub>A</sub>=25°C>

Items	Symbol	Ratings	Unit
Supply voltage <sup>Note.1</sup>	V <sub>DD</sub>	-0.3 to +6.5	V
Differential input voltage	V <sub>ID</sub>	-V <sub>DD</sub> to +V <sub>DD</sub>	V
Input voltage <sup>Note.2</sup>	V <sub>I</sub>	-0.3 to V <sub>DD</sub> +0.3	V
Maximum output current	I <sub>O</sub>	20	mA
Power dissipation <sup>Note.3</sup>	P <sub>T</sub>	550	mW
Junction temperature	T <sub>j</sub>	+150	°C
Operating temp. range	T <sub>A</sub>	-40 to +105	°C
Storage temp. range	T <sub>stg</sub>	-55 to +150	°C

Note 1. Please take note that reverse connection of a power supply may cause destruction.

2. Stresses above these ratings may cause permanent damage such as characteristics degradation or destruction. Please do not exceed voltage below of GND-0.3V as it is bottom limit. In addition, operation amplifier is operated as normal when input voltage for electrical characteristics is in common mode input voltage range.

3. The value is measured under mounted on a glass epoxy base board (size 100mm x 100mm, 1mm thickness, copper foiled surface base board area with 15% solid pattern).

Note that restrictions will be made to the following conditions for each product, and the derating ratio depending on the operating ambient temperature.

READ4304GSP: Derate at -7.0 mW/°C when T<sub>A</sub> > 71 °C

(Junction – ambient thermal resistance R<sub>th(J-A)</sub> = 144 °C/W)

**Electrical Characteristics**<V<sub>DD</sub>=5.0V, T<sub>A</sub>= 25°C>

Items	Symbol	MIN.	TYP.	MAX.	Unit	Test Condition
Supply voltage	V <sub>DD</sub> - V <sub>SS</sub>	2.5		5.5	V	
Input offset voltage	V <sub>IO</sub>			±6.0	mV	
Input offset current	I <sub>IO</sub>			(1)	pA	
Input bias current	I <sub>B</sub>			(1)	pA	
Output high voltage	V <sub>OH</sub>	V <sub>DD</sub> -0.2			V	I <sub>L</sub> = 10mA
Output low voltage	V <sub>OL</sub>			V <sub>SS</sub> +0.2	V	I <sub>L</sub> = 10mA
Voltage gain	A <sub>v</sub>	60	90		dB	R <sub>L</sub> ≥100kΩ
Channel supply current	I <sub>DD</sub> /ch		0.75	1.5	mA	R <sub>L</sub> =∞, I <sub>O</sub> =0
Common mode rejection ratio	CMRR	60	80		dB	
Supply voltage rejection ratio	SVRR	60	80		dB	
Common mode input voltage range	V <sub>ICM</sub>	V <sub>SS</sub>		V <sub>DD</sub>	V	
Gain bandwidth product	GBW		6		MHz	C <sub>L</sub> =20pF
Slew rate	SR		8		V/us	C <sub>L</sub> =20pF

( ) reference value of design

## Notes

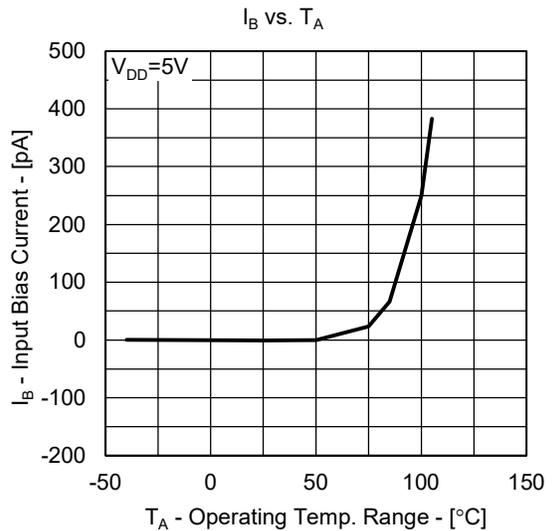
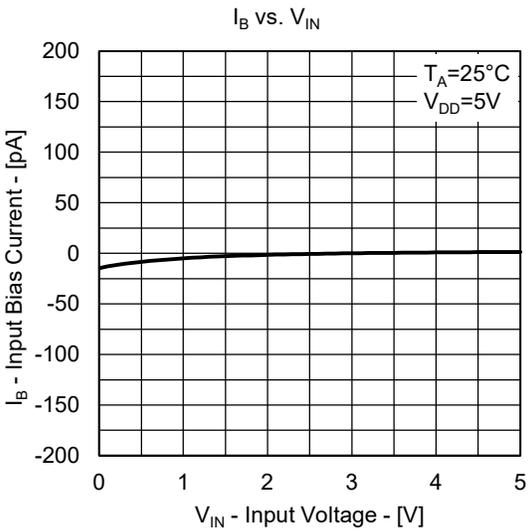
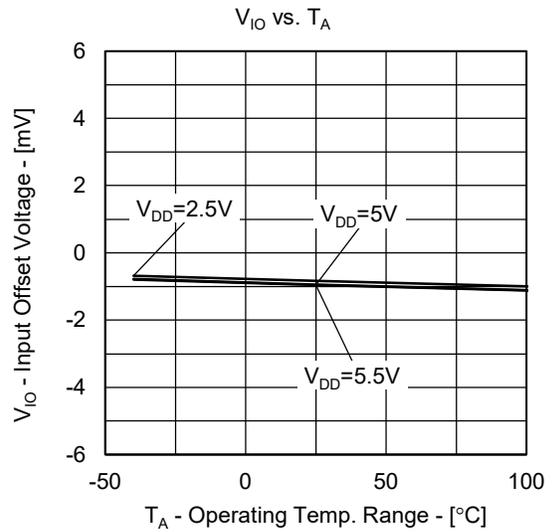
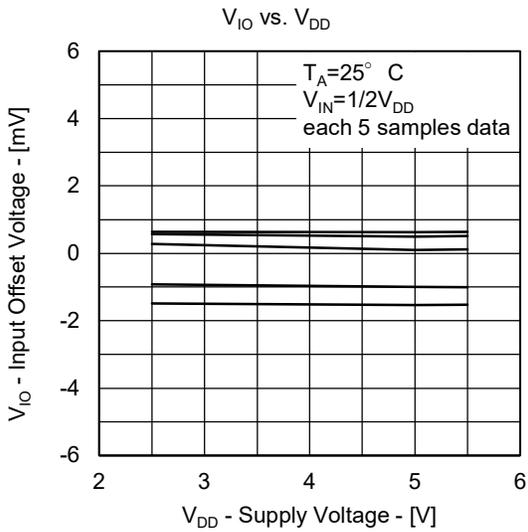
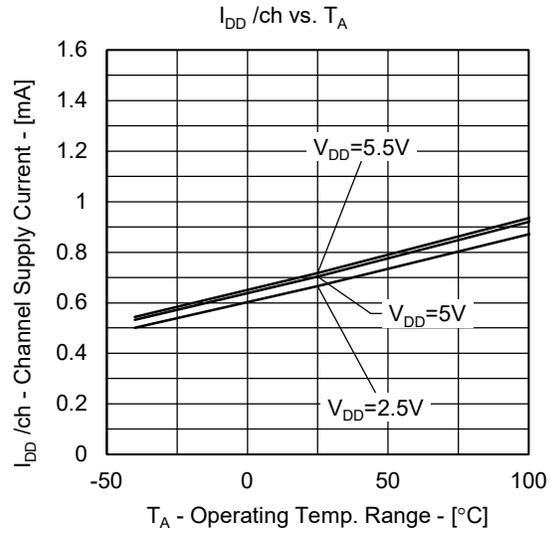
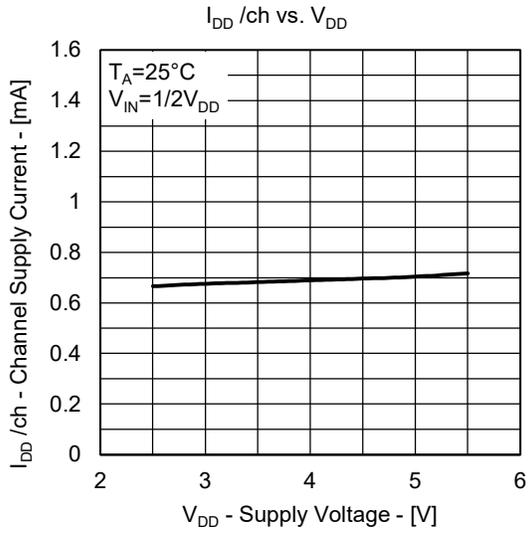
Output terminal: The over-current protection feature is not built in the output terminal of this product.

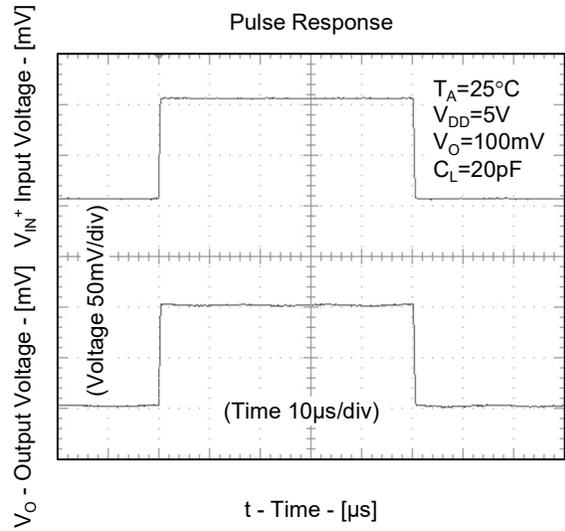
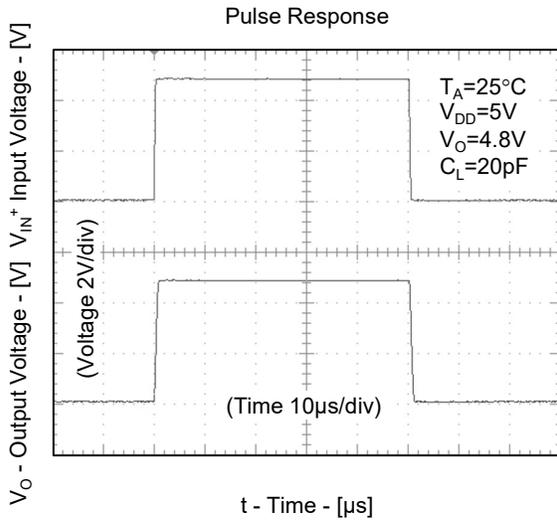
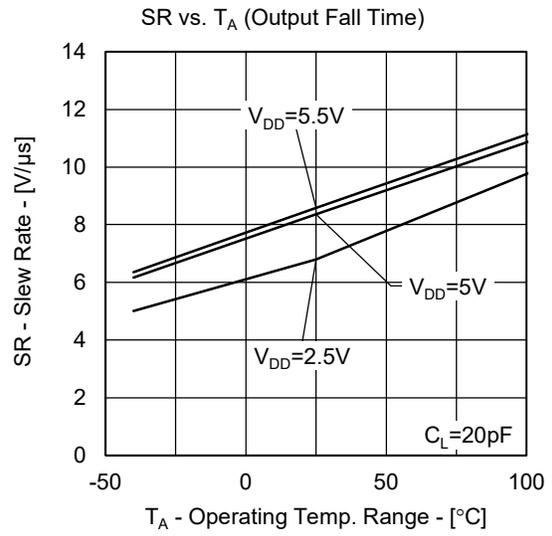
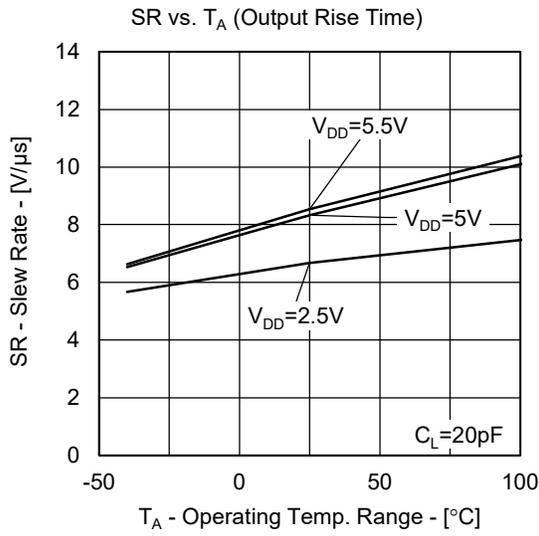
Therefore, please insert resistance to output port.

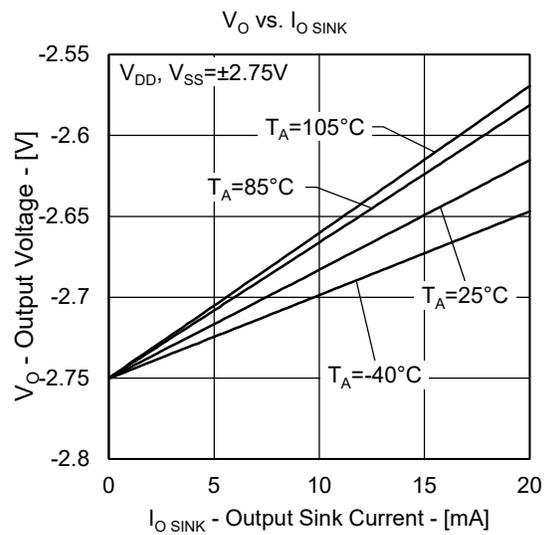
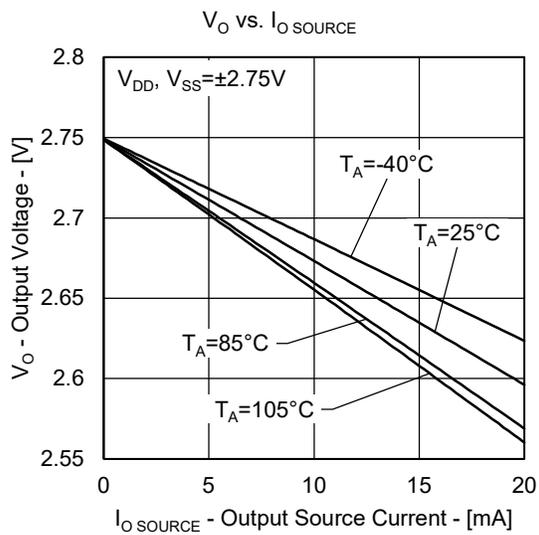
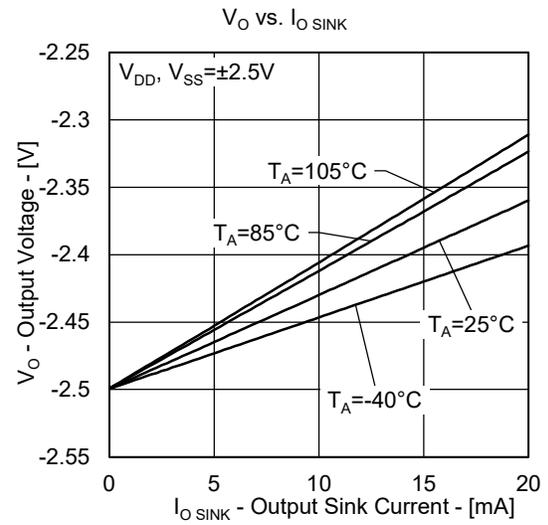
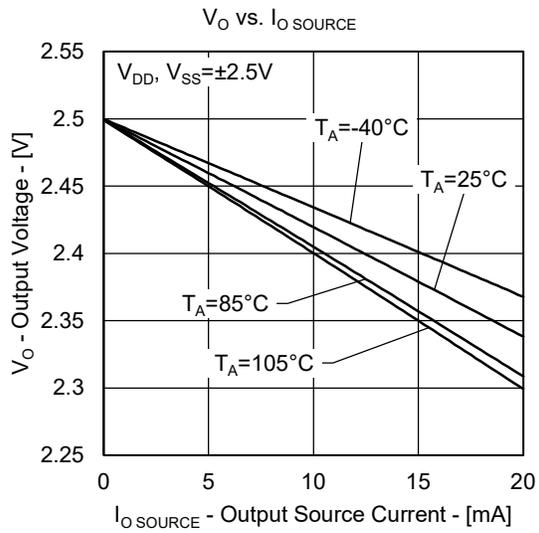
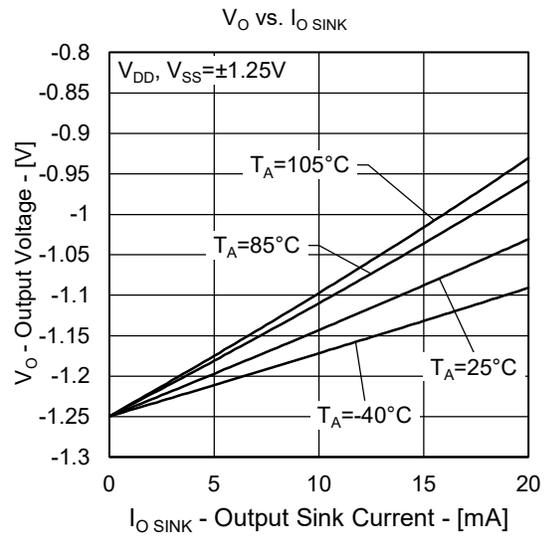
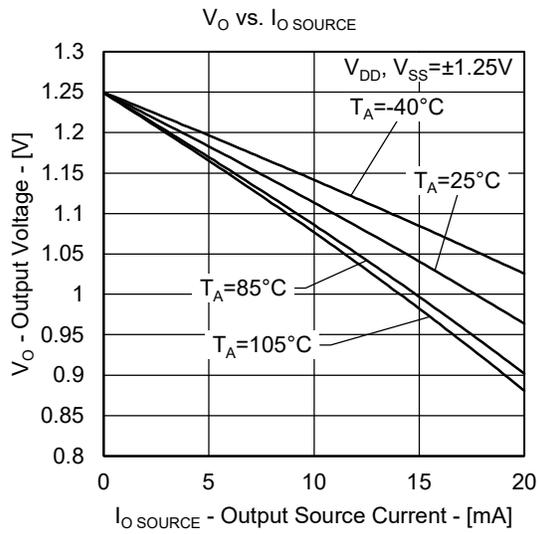
Input offset voltage : The amplifier circuit of the first block of operational amplifier.

A circuit suitable for operation near GND, and a circuit suitable for operation near +power supply. In case of input voltage of overlap point output port has a minute voltage shift or distortion.

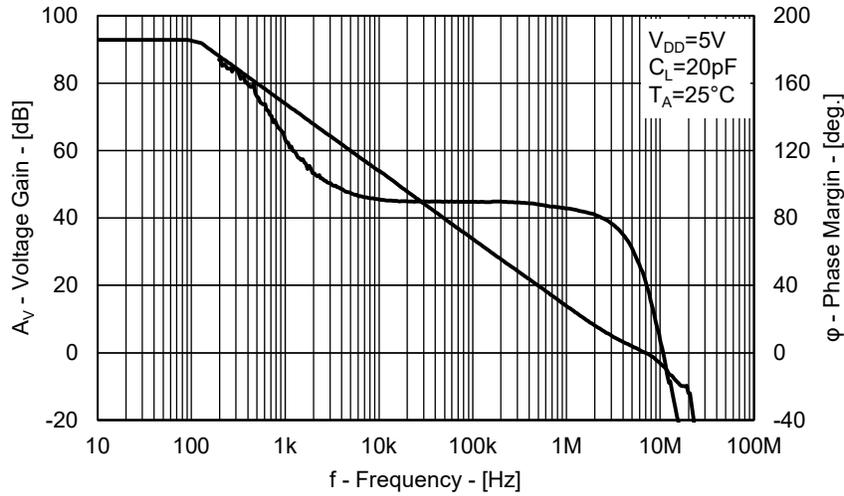
### Electrical Characteristics



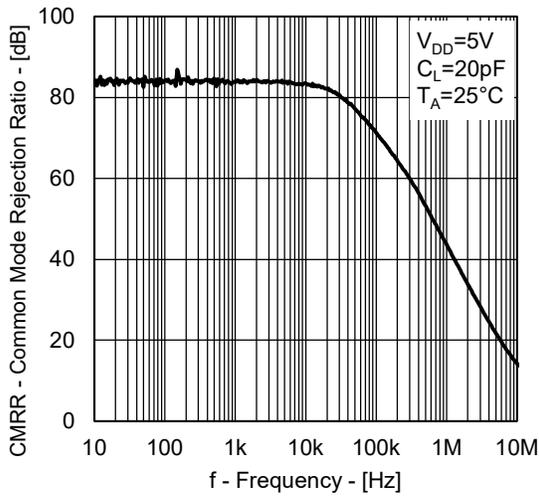




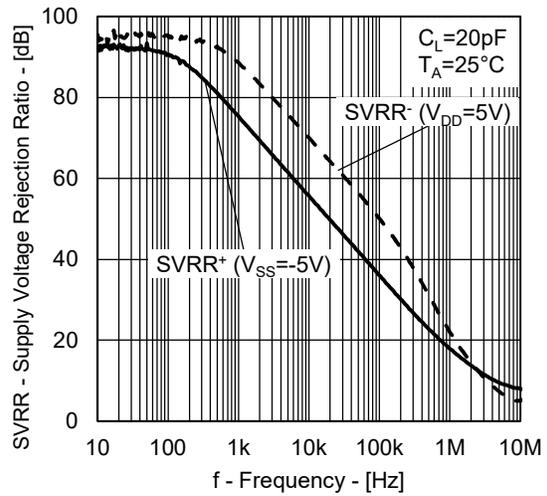
$A_v, \phi$  vs.  $f$



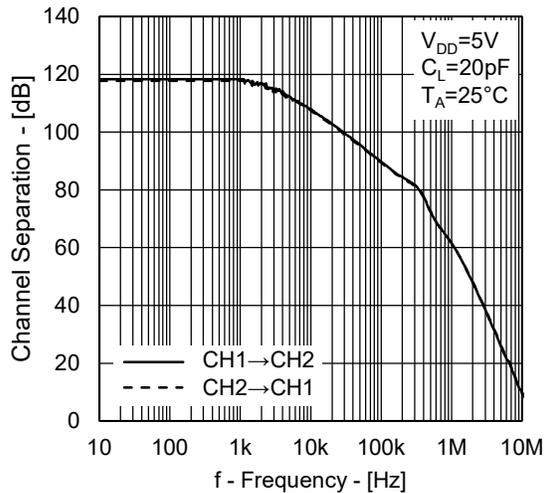
CMRR vs.  $f$



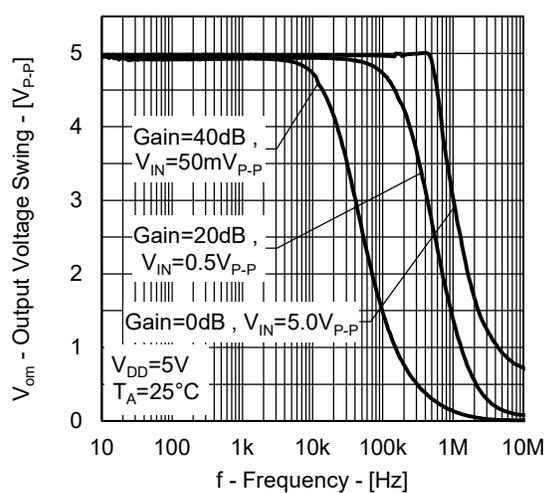
SVRR vs.  $f$



Channel Separation vs.  $f$



$V_{om}$  vs.  $f$

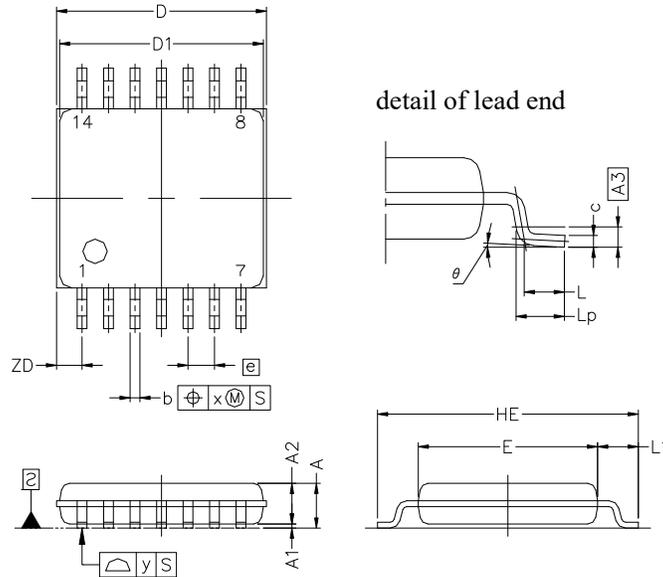


## Package Dimensions

### 14-PIN PLASTIC TSSOP

JEITA Package Code	RENESAS Code	Previous Code	MASS(TYP.)[g]
P-TSSOP14-0225-0.65	PTSP0014JB-A	P14GR-65-9LG-1	—

Unit:mm



#### NOTE

Each lead centerline is located within 0.10 mm of its true position at maximum material condition.

ITEM	MILLIMETERS
D	5.15 ±0.15
D1	5.00 ±0.10
E	4.40 ±0.10
HE	6.40 ±0.20
A	1.20 MAX.
A1	0.10 ±0.05
A2	1.00 ±0.05
A3	0.25
b	0.24 <sup>+0.06</sup> <sub>-0.05</sub>
c	0.145 ±0.055
L	0.5
Lp	0.60 ±0.15
L1	1.00 ±0.20
θ	3° <sup>+5°</sup> <sub>-3°</sub>
e	0.65
x	0.10
y	0.10
ZD	0.625

## Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
4. You shall be responsible for determining what licenses are required from any third parties, and obtaining such licenses for the lawful import, export, manufacture, sales, utilization, distribution or other disposal of any products incorporating Renesas Electronics products, if required.
5. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
6. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.

"Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.

"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.

Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.

7. No semiconductor product is absolutely secure. Notwithstanding any security measures or features that may be implemented in Renesas Electronics hardware or software products, Renesas Electronics shall have absolutely no liability arising out of any vulnerability or security breach, including but not limited to any unauthorized access to or use of a Renesas Electronics product or a system that uses a Renesas Electronics product. RENESAS ELECTRONICS DOES NOT WARRANT OR GUARANTEE THAT RENESAS ELECTRONICS PRODUCTS, OR ANY SYSTEMS CREATED USING RENESAS ELECTRONICS PRODUCTS WILL BE INVULNERABLE OR FREE FROM CORRUPTION, ATTACK, VIRUSES, INTERFERENCE, HACKING, DATA LOSS OR THEFT, OR OTHER SECURITY INTRUSION ("Vulnerability Issues"). RENESAS ELECTRONICS DISCLAIMS ANY AND ALL RESPONSIBILITY OR LIABILITY ARISING FROM OR RELATED TO ANY VULNERABILITY ISSUES. FURTHERMORE, TO THE EXTENT PERMITTED BY APPLICABLE LAW, RENESAS ELECTRONICS DISCLAIMS ANY AND ALL WARRANTIES, EXPRESS OR IMPLIED, WITH RESPECT TO THIS DOCUMENT AND ANY RELATED OR ACCOMPANYING SOFTWARE OR HARDWARE, INCLUDING BUT NOT LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY, OR FITNESS FOR A PARTICULAR PURPOSE.
8. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
9. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
11. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
12. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
13. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
14. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.

(Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.

(Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.5.0-1 October 2020)

## Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,  
Koto-ku, Tokyo 135-0061, Japan  
[www.renesas.com](http://www.renesas.com)

## Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

## Contact information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:  
[www.renesas.com/contact/](http://www.renesas.com/contact/)