Features

- Trench gate and thin wafer technology (G8H series)
- Built in fast recovery diode in one package
- Low collector to emitter saturation voltage
  \[ V_{CE(sat)} = 1.5 \text{ V typ. (at } I_c = 75 \text{ A, } V_{GE} = 15 \text{ V, } T_a = 25\degree C) \]
- Quality grade: Standard
- High speed switching
- Non-specification for short circuit
- Applications: UPS, Welding, photovoltaic inverters, Power converter system

Key Performance

<table>
<thead>
<tr>
<th>Type</th>
<th>( V_{CES} )</th>
<th>( I_c )</th>
<th>( V_{CE(sat)}, T_c=25\degree C )</th>
<th>( I_F )</th>
<th>( T_j )</th>
</tr>
</thead>
<tbody>
<tr>
<td>RBN75H65T1FPQ-A0</td>
<td>650 V</td>
<td>75 A</td>
<td>1.5 V</td>
<td>50 A</td>
<td>175 \degree C</td>
</tr>
</tbody>
</table>

Outline

RENESAS Package code: PRSS0003ZH-A
(Package name: TO-247A)
## Absolute Maximum Ratings

### (Tc = 25°C)

<table>
<thead>
<tr>
<th>Item</th>
<th>Symbol</th>
<th>Ratings</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Collector to emitter voltage</td>
<td>VCES</td>
<td>650</td>
<td>V</td>
</tr>
<tr>
<td>Gate to emitter voltage</td>
<td>VGES</td>
<td>±30</td>
<td>V</td>
</tr>
<tr>
<td>Collector current</td>
<td>IC</td>
<td>150</td>
<td>A</td>
</tr>
<tr>
<td>Collector current Tc = 25 °C</td>
<td>IC</td>
<td>75</td>
<td>A</td>
</tr>
<tr>
<td>Collector peak current</td>
<td>IC(peak)</td>
<td>300</td>
<td>A</td>
</tr>
<tr>
<td>Diode forward current Tc = 25 °C</td>
<td>IF</td>
<td>100</td>
<td>A</td>
</tr>
<tr>
<td>Diode forward current Tc = 100 °C</td>
<td>IF</td>
<td>50</td>
<td>A</td>
</tr>
<tr>
<td>Diode forward peak current</td>
<td>IF(peak)</td>
<td>300</td>
<td>A</td>
</tr>
<tr>
<td>Collector power dissipation</td>
<td>PC</td>
<td>312</td>
<td>W</td>
</tr>
<tr>
<td>Junction temperature</td>
<td>TJ</td>
<td>175</td>
<td>°C</td>
</tr>
<tr>
<td>Storage temperature</td>
<td>Tstg</td>
<td>-55 to +150</td>
<td>°C</td>
</tr>
</tbody>
</table>

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Notes:
1. PW ≤ 10 μs, duty cycle ≤ 1%
2. Please use this device in the thermal conditions which the junction temperature does not exceed 175 °C. Renesas IGBT Application Note is disclosed about reliability test and application condition up to 175 °C.

## Thermal Resistance Characteristics

### (Tc = 25°C)

<table>
<thead>
<tr>
<th>Item</th>
<th>Symbol</th>
<th>Max. Value Notes3</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Junction to case thermal resistance (IGBT)</td>
<td>Rth(j-c)</td>
<td>0.48</td>
<td>°C/W</td>
</tr>
<tr>
<td>Junction to case thermal resistance (Diode)</td>
<td>Rth(j-c)</td>
<td>0.73</td>
<td>°C/W</td>
</tr>
</tbody>
</table>

Notes:
3. Designed target value on Renesas measurement condition. (Not tested)
## Electrical Characteristics (Tc = 25°C)

<table>
<thead>
<tr>
<th>Item</th>
<th>Symbol</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
<th>Test Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Collector to emitter leakage current</td>
<td>ICES</td>
<td>—</td>
<td>—</td>
<td>200</td>
<td>μA</td>
<td>VCE = 650 V, VGE = 0 V</td>
</tr>
<tr>
<td>Gate to emitter leakage current</td>
<td>IGES</td>
<td>—</td>
<td>—</td>
<td>±1</td>
<td>μA</td>
<td>VGE = ±30 V, VCE = 0 V</td>
</tr>
<tr>
<td>Gate to emitter threshold voltage</td>
<td>VGE(th)</td>
<td>4.1</td>
<td>—</td>
<td>5.9</td>
<td>V</td>
<td>VCE = 10 V, IC = 1.5 mA</td>
</tr>
<tr>
<td>Collector to emitter saturation voltage</td>
<td>VCE(sat)</td>
<td>—</td>
<td>1.5</td>
<td>2.0</td>
<td>V</td>
<td>IC = 75 A, VGE = 15 V Notes4</td>
</tr>
<tr>
<td>Input capacitance</td>
<td>Cies</td>
<td>—</td>
<td>1500</td>
<td>—</td>
<td>pF</td>
<td>VCE = 25 V</td>
</tr>
<tr>
<td>Output capacitance</td>
<td>Coes</td>
<td>—</td>
<td>190</td>
<td>—</td>
<td>pF</td>
<td>VGE = 0 V</td>
</tr>
<tr>
<td>Reverse transfer capacitance</td>
<td>Cres</td>
<td>—</td>
<td>16</td>
<td>—</td>
<td>pF</td>
<td>f = 1 MHz</td>
</tr>
<tr>
<td>Total gate charge</td>
<td>Qg</td>
<td>—</td>
<td>54</td>
<td>—</td>
<td>nC</td>
<td>VGE = 15 V</td>
</tr>
<tr>
<td>Gate to emitter charge</td>
<td>Qge</td>
<td>—</td>
<td>13</td>
<td>—</td>
<td>nC</td>
<td>VCE = 400 V</td>
</tr>
<tr>
<td>Gate to collector charge</td>
<td>Qgc</td>
<td>—</td>
<td>24</td>
<td>—</td>
<td>nC</td>
<td>IC = 75 A</td>
</tr>
<tr>
<td>Turn-on delay time</td>
<td>t(d(on)</td>
<td>—</td>
<td>29</td>
<td>—</td>
<td>ns</td>
<td>VCC = 400 V</td>
</tr>
<tr>
<td>Rise time</td>
<td>tr</td>
<td>—</td>
<td>27</td>
<td>—</td>
<td>ns</td>
<td>VGE = +15 V/-5 V</td>
</tr>
<tr>
<td>Turn-off delay time</td>
<td>t(d(off)</td>
<td>—</td>
<td>113</td>
<td>—</td>
<td>ns</td>
<td>IC = 75 A</td>
</tr>
<tr>
<td>Fall time</td>
<td>tf</td>
<td>—</td>
<td>37</td>
<td>—</td>
<td>ns</td>
<td>Rg = 16 Ω</td>
</tr>
<tr>
<td>Turn-on loss energy</td>
<td>E_on</td>
<td>—</td>
<td>1.6</td>
<td>—</td>
<td>mJ</td>
<td>Tc = 25 °C</td>
</tr>
<tr>
<td>Turn-off loss energy</td>
<td>E_off</td>
<td>—</td>
<td>1.0</td>
<td>—</td>
<td>mJ</td>
<td>Inductive load Notes5</td>
</tr>
<tr>
<td>Total switching energy</td>
<td>E_total</td>
<td>—</td>
<td>2.6</td>
<td>—</td>
<td>mJ</td>
<td></td>
</tr>
<tr>
<td>Turn-on delay time</td>
<td>t(d(on)</td>
<td>—</td>
<td>27</td>
<td>—</td>
<td>ns</td>
<td>VCC = 400 V</td>
</tr>
<tr>
<td>Rise time</td>
<td>tr</td>
<td>—</td>
<td>24</td>
<td>—</td>
<td>ns</td>
<td>VGE = +15 V/-5V</td>
</tr>
<tr>
<td>Turn-off delay time</td>
<td>t(d(off)</td>
<td>—</td>
<td>137</td>
<td>—</td>
<td>ns</td>
<td>IC = 75 A</td>
</tr>
<tr>
<td>Fall time</td>
<td>tf</td>
<td>—</td>
<td>55</td>
<td>—</td>
<td>ns</td>
<td>Rg = 16 Ω</td>
</tr>
<tr>
<td>Turn-on loss energy</td>
<td>E_on</td>
<td>—</td>
<td>2.3</td>
<td>—</td>
<td>mJ</td>
<td>Tc = 150 °C</td>
</tr>
<tr>
<td>Turn-off loss energy</td>
<td>E_off</td>
<td>—</td>
<td>1.5</td>
<td>—</td>
<td>mJ</td>
<td>Inductive load Notes5</td>
</tr>
<tr>
<td>Total switching energy</td>
<td>E_total</td>
<td>—</td>
<td>3.8</td>
<td>—</td>
<td>mJ</td>
<td></td>
</tr>
</tbody>
</table>

| Diode forward voltage                                   | Vf     | —       | 1.7     | 2.2     | V     | If = 50 A Notes4                     |
| Diode reverse recovery time                              | ttr    | —       | 72      | —       | ns    | If = 50 A, df/dt = 300 A/μS          |
| Diode reverse recovery charge                            | Qr     | —       | 0.3     | —       | μC    |                                      |
| Diode peak reverse recovery current                      | Ir     | —       | 8       | —       | A     |                                      |

Notes:
4. Pulse test
5. Switching time test circuit and waveform are shown below.
Main Characteristics

Collector Dissipation vs. Case Temperature

Maximum DC Collector Current vs. Case Temperature

Forward Biase Safe Operating Area

Reverse Biase Safe Operating Area

Typical Output Characteristics

Typical Output Characteristics

Notes: 6. Designed target value on Renesas measurement condition. (Not tested)
Renesas recommends that operating conditions are designed according to a document "Power MOS FET・IGBT Attention of Handling Semiconductor Devices".
Switching Characteristics (Typical) (1)

Switching Characteristics (Typical) (2)

Switching Characteristics (Typical) (3)

Switching Characteristics (Typical) (4)

Switching Characteristics (Typical) (5)

Switching Characteristics (Typical) (6)
Reverse Recovery Time vs. Diode Current Slope (Typical)

Reverse Recovery Charge vs. Diode Current Slope (Typical)

Reverse Recovery Current vs. Diode Current Slope (Typical)

Reverse Recovery Time vs. Forward Current (Typical)

Reverse Recovery Charge vs. Forward Current (Typical)

Reverse Recovery Current vs. Forward Current (Typical)

Diode Current Slope $d_iF/dt$ (A/$\mu$s)

Forward Current $I_F$ (A)

VCC = 400 V

IF = 50 A

$T_c = 150$ °C

$25$ °C

$VCC = 400$ V

$diF/dt = 300$ A/$\mu$s

$T_c = 150$ °C

$25$ °C

VCC = 400 V

IF = 50 A

$T_c = 150$ °C

$25$ °C

VCC = 400 V

$diF/dt = 300$ A/$\mu$s

$T_c = 150$ °C

$25$ °C
Dynamic Input Characteristics (Typical)

- Collector to Emitter Voltage $V_{CE}$ (V)
- Gate Charge $Q_g$ (nC)
- Voltage $V_{CC} = 400$ V
- $I_C = 75$ A
- $T_c = 25{\degree}C$

Typical Capacitance vs. Collector to Emitter Voltage

- Capacitance $C$ (pF)
- Collector to Emitter Voltage $V_{CE}$ (V)
- $V_{GE} = 0$ V
- $f = 1$ MHz
- $T_c = 25{\degree}C$

Gate to Emitter Voltage $V_{GE}$ (V)
Notes: 7. Designed target value on Renesas measurement condition. (Not tested)
**Switching Time Test Circuit**

![Switching Time Test Circuit Diagram]

- **D.U.T.**
- **Rg**
- **L**
- **VCC**

**Diode Reverse Recovery Time Test Circuit**

![Diode Reverse Recovery Time Test Circuit Diagram]

- **D.U.T.**
- **Rg**
- **VCC**
- **L**

**Waveform**

- **VGE**
- **IC**
- **IF**
- **dl/dt**
- **Qrr**
- **0.5 Ir**
- **0.9 Ir**

**Symbols**:

- `td(off)`
- `td(on)`
- `tf`
- `tr`
- `90%`
- `10%`
## Package Dimensions

<table>
<thead>
<tr>
<th>JEDEC Package Code</th>
<th>RENESAS Code</th>
<th>Previous Code</th>
<th>MASS (Typ) [g]</th>
</tr>
</thead>
<tbody>
<tr>
<td>TO-247AD</td>
<td>PRSS0003ZH-A</td>
<td>—</td>
<td>6.14</td>
</tr>
</tbody>
</table>

Unit: mm

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### Ordering Information

<table>
<thead>
<tr>
<th>Orderable Part Number</th>
<th>Quantity</th>
<th>Shipping Container</th>
</tr>
</thead>
<tbody>
<tr>
<td>RBN75H65T1FPQ-A0#CB0</td>
<td>240 pcs</td>
<td>Box (Tube)</td>
</tr>
</tbody>
</table>
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