

# RBN200N180S2HFWA

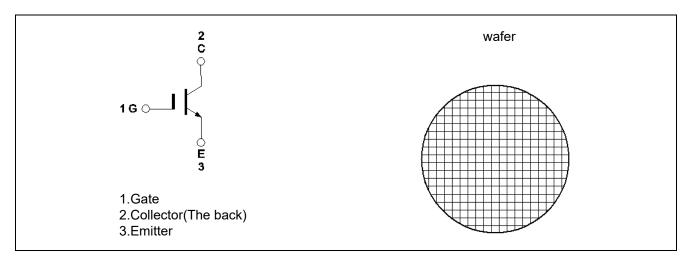
1800V - 400A/200A - IGBT

R07DS1453EJ0110 Rev.1.10 Apr 11th, 2023

### **Features**

- Renesas generation 8th Trench IGBT
- Short circuit withstands time (10 μs min.)
- Optimized for high power application
- Unsawn wafer Wafer size = 200 mm
- Quality grade: Standard

### **Outline**



# **Absolute Maximum Ratings**

(Tj = 25°C unless otherwise noted)

Item		Symbol	Ratings	Unit	
Collector to emitter vol	ltage	V <sub>CES</sub>	1800	V	
Gate to emitter voltage	<del></del>	V <sub>GES</sub>	±30 V		
Collector current	Tc = 25°C	Ic	400 Notes1	А	
	Tc = 100°C	Ic	200 Notes1	А	
Junction temperature	<u>.</u>	Tj	175 Notes2	°C	

Notes: 1. Depends on thermal properties of assembly.

- 2. Please use this device in the thermal conditions which the junction temperature does not exceed 175°C.
- 3. Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

# **Electrical Characteristics** (Data below are measured values on a package configuration.)

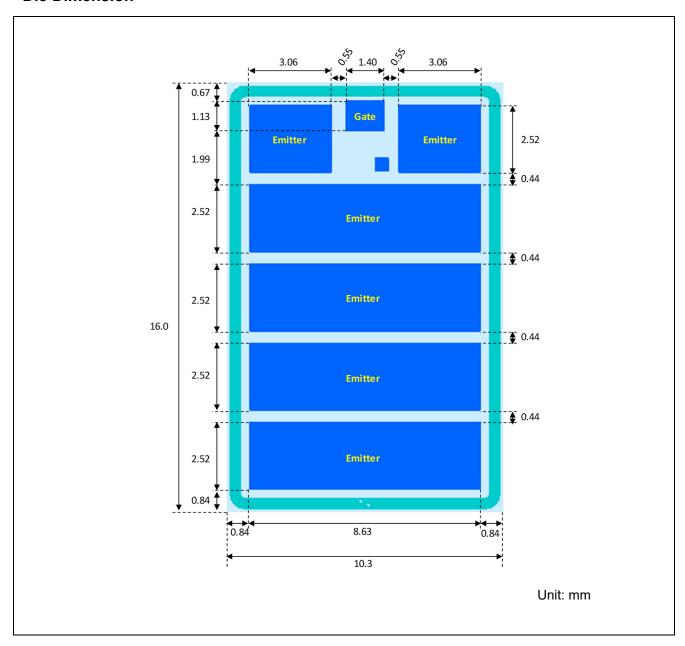
(Tj = 25 °C unless otherwise noted)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Zero gate voltage collector current	Ices	_		4.0	μΑ	V <sub>CE</sub> = 1800 V, V <sub>GE</sub> = 0 V Notes4	
Gate to emitter leak current	Iges	_		±600	nA	$V_{GE} = \pm 30 \text{ V}, V_{CE} = 0 \text{ V}^{\text{Notes4}}$	
Gate to emitter threshold voltage	$V_{\text{GE(th)}}$	5.0	5.8	6.5	<b>V</b>	$V_{CE} = 10 \text{ V}, I_{C} = 8.0 \text{ mA}^{Notes4}$	
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	_	1.60	1.90	V	I <sub>C</sub> = 200 A, V <sub>GE</sub> = 15 V Notes4, Notes5	
Input capacitance	Cies	_	16500	_	pF	V <sub>CE</sub> = 25 V	
Output capacitance	Coes	_	600	_	pF	$V_{GE} = 0 V$	
Reveres transfer capacitance	Cres	_	250	_	рF	f = 1 MHz <sup>Notes6</sup>	
Short circuit withstand time	t <sub>sc</sub>	10	_	_	μs	$V_{CC} = 900 \text{ V}, V_{GE} = 15 \text{ V}$ $Tj \le 150 \text{ °C}^{\text{Notes6}}$	
Short circuit collector saturation current	$I_{c,sc}$	600	_	_	Α	$V_{CC} = 900 \text{ V}, V_{GE} = 15 \text{ V}$ $Tj \le 150 \text{ °C}^{\text{Notes6}}$	
Reverse bias safe operating area (RBSOA)	I <sub>C</sub> , <sub>max</sub>	400	_	_	Α	$V_{\text{CE,max}}$ = 1700 V, Tj $\leq$ 175 °C Notes6	
Chip gate resistor	Rg_chip	_	5	_	Ω	Notes6	

Notes: 4. Tested on wafer.

- 5. Pulse test.
- 6. Designed target value on Renesas measurement condition. (Not tested)
- 7. Characteristic items prescribed in this document will guarantee the electrical characteristics in chip state but not the characteristic fluctuations or characteristic defects that occur in the processes after assembling.
- 8. Switching characteristics is depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

# **Die Dimension**



# **Ordering Information**

Please contact your Renesas sales representative for sample requests.

Delivery form	Ordering Part Number	Ordering quantity unit	
Unsawn wafer	RBN200N180S2HFWA-850#FF0	740 (5wafers)	
Unsawn wafer	RBN200N180S2HFWA-8F0#FF0	3700 (25wafers)	

Note. The order quantities indicate the maximum quantity of chips for each part number, and the actual quantity of chips shipped will be reduced due to yield. There is also a possibility that the number of wafers may decrease during the manufacturing process. The quantity shipped will be indicated on the label as the number of good chips.

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