NP29N06QDK

60 V – 30 A – Dual N-channel Power MOS FET
Application: Automotive

Description

NP29N06QDK is a dual N-channel MOS Field Effect Transistor designed for high current switching applications.

Features

- Super low on-state resistance
  - \( R_{DS(on)} = 20 \, \text{m}\Omega \, \text{MAX.} \) (\( V_{GS} = 10 \, \text{V}, \, I_D = 15 \, \text{A} \))
  - \( R_{DS(on)} = 30 \, \text{m}\Omega \, \text{MAX.} \) (\( V_{GS} = 4.5 \, \text{V}, \, I_D = 7.5 \, \text{A} \))
- Low \( C_{iss} \): \( C_{iss} = 1000 \, \text{pF TYP.} \) (\( V_{DS} = 25 \, \text{V} \))
- Designed for automotive application and AEC-Q101 qualified
- Small size package 8-pin HSON dual

Outline

8-pin HSON dual

Equivalent circuit

Remark: Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

Ordering Information

<table>
<thead>
<tr>
<th>Part No.</th>
<th>Lead Plating</th>
<th>Packing</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>NP29N06QDK-E1-AY</td>
<td>Pure Sn (Tin)</td>
<td>Tape 2500 p/reel</td>
<td>8-pin HSON dual</td>
</tr>
<tr>
<td>NP29N06QDK-E2-AY</td>
<td>Pure Sn (Tin)</td>
<td>Taping (E1 type)</td>
<td>8-pin HSON dual</td>
</tr>
</tbody>
</table>

Note: *1. Pb-free (This product does not contain Pb in the external electrode)
### Absolute Maximum Ratings \((T_A = 25^\circ C)\)

<table>
<thead>
<tr>
<th>Item</th>
<th>Symbol</th>
<th>Ratings</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Drain to Source Voltage ((V_{GS} = 0 \text{ V}))</td>
<td>(V_{DSS})</td>
<td>60</td>
<td>V</td>
</tr>
<tr>
<td>Gate to Source Voltage ((V_{DS} = 0 \text{ V}))</td>
<td>(V_{GSS})</td>
<td>±20</td>
<td>V</td>
</tr>
<tr>
<td>Drain Current ((\text{DC}) \ (T_C = 25^\circ C)) *4</td>
<td>(I_D(\text{DC}))</td>
<td>±30</td>
<td>A</td>
</tr>
<tr>
<td>Drain Current ((\text{pulse})) *1, 4, 5</td>
<td>(I_D(\text{pulse}))</td>
<td>±60</td>
<td>A</td>
</tr>
<tr>
<td>Total Power Dissipation ((T_C = 25^\circ C)) *4</td>
<td>(P_{T1})</td>
<td>44</td>
<td>W</td>
</tr>
<tr>
<td>Total Power Dissipation ((T_A = 25^\circ C)) *2, 4</td>
<td>(P_{T2})</td>
<td>1.0</td>
<td>W</td>
</tr>
<tr>
<td>Channel Temperature</td>
<td>(T_{ch})</td>
<td>175</td>
<td>(^\circ\text{C})</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>(T_{stg})</td>
<td>–55 to +175</td>
<td>(^\circ\text{C})</td>
</tr>
<tr>
<td>Repetitive Avalanche Current *3, 5</td>
<td>(I_{AR})</td>
<td>12</td>
<td>A</td>
</tr>
<tr>
<td>Repetitive Avalanche Energy *3, 5</td>
<td>(E_{AR})</td>
<td>15</td>
<td>mJ</td>
</tr>
</tbody>
</table>

### Thermal Resistance

- Channel to Case Thermal Resistance \(R_{th(ch-C)}\) *5: 3.37 \(^\circ\text{C/W}\)
- Channel to Ambient Thermal Resistance *2 \(R_{th(ch-A)}\) *5: 150 \(^\circ\text{C/W}\)

Notes:

*1. \(T_C = 25^\circ\text{C}\), \(PW \leq 10 \mu\text{s}\), Duty Cycle \(\leq 1\%\)
*2. Mounted on glass epoxy substrate of 40 mm \(\times\) 40 mm \(\times\) 1.6 mmt with 4% copper area (35 \(\mu\text{m}\))
*3. \(R_G = 25 \Omega\), \(V_{GS} = 20 \text{ V} \rightarrow 0 \text{ V}\)
*4. One channel operation
**Electrical Characteristics (T_A = 25°C)**

<table>
<thead>
<tr>
<th>Item</th>
<th>Symbol</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
<th>Test Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Zero Gate Voltage Drain Current</td>
<td>I_DSS</td>
<td>1.5</td>
<td>2.1</td>
<td>2.5</td>
<td>µA</td>
<td>V_DDS = 60 V, V_GS = 0 V</td>
</tr>
<tr>
<td>Gate Leakage Current</td>
<td>I_GSS</td>
<td>±100</td>
<td>±100</td>
<td>±100</td>
<td>nA</td>
<td>V_DDS = ±20 V, V_GS = 0 V</td>
</tr>
<tr>
<td>Gate to Source Threshold Voltage</td>
<td>V_GS(th)</td>
<td>1.5</td>
<td>2.1</td>
<td>2.5</td>
<td>V</td>
<td>V_DDS = V_GS, I_D = 250 µA</td>
</tr>
<tr>
<td>Forward Transfer Admittance *1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>V_DDS = 5 V, I_D = 15 A</td>
</tr>
<tr>
<td>Drain to Source On-state Resistance</td>
<td>R_DS(on)1</td>
<td>13.8</td>
<td>20</td>
<td>mΩ</td>
<td>V_GS = 10 V, I_D = 15 A</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R_DS(on)2</td>
<td>16.5</td>
<td>30</td>
<td>mΩ</td>
<td>V_GS = 4.5 V, I_D = 7.5 A</td>
<td></td>
</tr>
<tr>
<td>Input Capacitance *2</td>
<td>C Iss</td>
<td>1000</td>
<td>1500</td>
<td>1500</td>
<td>pF</td>
<td>V_DDS = 25 V, V_GS = 0 V, f = 1 MHz</td>
</tr>
<tr>
<td>Output Capacitance *2</td>
<td>C Os</td>
<td>110</td>
<td>170</td>
<td>170</td>
<td>pF</td>
<td>V_DDS = 0 V</td>
</tr>
<tr>
<td>Reverse Transfer Capacitance *2</td>
<td>C rss</td>
<td>50</td>
<td>90</td>
<td>90</td>
<td>pF</td>
<td>f = 1 MHz</td>
</tr>
<tr>
<td>Turn-on Delay Time *2</td>
<td>t_on</td>
<td>15</td>
<td>30</td>
<td>30</td>
<td>ns</td>
<td>V_DDD = 30 V, I_D = 15 A</td>
</tr>
<tr>
<td>Rise Time *2</td>
<td>t_r</td>
<td>5</td>
<td>12</td>
<td>12</td>
<td>ns</td>
<td>V_DDS = 10 V, R_G = 0 Ω</td>
</tr>
<tr>
<td>Turn-off Delay Time *2</td>
<td>t_off</td>
<td>40</td>
<td>80</td>
<td>80</td>
<td>ns</td>
<td></td>
</tr>
<tr>
<td>Fall Time *2</td>
<td>t_f</td>
<td>2</td>
<td>5</td>
<td>5</td>
<td>ns</td>
<td></td>
</tr>
<tr>
<td>Total Gate Charge *2</td>
<td>Q_G</td>
<td>16</td>
<td>24</td>
<td>24</td>
<td>nC</td>
<td>V_DDS = 48 V, V_GS = 0 V</td>
</tr>
<tr>
<td>Gate to Source Charge</td>
<td>Q_GS</td>
<td>4</td>
<td></td>
<td></td>
<td>nC</td>
<td>V_DDS = 10 V</td>
</tr>
<tr>
<td>Gate to Drain Charge</td>
<td>Q_GD</td>
<td>3</td>
<td></td>
<td></td>
<td>nC</td>
<td>I_D = 30 A</td>
</tr>
<tr>
<td>Body Diode Forward Voltage *1</td>
<td>V_F(S-D)</td>
<td>0.9</td>
<td>1.5</td>
<td>1.5</td>
<td>V</td>
<td>I_F = 30 A, V_GS = 0 V</td>
</tr>
<tr>
<td>Reverse Recovery Time</td>
<td>t_r</td>
<td>25</td>
<td></td>
<td></td>
<td>ns</td>
<td>I_F = 30 A, V_DDS = 0 V, di/dt = 100 A/µs</td>
</tr>
<tr>
<td>Reverse Recovery Charge</td>
<td>Q_rr</td>
<td>22</td>
<td></td>
<td></td>
<td>nC</td>
<td></td>
</tr>
</tbody>
</table>

Note: *1. Pulsed test
Note: *2. Not subject of production test. Verified by design/characterization.

---

**TEST CIRCUIT 1 AVALANCHE CAPABILITY**

![Test Circuit 1](image)

**TEST CIRCUIT 2 SWITCHING TIME**

![Test Circuit 2](image)

**TEST CIRCUIT 3 GATE CHARGE**

![Test Circuit 3](image)
**Typical Characteristics** \((T_A = 25^\circ C)\)

**DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA**

![Graph showing derating factor vs. case temperature.]

**TOTAL POWER DISSIPATION vs. CASE TEMPERATURE**

![Graph showing total power dissipation vs. case temperature.]

**FORWARD BIAS SAFE OPERATING AREA**

![Graph showing forward bias safe operating area.]

**TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH**

![Graph showing transient thermal resistance vs. pulse width.]

---

*Single pulse, Mounted on glass epoxy substrate of 40 mm x 40 mm x 1.6 mm with 4% copper area (35 \(\mu m\)).*
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

\[ R_{DS(on)} = \text{Drain to Source On-state Resistance} \quad \text{-} \Omega \]

\[ T_{ch} = \text{Channel Temperature} \quad ^\circ\text{C} \]

CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

\[ C_{iss}, C_{oss}, C_{rss} = \text{Capacitance} \quad \text{pF} \]

SWITCHING CHARACTERISTICS

\[ I_D = \text{Drain Current} \quad \text{A} \]

\[ t_{d(on)}, t_{d(off)}, t_{r}, t_{d(off)} = \text{Switching Time} \quad \text{ns} \]

DYNAMIC INPUT CHARACTERISTICS

\[ Q_G = \text{Gate Charge} \quad \text{nC} \]

SOURCE TO DRAIN DIODE FORWARD VOLTAGE

\[ I_F = \text{Diode Forward Current} \quad \text{A} \]

\[ V_{F(S-D)} = \text{Source to Drain Voltage} \quad \text{V} \]

REVERSE RECOVERY TIME vs. DRAIN CURRENT

\[ I_F = \text{Drain Current} \quad \text{A} \]

\[ t_{rr} = \text{Reverse Recovery Time} \quad \text{ns} \]
Package Drawings (Unit: mm)

8-pin HSON Dual (Mass: 0.12 g TYP.)

Renesas package code: PLSN0008DA-A

1: Source 1
2: Gate 1
3: Source 2
4: Gate 2
5, 6: Drain 1
7, 8: Drain 2
### Revision History

<table>
<thead>
<tr>
<th>Rev.</th>
<th>Date</th>
<th>Page</th>
<th>Summary</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.00</td>
<td>Mar 28, 2016</td>
<td>—</td>
<td>First Edition Issued</td>
</tr>
<tr>
<td>2.00</td>
<td>May 24, 2018</td>
<td>2</td>
<td>Note 5 was added</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3</td>
<td>Note 2 was added</td>
</tr>
</tbody>
</table>
Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.

2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to the product data, drawings, charts, programs, algorithms, and application examples.

3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.

4. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.

5. Renesas Electronics products are classified according to the following two quality grades: “Standard” and “High Quality”. The intended applications for each Renesas Electronics product depends on the product’s quality grade, as indicated below.

   “Standard”: Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.

   “High Quality”: Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.

   Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; underwater repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user’s manual or other Renesas Electronics document.

6. When using Renesas Electronics products, refer to the latest product information (data sheets, user’s manuals, application notes, “General Notes for Handling and Using Semiconductor Devices” in the reliability handbook, etc.) and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.

7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.

8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.

9. Renesas Electronics products and technologies shall not be used or for incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.

10. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.

11. This document shall not be republished, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.

12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.

(Note 1) “Renesas Electronics” as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.

(Note 2) “Renesas Electronics products” means any product developed or manufactured by or for Renesas Electronics.

(Rev.4.0-1 November 2017)

Renesas Electronics Corporation

http://www.renesas.com

SALES OFFICES

Refer to “http://www.renesas.com” for the latest and detailed information.

Renesas Electronics America Inc.
1001 Murphy Ranch Road, Milpitas, CA 95035, U.S.A.
Tel: +1-408-432-8888, Fax: +1-408-434-5351
Renesas Electronics Canada Limited
925 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3
Tel: +1-905-237-2004

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: +44-1628-651-700, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH
Arcadastraße 10, 40472 Düsseldorf, Germany
Tel: +49-211-6905-0, Fax: +49-211-6905-1327

Renesas Electronics (China) Co., Ltd.
Room 1709 Quantum Plaza, No.27 Zhichun Lu, Haidian District, Beijing, 100191 P. R. China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langa Road, Putuo District, Shanghai, 200033 P. R. China
Tel: +86-21-2226-0888, Fax: +86-21-2226-0669

Renesas Electronics Hong Kong Limited
Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2265-6688, Fax: +852-2265-6922

Renesas Electronics Taiwan Co., Ltd.
13F., No. 363, Fu Shing North Road, Taipei 10543, Taiwan
Tel: +886-2-8175-9600, Fax: +886-2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949
Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn. Bhd.
Unit 1207, Block B, Menara Ampang, Ampang Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7995-9390, Fax: +60-3-7995-9510

Renesas Electronics India Pvt. Ltd.
No.777C, 100 Feet Road, HAL 2nd Stage, Indiranagar, Bangalore 560 038, India
Tel: +91-80-67208700, Fax: +91-80-67208777

Renesas Electronics Korea Co., Ltd.
17F., KAMCO Yangjae Tower, 263, Gangnam-daero, Gangnam-gu, Seoul, 06265 Korea
Tel: +82-2-568-3737, Fax: +82-2-558-5358

© 2018 Renesas Electronics Corporation. All rights reserved.

Colophon 7.0