

# N0436N

# N-channel MOSFET

40 V, 56 A, 4.7 m $\Omega$ 

R07DS1414EJ0100 Rev.1.00 2019.9.30

#### **Features**

• Low on-state resistance :

 $R_{DS(on)}$  = 4.7 m $\Omega$  max. (  $V_{GS}$  = 10 V,  $I_D$  = 28 A )

• Low Ciss : Ciss = 3200 pF typ. ( V<sub>DS</sub> = 25 V )

• High current : I<sub>D(DC)</sub> = ±56 A

RoHS Compliant

• Quality Grade: Standard

· Applications: For high current switching



TO-252

### Ordering Information

Orderable Part Number	Package	Packing
N0436N-ZK-E1-AY	TO-252, Pb-free Note1	3000 pcs / Tape and Reel

Note: 1. Pb-free means that this product does not contain lead in the external electrode.

### Absolute Maximum Ratings (T<sub>A</sub> = 25°C)

Item	Symbol	Ratings	Unit
Drain to Source Voltage (V <sub>GS</sub> = 0 V)	V <sub>DSS</sub>	40	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	V <sub>GSS</sub>	± 20	V
Drain Current (DC) (T <sub>C</sub> = 25 °C)	I <sub>D(DC)</sub>	± 56	Α
Drain Current (pulse) Note2	I <sub>D(pulse)</sub>	± 112	Α
Total Power Dissipation (T <sub>C</sub> = 25 °C)	P <sub>T1</sub>	87.4	W
Total Power Dissipation (T <sub>A</sub> = 25 °C)	P <sub>T2</sub>	1.0	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to 150	°C
Single Avalanche Current Note3	las	34	Α
Single Avalanche Energy Note3	Eas	113	mJ

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Notes: 2. PW  $\leq$  10  $\mu s, \, Duty \, Cycle \leq$  1%

3. Starting Tch = 25°C, VDD = 20 V, RG = 25  $\Omega$ , VGS = 20  $\rightarrow$  0 V, L = 100  $\mu$ H

### Thermal Resistance

Item	Symbol	Max. Value Note4	Unit
Channel to Case Thermal Resistance	R <sub>th(ch-C)</sub>	1.43	°C/W
Channel to Ambient Thermal Resistance	R <sub>th(ch-A)</sub>	125	°C/W

Notes: 4. This data is the designed target maximum value on Renesas's measurement condition. (Not tested)

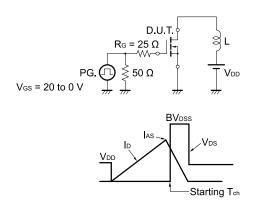
## Electrical Characteristics (T<sub>A</sub> = 25°C)

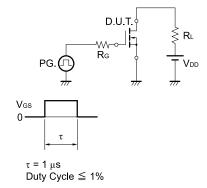
Item	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Zero Gate Voltage Drain Current	IDSS			1	μΑ	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V
Gate Leakage Current	I <sub>GSS</sub>			±100	nA	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$
Gate to Source Cut-off Voltage	V <sub>GS(off)</sub>	2.0		4.0	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance Note5	y <sub>fs</sub>		49		S	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 28 A
Drain to Source On-state Resistance Note5	R <sub>DS(on)</sub>		3.9	4.7	mΩ	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 28 A
Input Capacitance	C <sub>iss</sub>		3200		pF	V <sub>DS</sub> = 25 V
Output Capacitance	Coss		325		pF	V <sub>GS</sub> = 0 V
Reverse Transfer Capacitance	Crss		195		pF	f = 1 MHz
Turn-on Delay Time	t <sub>d(on)</sub>		23		ns	V <sub>DD</sub> = 20 V, I <sub>D</sub> = 28 A
Rise Time	t <sub>r</sub>		12		ns	V <sub>GS</sub> = 10 V
Turn-off Delay Time	t <sub>d(off)</sub>		51		ns	$R_G = 0 \Omega$
Fall Time	t <sub>f</sub>		10		ns	
Total Gate Charge	Q <sub>G</sub>		62		nC	V <sub>DD</sub> = 32 V
Gate to Source Charge	Q <sub>GS</sub>		16		nC	V <sub>GS</sub> = 10 V
Gate to Drain Charge	$Q_{GD}$		20		nC	I <sub>D</sub> = 56 A
Body Diode Forward Voltage Note5	$V_{F(S-D)}$			1.5	V	I <sub>F</sub> = 56 A, V <sub>GS</sub> = 0 V
Reverse Recovery Time	t <sub>rr</sub>		30		ns	I <sub>F</sub> = 56 A, V <sub>GS</sub> = 0 V
Reverse Recovery Charge	Q <sub>rr</sub>		15		nC	d <sub>i</sub> /d <sub>t</sub> = 100 A /μs

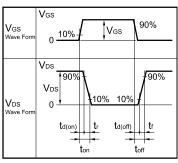
Notes: 5. Pulsed test

#### **TEST CIRCUIT 1 AVALANCHE CAPABILITY**

### TEST CIRCUIT 2 SWITCHING TIME







#### **TEST CIRCUIT 3 GATE CHARGE**

dT - Percentage of Rated Power - %

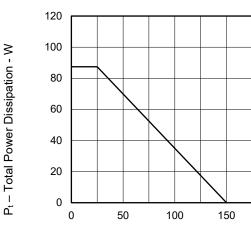
## Typical Characteristics Note6

# DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

# 120 100 80 60 40 20 0 50 100 150 200

### T<sub>C</sub> - Case Temperature - °C

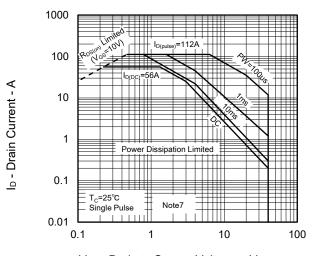
# TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



T<sub>C</sub> - Case Temperature - °C

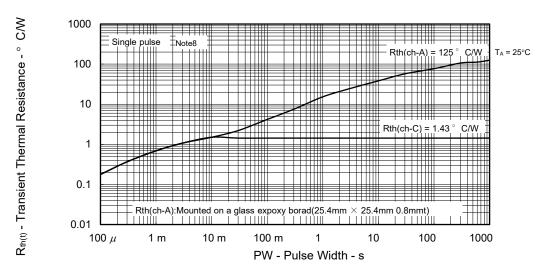
200

#### FORWARD BIAS SAFE OPERATING AREA



 $V_{\text{DS}}$  - Drain to Source Voltage  $-\ V$ 

### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



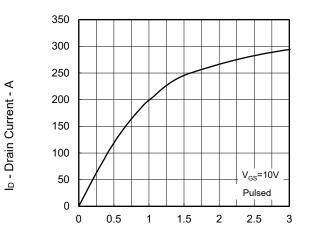
Notes: 6. Designed target value on Renesas measurement condition. (T<sub>C</sub> = 25°C, unless otherwise specified)

- This data is the designed value on Renesas's measurement condition. Renesas recommends that operating conditions
  are designed according to a document "Power MOSFET/IGBT Attention of Handling Semiconductor Devices (R07ZZ0010)".
- 8. This data is the designed target maximum value on Renesas's measurement condition.

V<sub>GS(off)</sub> – Gate to Source Cut-off Voltage - V

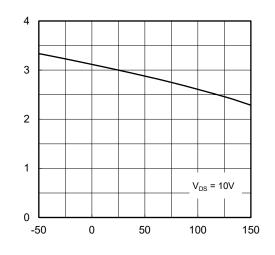
 $R_{\text{DS}(\text{on})}$  - Drain to Source On-state Resistance -  $m\Omega$ 

### DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



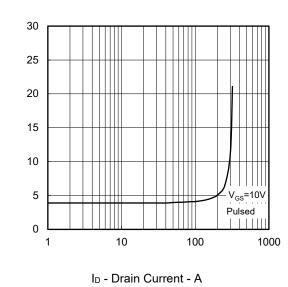
V<sub>DS</sub> - Drain to Source Voltage - V

### GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE

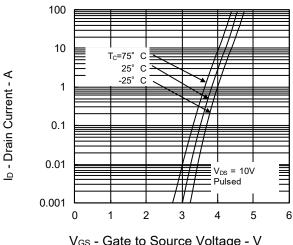


T<sub>ch</sub> - Channel Temperature - °C

### DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

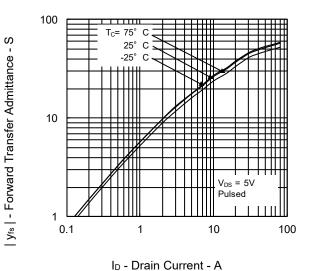


#### FORWARD TRANSFER CHARACTERISTICS

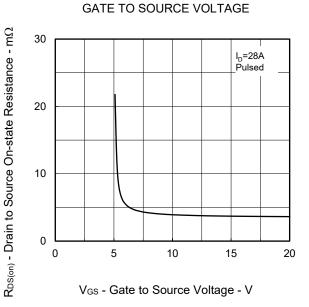


V<sub>GS</sub> - Gate to Source Voltage - V

### FORWARD TRANSFER ADMITTANCE vs. **DRAIN CURRENT**



DRAIN TO SOURCE ON-STATE RESISTANCE vs.

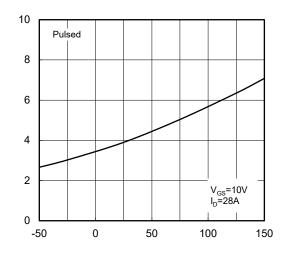


 $R_{DS(on)}$  - Drain to Source On-state Resistance -  $m\Omega$ 

:d(on),tr,td(off),tf - Switching Time - ns

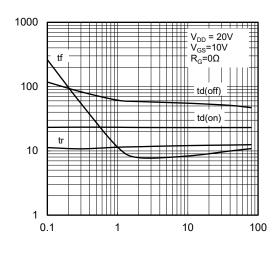
IF - Diode Forward Current - A

# DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



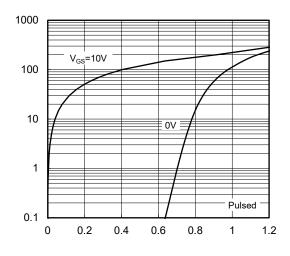
T<sub>ch</sub> - Channel Temperature - °C

### SWITCHING CHARACTERISTICS



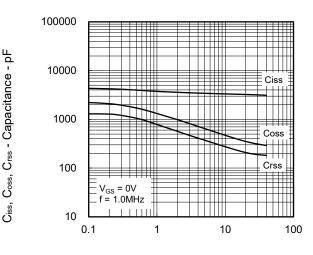
ID - Drain Current - A

# SOURCE TO DRAIN DIODE FORWARD VOLTAGE



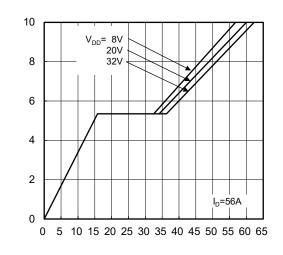
 $V_{F(S-D)}$  - Source to Drain Voltage - V

# CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



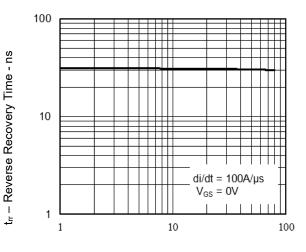
V<sub>DS</sub> - Drain to Source Voltage - V

#### DYNAMIC INPUT CHARACTERISTICS



Q<sub>G</sub> - Gate Charge - nC

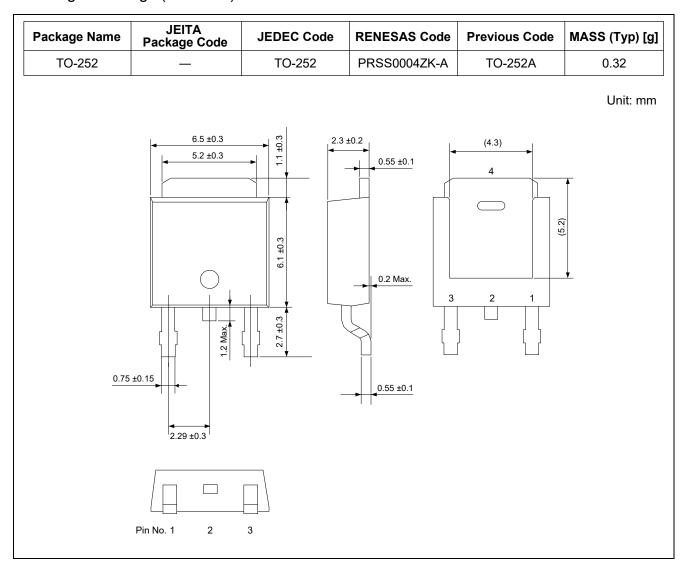
# REVERSE RECOVERY TIME vs. DRAIN CURRENT



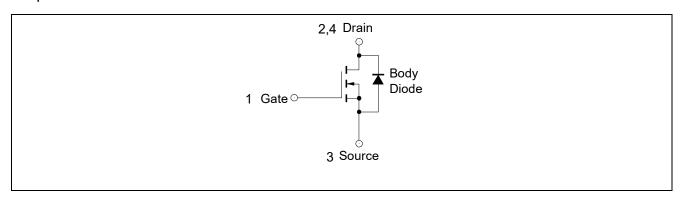
IF - Drain Current - A

V<sub>GS</sub> - Gate to Source Voltage - V

## Package Drawings (Unit: mm)



## **Equivalent Circuit**



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Renesas Electronics (China) Co., Ltd.
Room 101-T01, Floor 1, Building 7, Yard No. 7, 8th Street, Shangdi, Haidian District, Beijing 100085, China
Tel: +86-10-8253-1155, Fax. +86-10-8235-7679

Reneasa Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai 200333, China
Tet: +862-12226-0688, Fax. +862-12226-0699

Renesas Electronics Hong Kong Limited
Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2265-6688, Fax: +852 2886-9022

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300

Reneasa Electronics Malaysia Sdn.Bhd.
Unit No 3A-1 Level 3A Tower 8 UOA Business Park, No 1 Jalan Pengaturcara U1/51A, Seksyen U1, 40150
Shah Alam, Selangor, Malaysia
Tel: +60-3-5022-1286, Fax: +60-3-5022-1290

Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HAL 2nd Stage, Indiranagar, Bangalore 560 038, India Tel: +91-80-67208700

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