

## HSM226S

### Silicon Schottky Barrier Diode for High Speed Switching

REJ03G0057-0200

Rev.2.00

Sep 15, 2006

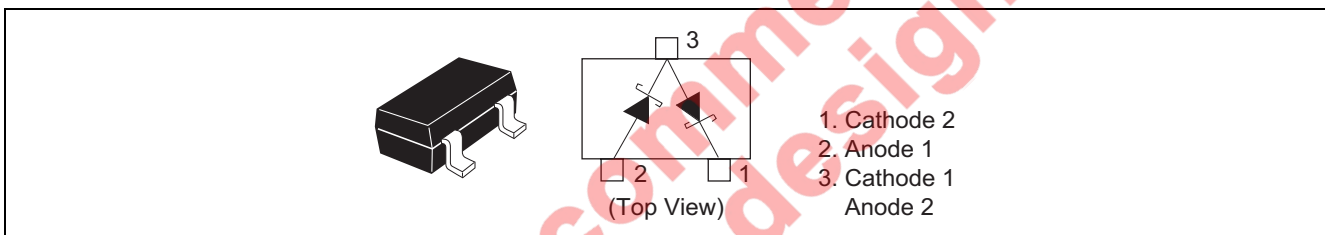
#### Features

- Low reverse current, Low capacitance.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

#### Ordering Information

Type No.	Laser Mark	Package Name	Package Code
HSM226S	S22	MPAK	PLSP0003ZC-A

#### Pin Arrangement



## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$	25	V
Non-Repetitive peak forward surge current	$I_{FSM}^{*1 *2}$	200	mA
Forward current	$I_F^{*2}$	50	mA
Junction temperature	$T_j$	125	°C
Storage temperature	$T_{stg}$	-55 to +125	°C

Notes: 1. 10 ms sine wave 1 pulse  
2. Two device total

## Electrical Characteristics \*

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	$V_{F1}$	—	—	0.33	V	$I_F = 1 \text{ mA}$
	$V_{F2}$	—	—	0.38		$I_F = 5 \text{ mA}$
Reverse current	$I_R$	—	—	450	nA	$V_R = 20 \text{ V}$
Capacitance	C	—	—	2.80	pF	$V_R = 1 \text{ V}, f = 1 \text{ MHz}$

Note: Per one device.

Not recommended for new designs

Main Characteristic

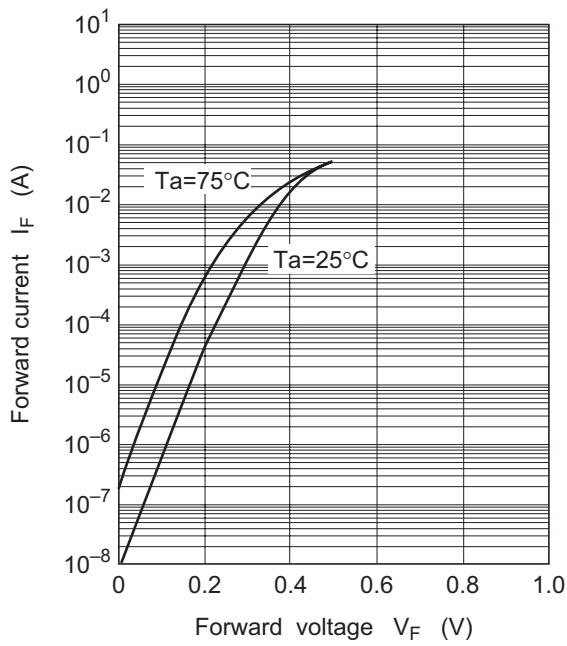


Fig.1 Forward current vs. Forward voltage

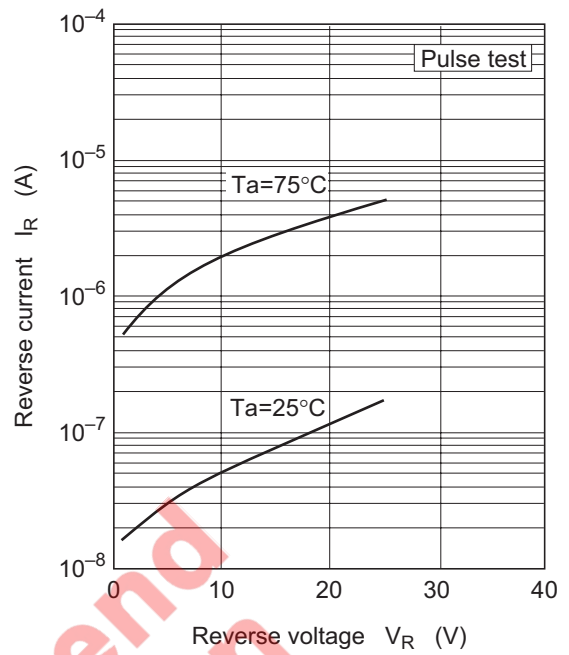


Fig.2 Reverse current vs. Reverse voltage

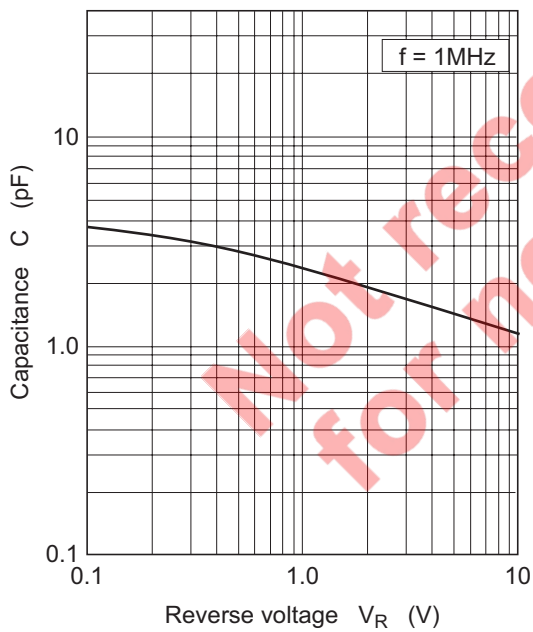
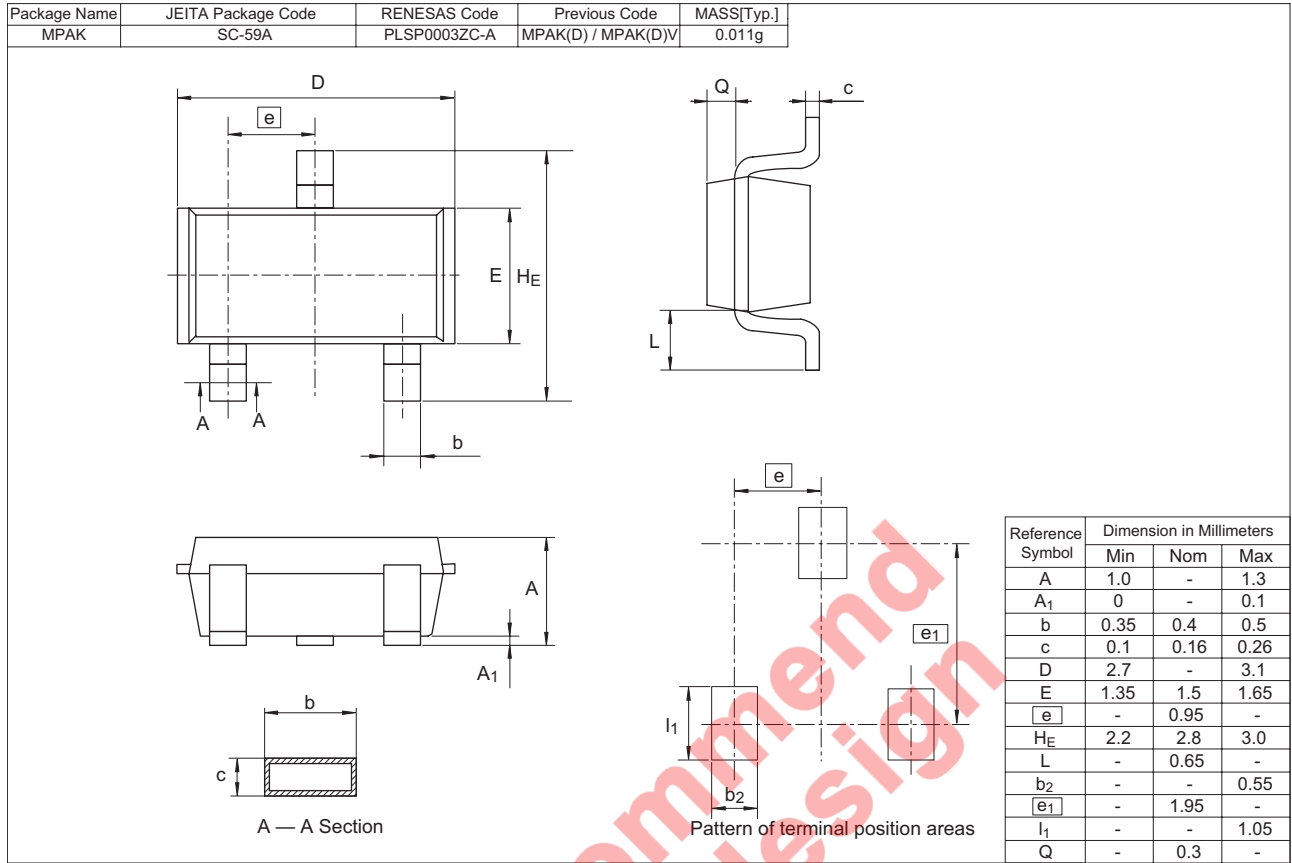


Fig.3 Capacitance vs. Reverse voltage

### Package Dimensions



Not recommended for new design

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