Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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Quad 2-Input NAND Gate

REJ03D0240–0200Z (Previous ADE-205-356 (Z)) Rev.2.00 Jul.16.2004

Features

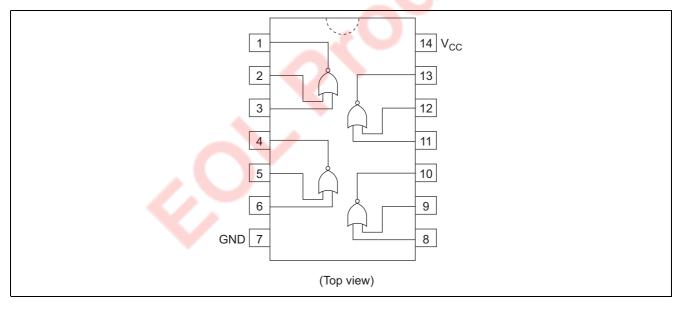
- Outputs Source/Sink 24 mA
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74AC02P	DIP-14 pin	DP-14, -14AV	Р	
HD74AC02FPEL	SOP-14 pin (JEITA)	FP-14DAV	FP	EL (2,000 pcs/reel)
HD74AC02RPEL	SOP-14 pin (JEDEC)	FP-14DNV	RP	EL (2,500 pcs/reel)
HD74AC02TELL	TSSOP-14 pin	TTP-14DV	Т	ELL (2,000 pcs/reel)

Notes: 1. Please consult the sales office for the above package availability.

2. The packages with lead-free pins are distinguished from the conventional products by adding V at the end of the package code.

Pin Arrangement





Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V _{cc}	–0.5 to 7	V	
DC input diode current	I _{IK}	-20	mA	$V_{1} = -0.5V$
		20	mA	$V_1 = Vcc+0.5V$
DC input voltage	V	-0.5 to Vcc+0.5	V	
DC output diode current	Ι _{οκ}	-50	mA	$V_0 = -0.5V$
		50	mA	$V_{o} = Vcc+0.5V$
DC output voltage	Vo	-0.5 to Vcc+0.5	V	
DC output source or sink current	I _o	±50	mA	
DC V_{cc} or ground current per output pin	I _{CC} , I _{GND}	±50	mA	
Storage temperature	Tstg	-65 to +150	°C	

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V _{cc}	2 to 6	V	
Input and output voltage	V _I , V _O	0 to V _{cc}	V	
Operating temperature	Та	-40 to +85	°C	
Input rise and fall time	tr, tf	8	ns/V	$V_{\rm CC} = 3.0 V$
(except Schmitt inputs)				V _{CC} = 4.5 V
V_{IN} 30% to 70% V_{CC}				V _{CC} = 5.5 V

DC Characteristics

ltem	Sym- Vcc		Ta = 25°C		Ta = -40 to +85°C		Unit	Condition	
	bol	(V)	min. typ. max.			min. max.			
Input Voltage	VIH	3.0	2.1	1.5		2.1	_	V	$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
1		4.5	3.15	2.25	<u> </u>	3.15	_		
		5.5	3.85	2.75	-	3.85	—		
	V _{IL}	3.0	-	1.50	0.9	—	0.9		$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	_	2.25	1.35	—	1.35		
		5.5	_	2.75	1.65	—	1.65		
Output voltage	V _{OH}	3.0	2.9	2.99	—	2.9	—	V	$V_{IN} = V_{IL} \text{ or } V_{IH}$
		4.5	4.4	4.49	—	4.4	—		I _{OUT} = -50 μA
		5.5	5.4	5.49	—	5.4	—		
		3.0	2.58	—	—	2.48	—		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OH} = -12 \text{ mA}$
		4.5	3.94	—	—	3.80	—		I _{OH} = -24 mA
		5.5	4.94	—	—	4.80	—		I _{OH} = -24 mA
	V _{OL}	3.0	_	0.002	0.1	—	0.1		$V_{IN} = V_{IL} \text{ or } V_{IH}$
		4.5	_	0.001	0.1	—	0.1		Ι _{ΟUT} = 50 μΑ
		5.5	—	0.001	0.1	—	0.1		
		3.0	—	—	0.32	—	0.37		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OL} = 12 \text{ mA}$
		4.5	_	_	0.32	—	0.37		I _{OL} = 24 mA
		5.5	_	—	0.32	—	0.37		I _{OL} = 24 mA
Input leakage current	I _{IN}	5.5	—	-	±0.1	-	±1.0	μA	$V_{IN} = V_{CC}$ or GND
Dynamic output	I _{OLD}	5.5	—	—	—	86		mA	V _{OLD} = 1.1 V
current*	I _{OHD}	5.5	—	—	—	-75		mA	V _{OHD} = 3.85 V
Quiescent supply current	I _{cc}	5.5	—	—	4.0	—	40	μA	$V_{IN} = V_{CC}$ or ground

*Maximum test duration 2.0 ms, one output loaded at a time.



AC Characteristics

			Ta = +25°C C _L = 50 pF		Ta = -40°C to +85°C C _L = 50 pF			
Item	Symbol	V _{cc} (V)* ¹	Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	3.3	1.0	5.0	7.5	1.0	8.0	ns
		5.0	1.0	4.0	6.0	1.0	6.5	
Propagation delay	t _{PHL}	3.3	1.0	5.0	7.5	1.0	8.0	ns
		5.0	1.0	4.5	6.5	1.0	7.0	

 Note:
 1.
 Voltage Range 3.3 is 3.3 V ± 0.3 V

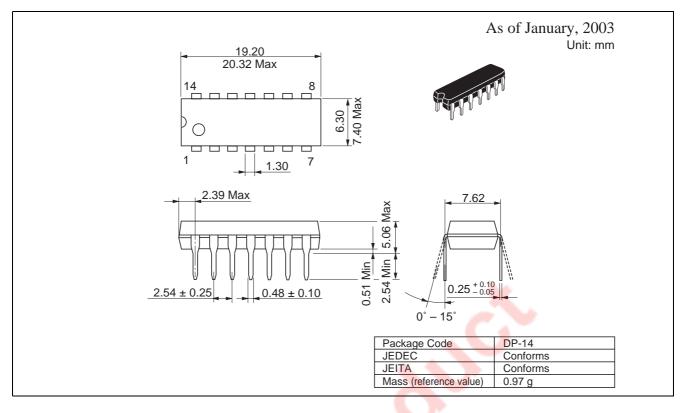
 Voltage Range 5.0 is 5.0 V ± 0.5 V

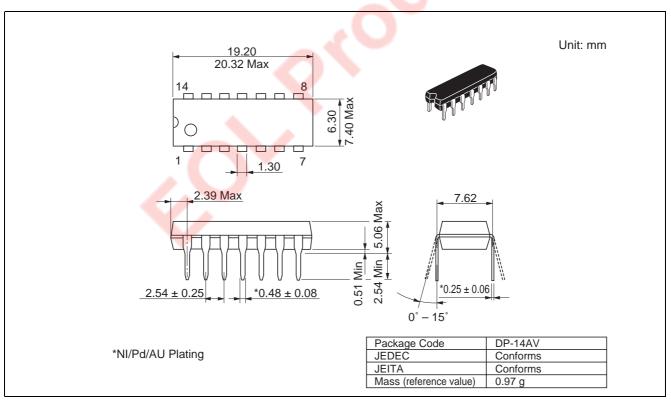
Capacitance

ltem	Symbol	Typ Unit		Condition	
Input capacitance	C _{IN}	4.5	pF	V _{cc} = 5.5 V	
Power dissipation capacitance	C _{PD}	30.0	pF	$V_{\rm CC} = 5.0 \text{ V}$	

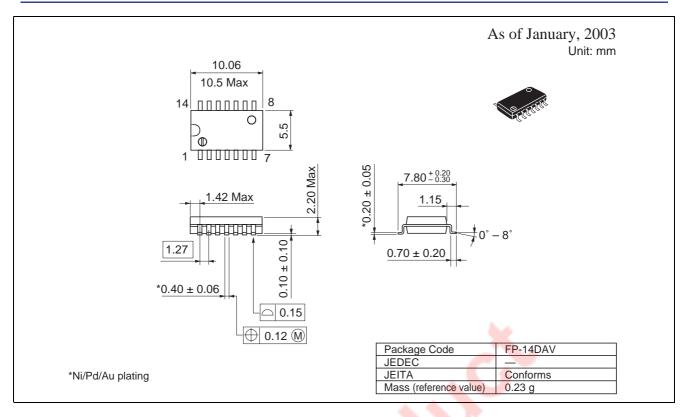
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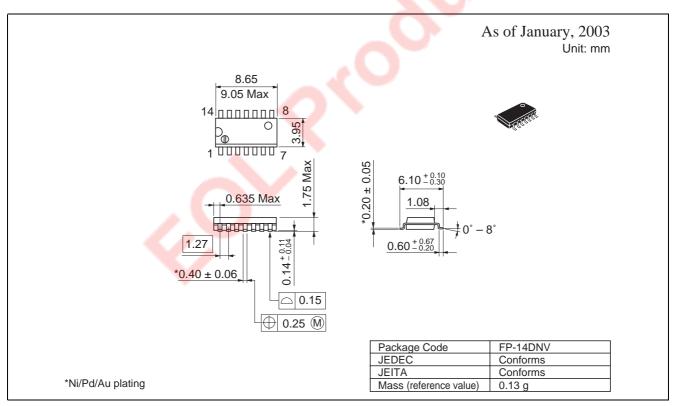
Package Dimensions



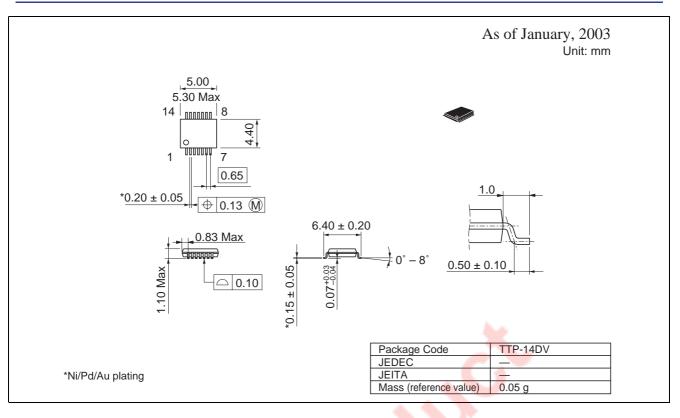














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