Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)
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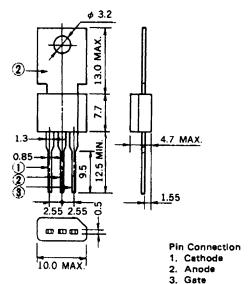


THYRISTOR 3S4M

HIGH SPEED SWITCHING 3 A (av.) MOLD THYRISTOR

PACKAGE DIMENSIONS

in millimeters



The 3S4M is P-gate all diffused mold type SCR granted Average on-state current 3 Amps ($T_c = 70$ °C), with rated voltages up to 400 Volts.

FEATURES

- Designed for Inverter, Pulse modulator, and other high frequency applications.
- Insured turn-off time of less than 5 µs.
- 65 A surge current.

APPLICATIONS

- Automatic gas lighter
- Speed control of miniature type motor
- Electric sewing machine
- Battery charger
- TV
- Solenoid operation
- Inverter

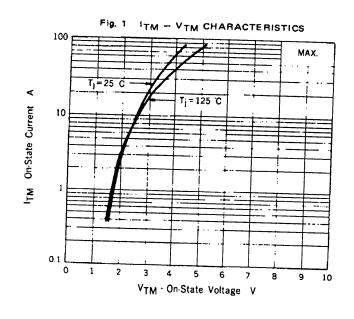
MAXIMUM RATINGS

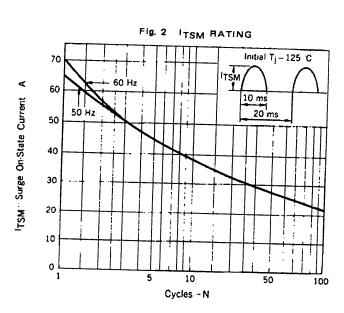
CHARACTERISTIC	SYMBOL	MAXIMUM RATINGS	UNIT	NOTE
Non-Repetitive Peak Reverse Voltage	VRSM	500	V	
Non-Repetitive Peak Off-State Voltage	VDSM	500	V	
Repetitive Peak Reverse Voltage	VRRM	400	٧	
Repetitive Peak Off-state Voltage	VDRM	400	٧	
On-State Current	lT(AV)	3 (T _C = 70 °C, θ = 180 ° Single phase half wave)	Α	See Fig. 4
Surge Non-Repetitive On-State Current	^I TSM	65	Α	See Fig. 2
Peak Gate Power Dissipation	P _{GM}	3 (f ≥50 Hz, Duty ≤10 %)	W	-
Average Gate Power Dissipation	PG(AV)	0.5	W	
Peak Gate Forward Current	IFGM	0.5 (f ≥ 50 Hz, Duty ≤ 10 %)	Α	
Peak Gate Reverse Voltage	VRGM	10	V	
Junction Temperature	Τj	-40 to +125	°C	
Storage Temperature	T _{stg}	-55 to +150	°C	
Weight	-	1.4	g	<u> </u>

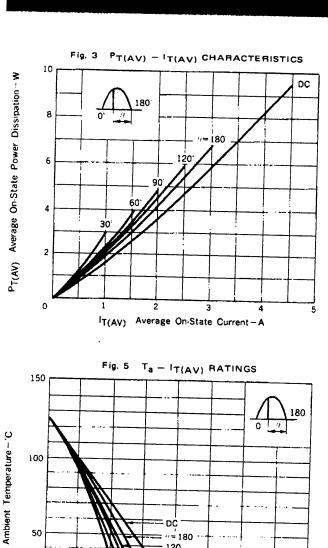
 T_{c} : Case Temperature is measured at 1.5 mm from the neck of Tablet.

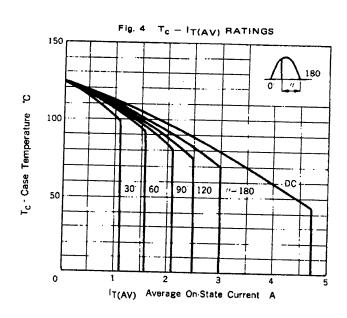
CHARACTERISTICS (Tj = 25 °C)

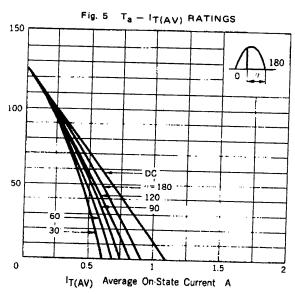
CHARACTERISTIC	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Repetitive Peak Off-State Current	IDRM	VDM = VDRM, T _j = 125 °C RGK = 1 kΩ	-	_	2.5	mA	HOIL
Repetitive Peak Reverse Current	IRRM	VRM = VRRM, T _j = 125 °C RGK = 1 kΩ	_	_	2.5	mA	
On-State Voltage	VTM	ITM = 3 A	 -		2.0	v	
Gate Trigger Current	1 _{GT}	V _{DM} = 6 V, R _L = 100 Ω	 				See Fig. 1
Gate Trigger Voltage	VGT	V _{DM} = 6 V, R _L = 100 Ω	 -		30	mA_	See Fig. 6
Gate Non-Trigger Voltage	VGD		 -		2.0		See Fig. 6
Critical Rate-of-Rise of Off-State Voltage		$V_{DM} = \frac{1}{2} V_{DRM}, T_j = 125 ^{\circ}C$	0.2			V	
	dv/dt	$V_{DM} = \frac{2}{3} V_{DRM}, T_j = 125 °C$	50	100	-	V/µs	
Holding Current	lH	V _D = 24 V	_	30		mA	
Circuit Commutated Turn-Off Time	^t q	$V_{DM} = \frac{2}{3} V_{DRM}, T_j = 125 ^{\circ}C$ $dv/dt = 50 V/\mu s, V_R \ge 25 V$ $I_T = 20 A, dI_R/dt = 10 A/\mu s$	-	~	5.0	μς	
Turn-On Time	tgt	$V_{DM} = \frac{2}{3} V_{DRM}, I_{TM} = 3 A$ $I_{G} = 0.1 A, t_{IG} = 5 \mu s$	-	_	3.0	μς	
Thermal Resistance	Ath(j-c)	Junction to Case			8	°C/W	
	Rth(j-a)	Junction to Ambient		_	75		See Fig. 9

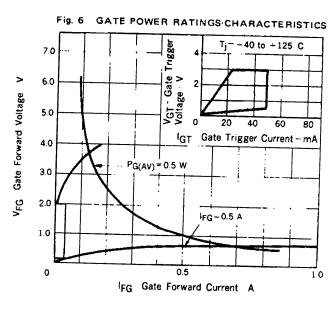


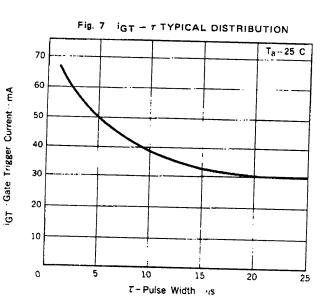


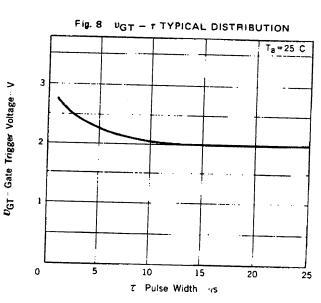


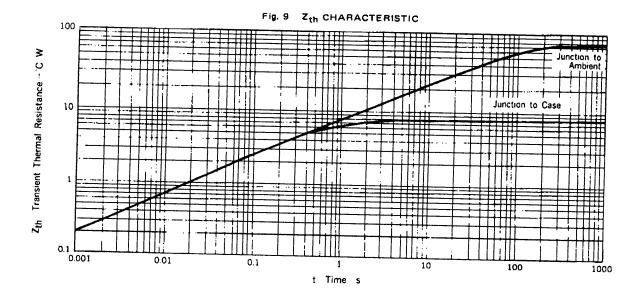






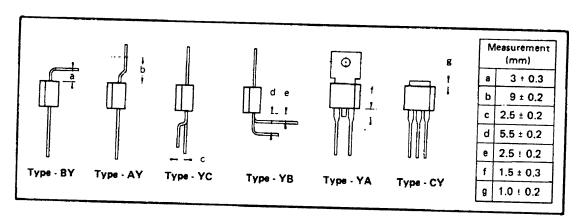






NOTICE FOR INSTALLATION

- 1. Electrode leads (especially heat sink tablet) are not granted to be bent because of wet-proof. However in case it is required inevitably, a mechanical stress should not be put on mold. Fix tightly between the mold case and the area to be formed or
- 2. Special lead and heat tab formings as indicated below are available at an additional cost.



- 3. The surface of heat sink for thermal radiator is to be smooth without any foreign matter.
- 4. Suitable torque value is 4 to 5 kg/cm.
- 5. Soldering

0	Recommended solder:	PbSu (4:6)
		Melting point 180 °C
Ç	Dimension from the necl	c of lead to dipping points
0	Soldering temperature ar	c of lead to dipping points
		250 °C