Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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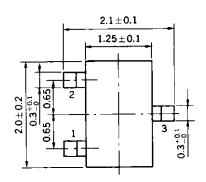
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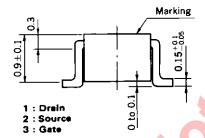


IUNCTION FIELD EFFECT TRANSISTOR 2SK852

AUDIO FREQUENCY AMPLIFIER N-CHANNEL SILICON JUNCTION FIELD EFFECT TRANSISTOR

PACKAGE DIMENSIONS in millimeters





FEATURES

- High Voltage V_{GDO} > −50 V
- High lyfs | lyfs | = 12 m\$ TYP.

ABSOLUTE MAXIMUM RATINGS

Maximum Voltages and Currents (Ta=25 °C)

Gate to Drain Voltage	V_{GDO}	-50	V
Gate to Source Voltage	V _{GSO}	-50	٧
Drain to Source Voltage $(V_{GS} = -2.0 \text{ V})$	V_{DSX}	50	V
Drain Current (DC)	ID	20	mΑ
Gate Current (DC)		10	mΑ

Maximum Power Dissipation

Total Power Dissipation
at 25 °C Ambient Temperature P_T 150 mW

Maximum Temperatures

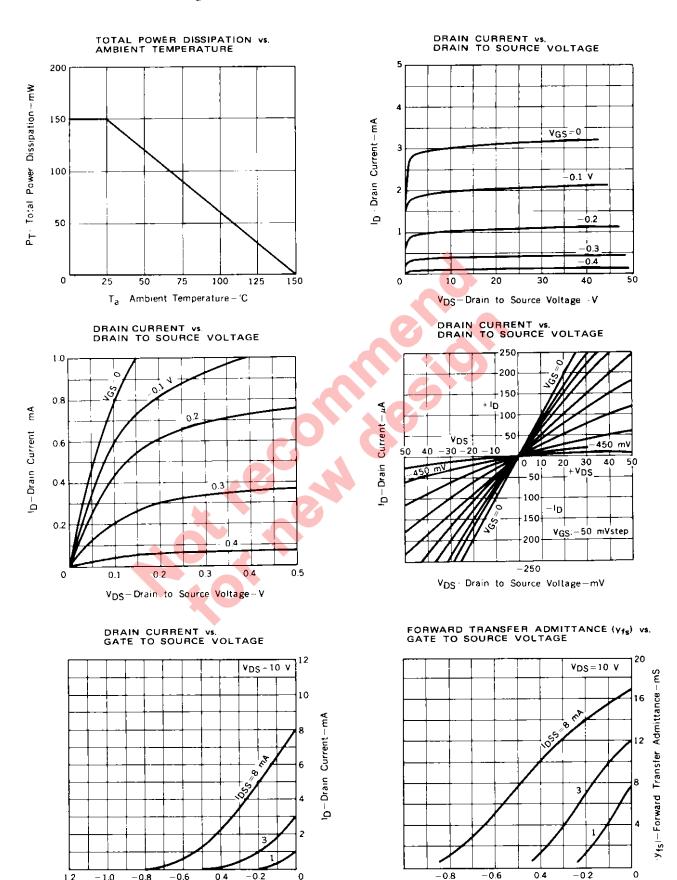
ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Gate Cutoff Current	IGSS			-5	nΑ	V _{GS} = -20 V, V _{DS} = 0
Zero-Gate Voltage Drain Current	DSS	0.5	2.5	12	mA	V _{DS} = 10 V, V _{GS} = 0
Gate to Source Cutoff Voltage	VGS(off)	-0.13	-0.5	-1.5	V	V _{DS} = 10 V, I _D = 10 μA
Forward Transfer Admittance	lyfsl1	4.0	5.2		mS	$V_{DS} = 10 \text{ V}, I_{D} = 0.5 \text{ mA}, f = 1.0 \text{ kHz}$
Forward Transfer Admittance	lyfsl2	4.0	12		m\$	V _{DS} = 10 V, V _{GS} = 0, f = 1.0 kHz
Input Capacitance	C _{iss}		13		pF	V _{DS} = 10 V, V _{GS} = 0, f = 1.0 MHz
Feedback Capacitance	C _{rss}		2.6		pF	V _{DS} = 10 V, V _{GS} = 0, f = 1.0 MHz

IDSS Classification

ſ	Marking	X1	X2	Х3	X4	
ſ	IDSS(mA)	0.5 to 1.5	1.0 to 3.0	2.0 to 6.0	4.0 to 12	

TYPICAL CHARACTERISTICS (Ta = 25 °C)



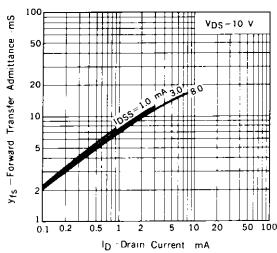
VGS-Gate to Source Voltage-V

1.2

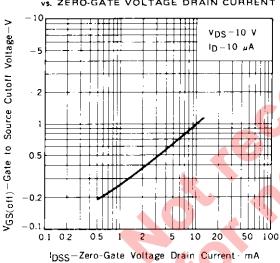
-1.0

 $V_{GS}-Gate$ to Source Voltage V

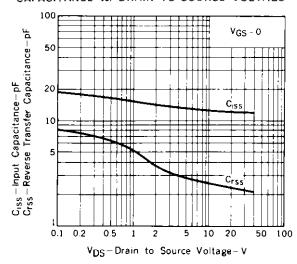




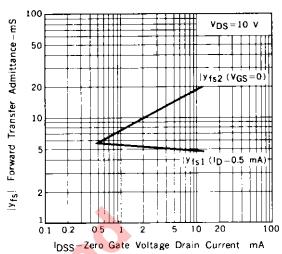
GATE TO SOURCE CUTOFF VOLTAGE vs. ZERO-GATE VOLTAGE DRAIN CURRENT



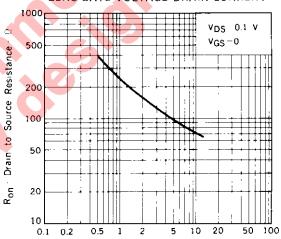
INPUT AND REVERSE TRANSFER CAPACITANCE VS. DRAIN TO SOURCE VOLTAGE



FORWARD THANSFER ADMITTANCE (yfs) vs. ZERO-GATE VOLTAGE CURRENT CO-RELATIONS



DRAIN TO SOURCE RESISTANCE VS. ZERO-GATE VOLTAGE DRAIN CURRENT



I_{DSS} Zero-Gate Voltage Drain Current -mA