Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)
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-CHANNEL MOS FIELD EFFECT POWER TRANSISTOR

Phase-out/Discontinued 2SK787

DESCRIPTION

The 2SK787 is N-channel MOS Field Effect Power Transistor designed for switching power supplies DC-DC converters.

FEATURES

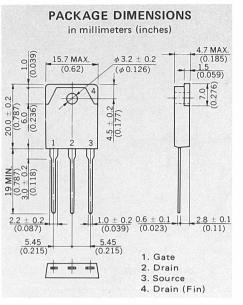
- Suitable for switching power supplies, actuater controls, and pulse circuits.
- Low R_{DS(on)}
- No second breakdown

ABSOLUTE MAXIMUM RATINGS

Maximum Temperatures

Maximum Power Dissipation ($T_c = 25$ °C)

Total Power Dissipation 150 V Maximum Voltages and Currents ($T_a = 25$ °C)



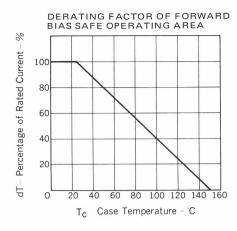
ELECTRICAL CHARACTERISTICS ($T_a = 25$ °C)

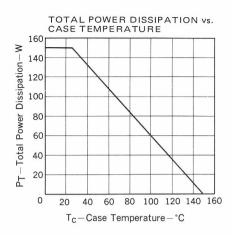
SYMBOL	CHARACTERISTIC	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
IDSS	Drain Leakage Current			100	μΑ	V _{DS} = 900 V, V _{GS} = 0
I _{GSS}	Gate to Source Leakage Current			±100	nΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
V _{GS(off)}	Gate to Source Cutoff Voltage	1.5		3.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
ly _{fs} l	Forward Transfer Admittance	1.0			S	$V_{DS} = 10 \text{ V}, I_{D} = 4 \text{ A}$
R _{DS(on)}	Drain to Source On-State Resistance		1.25	1.6	Ω	$V_{GS} = 10 \text{ V}, I_D = 4 \text{ A}$
Ciss	Input Capacitance		2500		pF)	
Coss	Output Capacitance		400		pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$
C _{rss}	Reverse Transfer Capacitance		200		pF	
^t d(on)	Turn-On Delay Time		40		ns	$I_D = 4 A, V_{CC} = 150 V$
t _r	Rise Time		55		ns	V _{GS(on)} = 10 V
^t d(off)	Turn-Off Delay Time		200		ns	$R_L = 37.5 \Omega$
t _f ~	Fall Time		40		ns	$R_{in} = 10 \Omega$

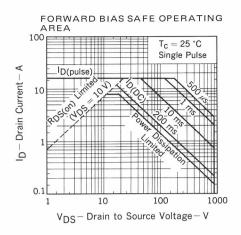
^{*} PW \leq 100 μ s, Duty Cycle \leq 2 %

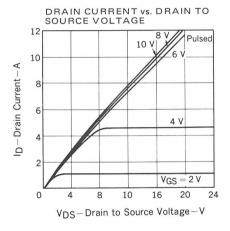
Phase-out/Discontinued

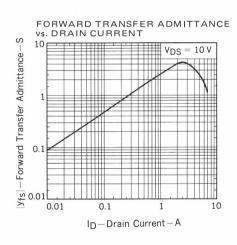
TYPICAL CHARACTERISTICS (T_a = 25 °C)

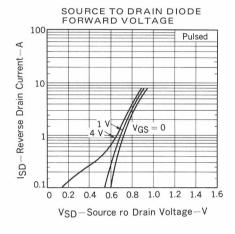


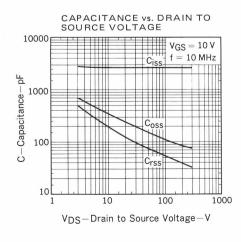


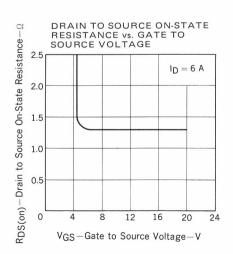


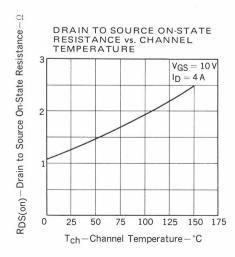




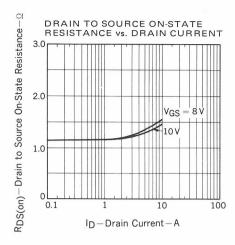


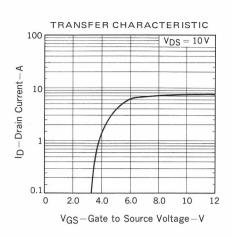


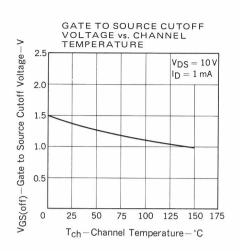


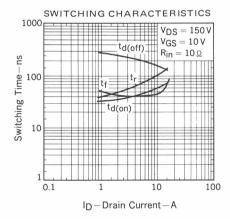


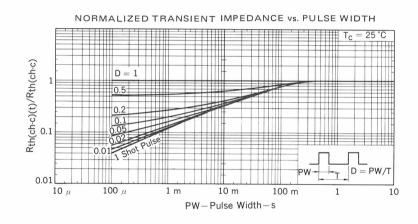
Phase-out/Discontinued



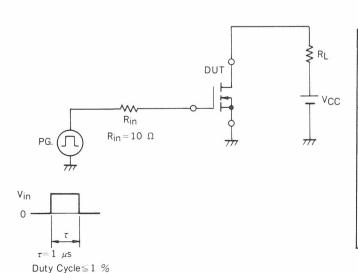


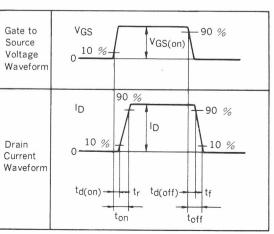






SWITCHING TIME TEST CIRCUIT







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