

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

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(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.

Phase-out/Discontinued

2SK679

FEATURES

- Suitable for switching power supplies, actuator controls, and pulse circuits
- Low $R_{DS(on)}$; $R_{DS(on)} = 0.95 \Omega$ TYP.
($V_{GS} = 4 V$, $I_D = 0.5 A$)
- No second breakdown

ABSOLUTE MAXIMUM RATINGS

Maximum Temperatures

Storage Temperature -55 to $+150^\circ C$

Channel Temperature $150^\circ C$ Maximum

Maximum Power Dissipation ($T_a = 25^\circ C$)

Total Power Dissipation $0.75 W$

Maximum Voltages and Currents ($T_a = 25^\circ C$)

V_{DS} Drain to Source Voltage $30 V$

V_{GS} Gate to Source Voltage $\pm 20 V$

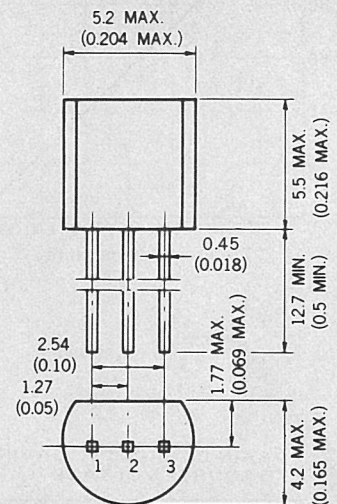
$I_{D(DC)}$ Drain Current (DC) $\pm 1.0 A$

$I_{D(pulse)}$ Drain Current (pulse)* $\pm 2.0 A$

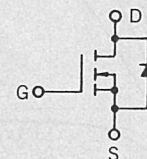
* $PW \leq 300 \mu s$, Duty Cycle $\leq 10 \%$

PACKAGE DIMENSIONS

in millimeters (inches)

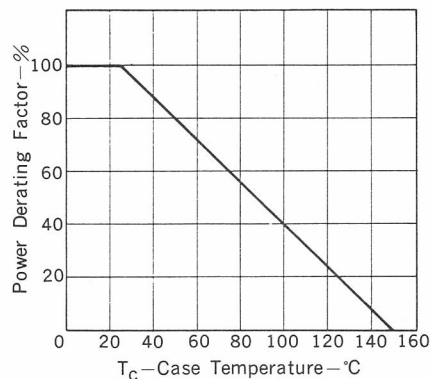
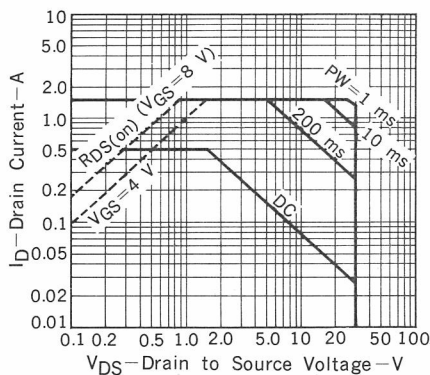
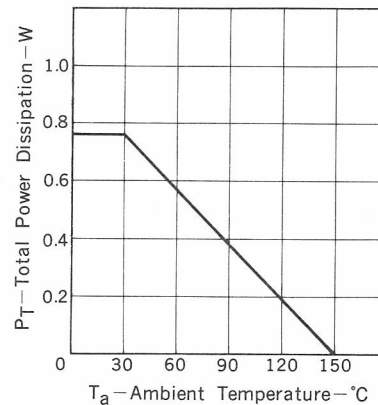
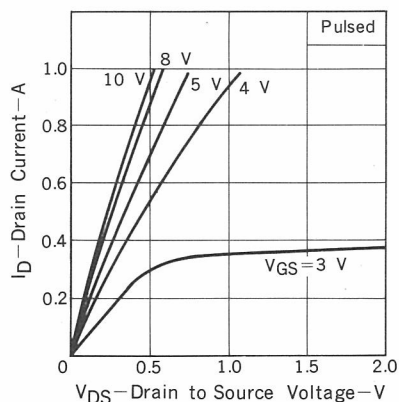
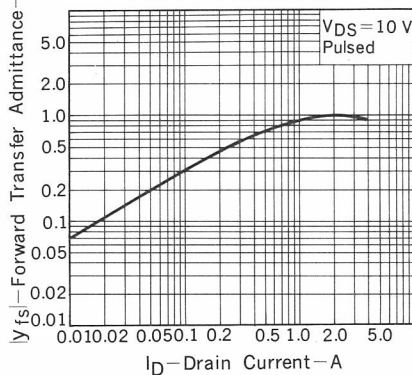
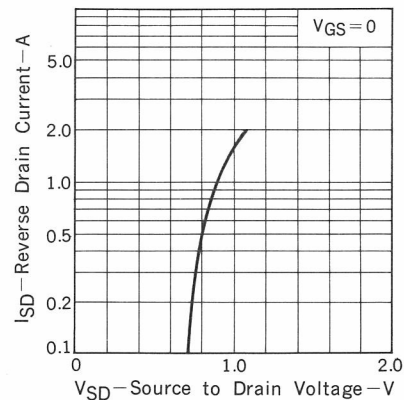
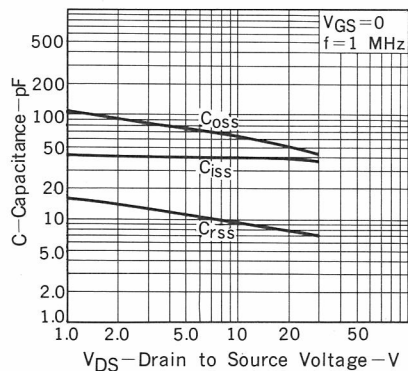
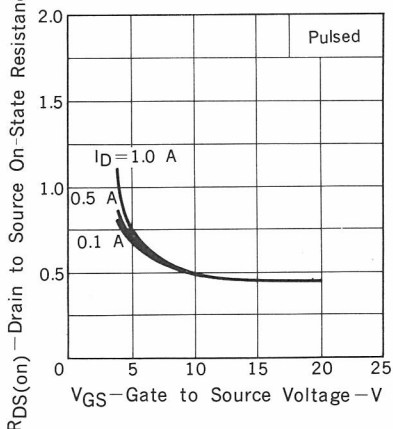
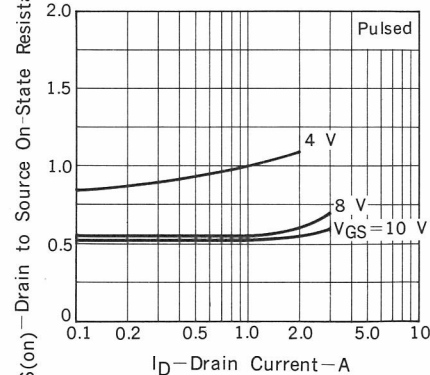


- | | | |
|---------------|-------|----------|
| 1. Gate (G) | EIAJ | : SC-43B |
| 2. Drain (D) | JEDEC | : TO-92 |
| 3. Source (S) | IEC | : PA33 |

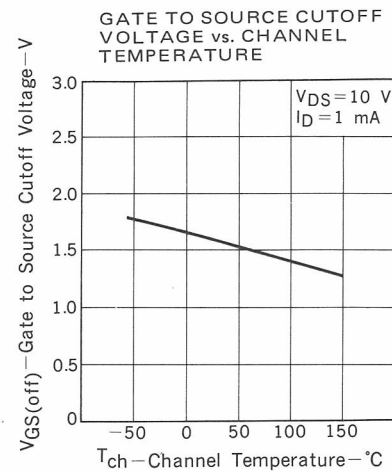
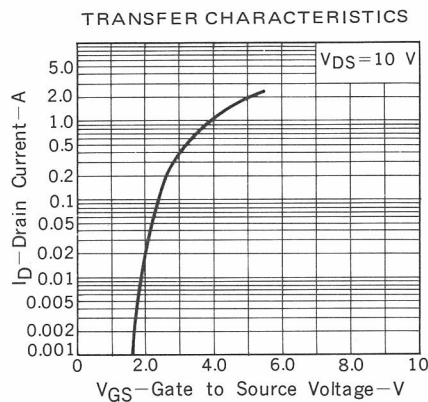
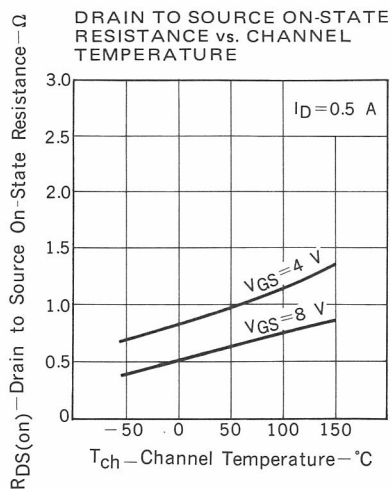


ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ C$)

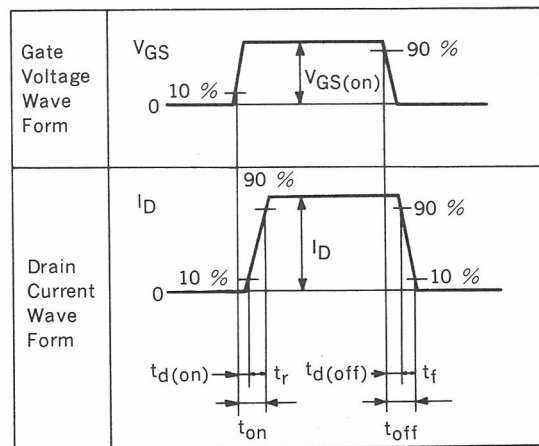
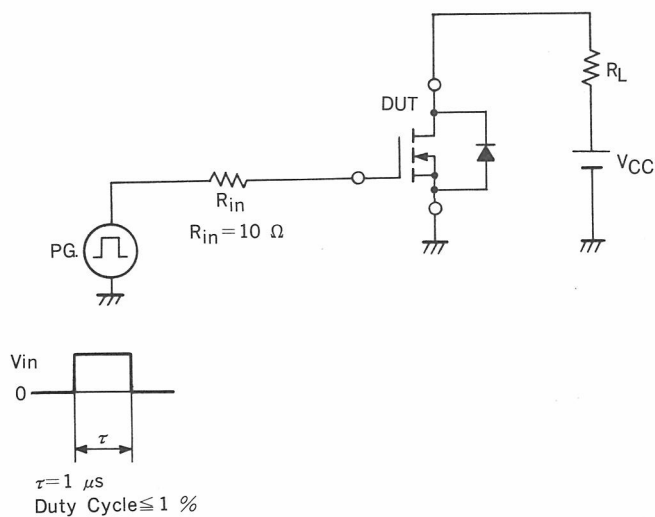
SYMBOL	CHARACTERISTIC	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
I_{DSS}	Drain Leakage Current			10	μA	$V_{DS} = 20 V$, $V_{GS} = 0$
I_{GSS}	Gate to Source Leakage Current			± 100	nA	$V_{GS} = \pm 20 V$, $V_{DS} = 0$
$V_{GS(off)}$	Gate to Source Cutoff Voltage	1.0		2.5	V	$V_{DS} = 10 V$, $I_D = 1.0 mA$
$ y_{fs} $	Forward Transfer Admittance	0.4			S	$V_{DS} = 10 V$, $I_D = 0.5 A$
$R_{DS(on)}$	Drain to Source On-State Resistance		0.5	1.0	Ω	$V_{GS} = 10 V$, $I_D = 0.5 A$
$R_{DS(on)}$	Drain to Source On-State Resistance		0.95	1.5	Ω	$V_{GS} = 4.0 V$, $I_D = 0.5 A$
C_{iss}	Input Capacitance		40		pF	$V_{DS} = 10 V$, $V_{GS} = 0$, $f = 1 MHz$
C_{oss}	Output Capacitance		65		pF	
C_{rss}	Reverse Transfer Capacitance		10		pF	
$t_{d(on)}$	Turn-On Delay Time		60		ns	$I_D = 0.5 A$, $V_{CC} = 25 V$ $V_{GS(on)} = 10 V$ $R_L = 50 \Omega$ $R_{in} = 10 \Omega$
t_r	Rise Time		180		ns	
$t_{d(off)}$	Turn-Off Delay Time		550		ns	
t_f	Fall Time		400		ns	

TYPICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)DERATING FACTOR OF
FORWARD BIAS SAFE
OPERATING AREAFORWARD BIAS SAFE
OPERATING AREATOTAL POWER DISSIPATION
vs. AMBIENT TEMPERATUREDRAIN CURRENT vs. DRAIN
TO SOURCE VOLTAGEFORWARD TRANSFER ADMIT-
TANCE vs. DRAIN CURRENTSOURCE TO DRAIN DIODE
FORWARD VOLTAGECAPACITANCE vs. DRAIN TO
SOURCE VOLTAGEDRAIN TO SOURCE ON-STATE
RESISTANCE vs. GATE TO
SOURCE VOLTAGEDRAIN TO SOURCE ON-STATE
RESISTANCE vs. DRAIN
CURRENT

Phase-out/Discontinued



TURN-ON AND TURN-OFF TIME TEST CIRCUIT



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