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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# MOS FIELD EFFECT TRANSISTOR 2SK1824

## N-CHANNEL MOS FET FOR SWITCHING

#### **DESCRIPTION**

The 2SK1824 is a N-channel vertical type MOS FET that is driven at 2.5 V.

Because this MOS FET can be driven on a low voltage and because it is not necessary to consider the drive current, the 2SK1824 is ideal for driving the actuator of power-saving systems, such as VCR cameras and headphone stereo systems.

Moreover, the 2SK1824 is housed in a super small mini-mold package so that it can help increase the mounting density on the printed circuit board and lower the mounting cost, contributing to miniaturization of the application systems.

#### **FEATURES**

- Small mounting area: about 60% of the conventional mini-mold package (SC-70)
- · Can be automatically mounted
- · Can be directly driven by 3-V IC

#### **★ ORDERING INFORMATION**

| PART NUMBER | PACKAGE     | D. |
|-------------|-------------|----|
| 2SK1824     | SC-75 (USM) |    |

Marking: B1

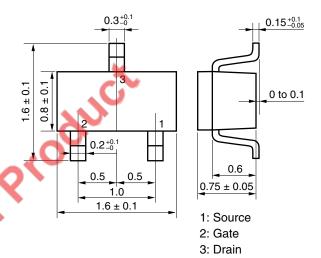
#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

| Drain to Source Voltage (V <sub>GS</sub> = 0 V) | Voss      | 30          | V  |
|---|-----------|-------------|----|
| Gate to Source Voltage (VDS = 0 V)              | Vgss      | ±7.0        | V  |
| Drain Current (DC)                              | ID(DC)    | ±100        | mΑ |
| Drain Current (pulse) Note1                     | ID(pulse) | ±200        | mΑ |
| Total Power Dissipation Note2                   | Рт        | 200         | mW |
| Channel Temperature                             | Tch       | 150         | °C |
| Storage Temperature                             | $T_{stg}$ | -55 to +150 | °C |

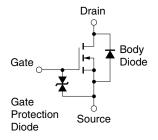
**Notes 1.** PW  $\leq$  10 ms, Duty Cycle  $\leq$  50%

2. Mounted on ceramic substrate of 3.0 cm<sup>2</sup> x 0.64 mm

#### **★ PACKAGE DRAWING (Unit: mm)**



#### **EQUIVALENT CIRCUIT**



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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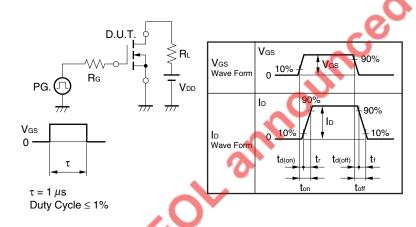
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#### **ELECTRICAL CHARACTERISTICS (TA = 25°C)**

| CHARACTERISTICS                          | SYMBOL               | TEST CONDITIONS                                  | MIN. | TYP. | MAX. | UNIT |
|--|----------------------|--|------|------|------|------|
| Zero Gate Voltage Drain Current          | IDSS                 | V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V    |      |      | 1.0  | μΑ   |
| Gate Leakage Current                     | Igss                 | V <sub>GS</sub> = ±5.0 V, V <sub>DS</sub> = 0 V  |      | ±0.1 | ±3.0 | μΑ   |
| Gate Cut-off Voltage                     | V <sub>GS(off)</sub> | $V_{DS} = 3.0 \text{ V}, I_{D} = 10 \mu\text{A}$ | 0.8  | 1.0  | 1.5  | V    |
| Forward Transfer Admittance Note         | yfs                  | V <sub>DS</sub> = 3.0 V, I <sub>D</sub> = 10 mA  | 20   | 50   |      | mS   |
| Drain to Source On-state Resistance Note | RDS(on)1             | V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 1.0 mA |      | 7.0  | 13   | Ω    |
|  | RDS(on)2             | V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 10 mA  |      | 5.0  | 8.0  | Ω    |
| Input Capacitance                        | Ciss                 | V <sub>DS</sub> = 5.0 V                          |      | 16   |      | pF   |
| Output Capacitance                       | Coss                 | V <sub>GS</sub> = 0 V                            |      | 14   |      | pF   |
| Reverse Transfer Capacitance             | Crss                 | f = 1 MHz  |      | 2.0  |      | pF   |
| Turn-on Delay Time                       | td(on)               | V <sub>DD</sub> = 5.0 V, I <sub>D</sub> = 10 mA  |      | 15   |      | ns   |
| Rise Time                                | tr                   | V <sub>GS</sub> = 5.0 V                          | .(   | 20   |      | ns   |
| Turn-off Delay Time                      | td(off)              | R <sub>G</sub> = 10 Ω                            | V    | 100  |      | ns   |
| Fall Time                                | tf                   |  | 7    | 100  |      | ns   |

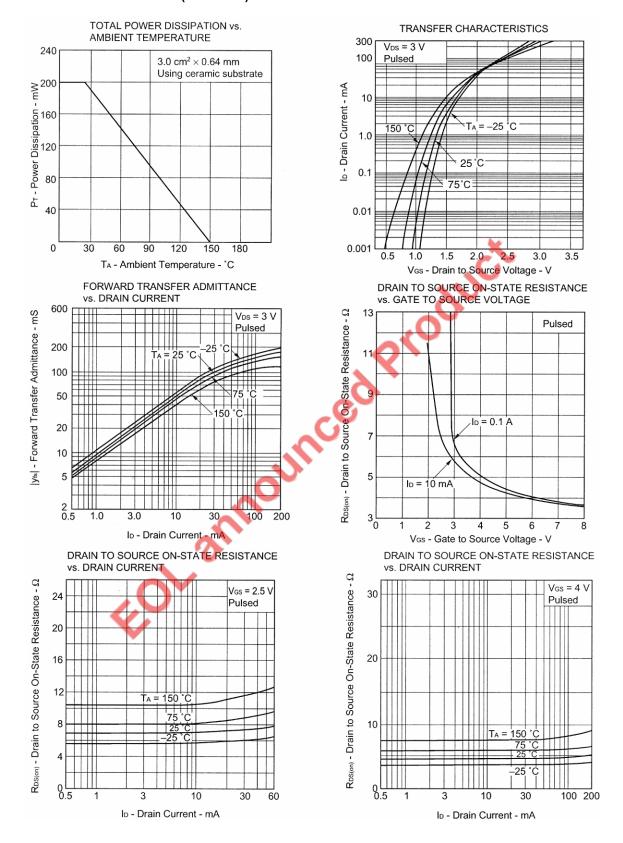
**Note** Pulsed: PW  $\leq$  350  $\mu$ s, Duty Cycle  $\leq$  2%

#### **★** TEST CIRCUIT SWITCHING TIME

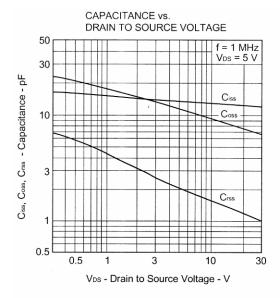


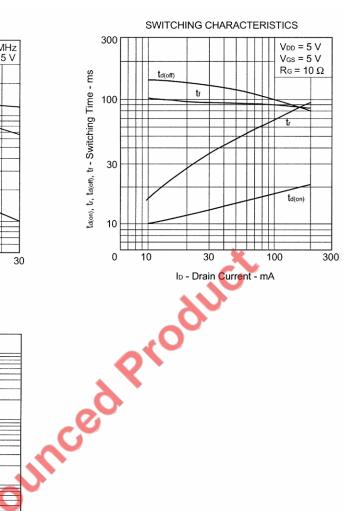


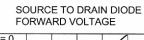
#### TYPICAL CHARACTERISTICS (TA = 25°C)

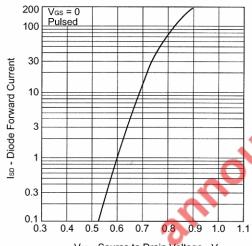


Data Sheet D11220EJ2V0DS 3









Vsp - Source to Drain Voltage - V

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