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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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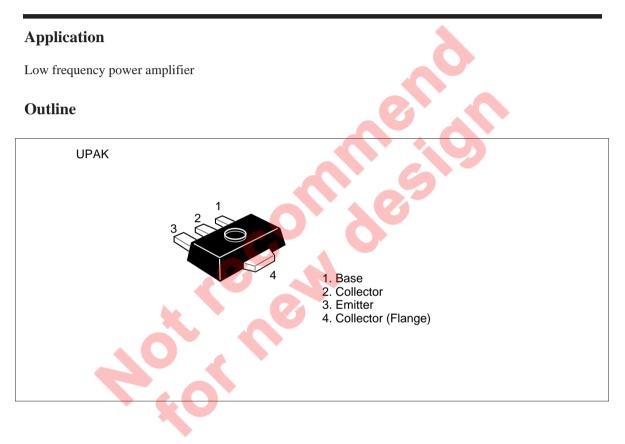
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Silicon NPN Epitaxial

RENESAS

ADE-208-1145 (Z) 1st. Edition Mar. 2001



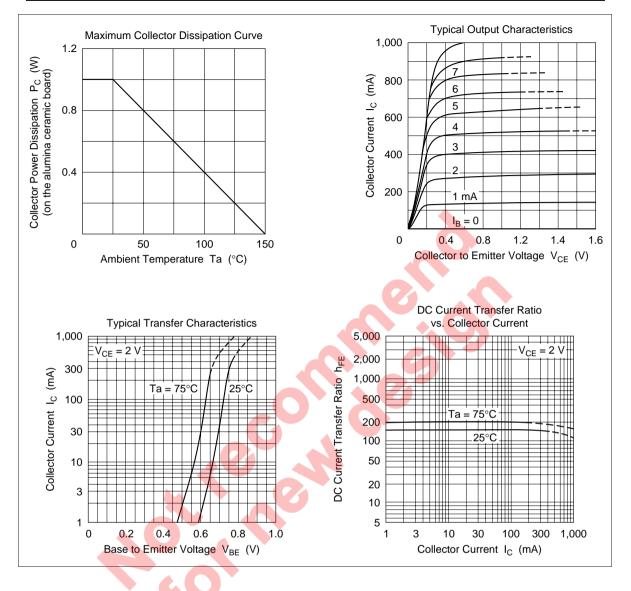
Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

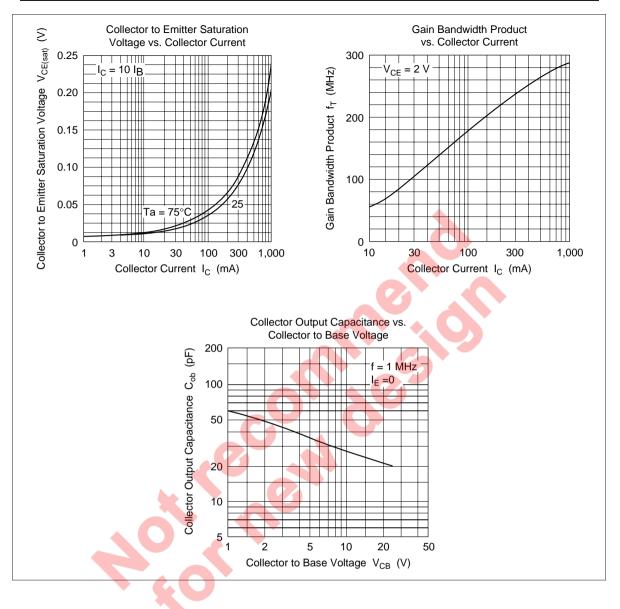
Item	Symbol	Ratings	Unit			
Collector to base voltage	V _{CBO}	25	V			
Collector to emitter voltage	V _{CEO}	20	V			
Emitter to base voltage	V _{EBO}	5	V			
Collector current	I _c	1	A			
Collector peak current	i _{C(peak)} *1	1.5	A			
Collector power dissipation	P _c * ²	1	W			
Junction temperature	Тј	150	°C			
Storage temperature	Tstg	-55 to +150	°C			
 Notes: 1. PW ≤ 10 ms, Duty cycle ≤ 20%. 2. Value on the alumina ceramic board (12.5 × 20 × 0.7 mm) 						
Electrical Characteristics (Ta = 25°C)						

Electrical Characteristics (Ta = 25°C)

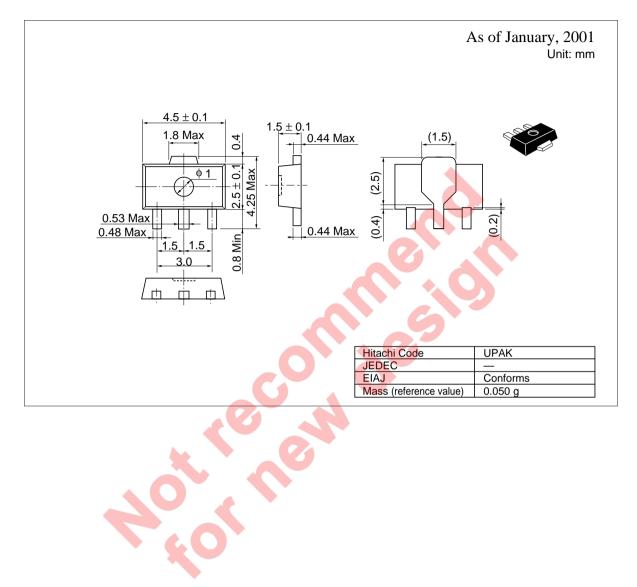
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(\text{BR})\text{CBO}}$	25		3	V	$I_{c} = 10 \ \mu A, \ I_{e} = 0$
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	20	_	0	V	$I_{\rm C}$ = 1 mA, $R_{\rm BE}$ = ∞
Emitter to base breakdown voltage	V _{(BR)EBO}	5	1	—	V	$I_{\rm E} = 10 \ \mu A, \ I_{\rm C} = 0$
Collector cutoff current	I _{CBO}	-0	—	0.1	μΑ	$V_{cB} = 20 \text{ V}, \text{ I}_{E} = 0$
Emitter cutoff current	EBO	A		0.1	μΑ	$V_{EB} = 4 V, I_{C} = 0$
DC current transfer ratio	h _{FE} *1	85	_	240		V_{ce} = 2 V, I_c = 0.5 A, Pulse
Collector to emitter saturation voltage	V _{CE(sat)}	_	0.15	0.3	V	$I_{\rm C} = 0.8$ A, $I_{\rm B} = 0.08$ A, Pulse
Base to emitter saturation voltage	V _{BE(sat)}	—	0.9	1.0	V	$I_{c} = 0.8 \text{ A}, I_{B} = 0.08 \text{ A}, \text{Pulse}$
Gain bandwidth product	f _T	—	240	—	MHz	V_{ce} = 2 V, I_c = 0.5 A, Pulse
Collector output capacitance	Cob	—	22	—	pF	$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$
Note: 1. The 2SD1366 is grouped by h _{FE} as follows.						
Mark AA A	В	_				
h _{FE} 85 to 170 12	20 to 240	-				







Package Dimensions



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