

## Supplemental Information

This Document Errata reflects changes required for device 85211BI-03.

## Revision History

**January 27, 2010:** First version of documentation errata for this device. There is no change to the design of the device. This is only a change to the datasheet to correct an error that did not accurately specify the correct input resistor configuration.

## Errata Items

**Page 2, Table 1** Pin Description Table - changed pin 6 resistor (nCLK) from a Pullup/Pulldown to Pulldown.

**Page 3, Table 4B** Differential DC Characteristics Table - changed I<sub>IH</sub> CLK from 150uA to 5uA max. Changed I<sub>IL</sub> nCLK from -150uA to -5uA min., and CLK from -5uA to -150uA min.

FROM:

**Table 1. Pin Descriptions**

Number	Name	Type	Description
6	nCLK	Input	Pullup/ Pulldown
			Inverting differential clock input. $V_{DD}/2$ default when left floating.

**Table 4B. Differential DC Characteristics,  $V_{DD} = 3.3V \pm 5\%$ ,  $T_A = -40^\circ C$  to  $85^\circ C$**

Symbol	Parameter	Test Conditions	Minimum	Typical	Maximum	Units
I <sub>IH</sub>	Input High Current	nCLK	$V_{DD} = V_{IN} = 3.465V$		150	$\mu A$
		CLK	$V_{DD} = V_{IN} = 3.465V$		150	$\mu A$
I <sub>IL</sub>	Input Low Current	nCLK	$V_{DD} = 3.465V, V_{IN} = 0V$	-150		$\mu A$
		CLK	$V_{DD} = 3.465V, V_{IN} = 0V$	-5		$\mu A$

TO:

**Table 1. Pin Descriptions**

Number	Name	Type	Description
6	nCLK	Input	Pulldown
			Inverting differential clock input.

**Table 4B. Differential DC Characteristics,  $V_{DD} = 3.3V \pm 5\%$ ,  $T_A = -40^\circ C$  to  $85^\circ C$**

Symbol	Parameter	Test Conditions	Minimum	Typical	Maximum	Units
I <sub>IH</sub>	Input High Current	CLK	$V_{DD} = V_{IN} = 3.465V$		5	$\mu A$
		nCLK	$V_{DD} = V_{IN} = 3.465V$		150	$\mu A$
I <sub>IL</sub>	Input Low Current	CLK	$V_{DD} = 3.465V, V_{IN} = 0V$	-150		$\mu A$
		nCLK	$V_{DD} = 3.465V, V_{IN} = 0V$	-5		$\mu A$