

Supplemental Information

This Document Errata reflects changes made on the datasheet for device 843002AKI-41 and 843002AKI-41LF.

Revision History

March 13, 2009: Second version of documentation errata for this device. There is no change to the actual characteristics or design of the device. This is only a change to the datasheet to correct a parameter incorrectly specified.

Errata Items

Item #1: Table 5 – AC Characteristics

Page	Description of Change
7	Correct the Maximum Output Skew (tsk (o)).

From:

Table 5. AC Characteristics, $V_{CC} = 3.3V \pm 5\%$, $V_{CC0_LVCMOS} = V_{CC0_LVPECL} = 3.3V \pm 5\%$ or $2.5V \pm 5\%$, $V_{EE} = 0V$, $T_A = -40^\circ C$ to $85^\circ C$

Symbol	Parameter	Test Conditions	Minimum	Typical	Maximum	Units
f_{OUT}	Output Frequency		19.44		700	MHz
$t_{sk(o)}$	Output Skew; NOTE 1, 2				50	ps

To:

Table 5. AC Characteristics, $V_{CC} = 3.3V \pm 5\%$, $V_{CC0_LVCMOS} = V_{CC0_LVPECL} = 3.3V \pm 5\%$ or $2.5V \pm 5\%$, $V_{EE} = 0V$, $T_A = -40^\circ C$ to $85^\circ C$

Symbol	Parameter	Test Conditions	Minimum	Typical	Maximum	Units
f_{OUT}	Output Frequency		19.44		700	MHz
$t_{sk(o)}$	Output Skew; NOTE 1, 2				150	ps