Supplemental Information

This Document Errata reflects changes required for the device datasheet of 844003AKI-04LF.

Revision History

March 24, 2011: First version of documentation errata for the affected device.

Errata Item

Page 6, Table 4A .

CURRENT:

DC Electrical Characteristics

Table 4A. Power Supply DC Characteristics, $V_{DD} = V_{DDO\_A} = V_{DDO\_B} = 3.3V \pm 10\%$, $T_A = -40°C$ to $85°C$

Symbol	Parameter	Test Conditions	Minimum	Typical	Maximum	Units
$I_{DDO\_A} + I_{DDO\_B}$	Output Supply Current				55	mA

CHANGE TO:

DC Electrical Characteristics

Table 4A. Power Supply DC Characteristics, $V_{DD} = V_{DDO\_A} = V_{DDO\_B} = 3.3V \pm 10\%$, $T_A = -40°C$ to $85°C$

Symbol	Parameter	Test Conditions	Minimum	Typical	Maximum	Units
$I_{DDO\_A} + I_{DDO\_B}$	Output Supply Current				70	mA

Page 8, Table 6

CURRENT:

AC Electrical Characteristics

Table 6. AC Characteristics, $V_{DD} = V_{DDO} = 3.3V \pm 5\%$, $T_A = -40°C$ to $85°C$

Symbol	Parameter		Test Conditions	Minimum	Typical	Maximum	Units
$t$sk(b)	Bank Skew; NOTE 1					25	ps
$t$sk(o)	Output Skew	NOTE 2, 3	Outputs @ Same Frequency			50	ps
		NOTE 2, 3, 4	QB ≠ 1, Outputs @ Different Frequencies			250	ps
			QB = 1, Outputs @ Different Frequencies			525	ps
$t$jit(Ø)	RMS Phase Jitter, Random; NOTE 5		625MHz, (1.875MHz - 20MHz)		0.34		ps
			312.5MHz, (1.875MHz - 20MHz)		0.34		ps
			250MHz, (1.875MHz - 20MHz)		0.42		ps
			125MHz, (1.875MHz - 20MHz)		0.50		ps
			100MHz, (1.875MHz - 20MHz)		0.41		ps

NOTE 4: Characterized using output dividers 1, 2, 4, 8.
NOTE 5: Please refer to the Phase Noise Plots.

CHANGE TO:

AC Electrical Characteristics

Table 6. AC Characteristics, $V_{DD} = V_{DDO} = 3.3V \pm 5\%$, T_A = -40°C to 85°C

Symbol	Parameter		Test Conditions	Minimum	Typical	Maximum	Units
$t$sk(b)	Bank Skew; NOTE 1					25	ps
$t$sk(o)	Output Skew	NOTE 2, 3	Outputs @ Same Frequency			50	ps
		NOTE 2, 3, 4	QB ≠ 1, Outputs @ Different Frequencies			250	ps
		NOTE 2, 3, 5	QB = 1, Outputs @ Different Frequencies			525	ps
$t$jit(Ø)	RMS Phase Jitter, Random; NOTE 6		625MHz, (1.875MHz - 20MHz)		0.34		ps
			312.5MHz, (1.875MHz - 20MHz)		0.34		ps
			250MHz, (1.875MHz - 20MHz)		0.42		ps
			125MHz, (1.875MHz - 20MHz)		0.50		ps
			100MHz, (1.875MHz - 20MHz)		0.41		ps

NOTE 4: Characterized with DIV_SELA[1:0] = 11 and DIV_SELB[1:0] = 11.
NOTE 5: Characterized with DIV_SELA[1:0] = 00 and DIV_SELB[1:0] = 00.
NOTE 6: Please refer to the Phase Noise Plots.