

Supplemental Information

This notice describes the differences between the updated version (Version 3, dated Feb 6, 2009) and its previous version (Version 2, dated January 11, 2007) of the IDT82P2816 data sheet. It helps readers to identify the changes when the data sheet is upgraded.

Revision History

Revision Date	PCN Number (if applicable)	Date Code	Changed Items
February 6, 2009	-	-	9
January 11, 2007	-	-	8
December 7, 2005	-	-	1-7

Changed Items

February 6, 2009

Item 9: Changed 'MCLK should be a jitter-free clock with ± 32 ppm (in T1/J1 mode) or ± 50 ppm (in E1 mode) accuracy' to 'MCLK should be a clock with ± 32 ppm (in T1/J1 mode) or ± 50 ppm (in E1 mode) accuracy'. (Page 18, 56, 61)

January 11, 2007

Item 8: Changed the Min and Max values of 'Output Pulse Amplitude' in 'T1/J1 TRANSMITTER ELECTRICAL CHARACTERISTICS' (Page 124)

December 7, 2005

Item 7: Changed the Pd value in '8.1 Absolute Maximum Ratings' (Page 116)

Item 6: Changed the thermal data table and the corresponding data in 3 equations (Page 115)

Item 5: Changed Figure-6, Figure-54 and Figure-55 (Page 26, 131)

Item 4: Changed test conditions for Analog LOS Level (Line Monitor Mode) (Page 121, 122)

Item 3: Added diodes to Figure-42, Figure-43 and Figure-44 (Page 66, 67, 68)

Item 2: Changed the description for the REF pin (Page 19)

Item 1: Changed data sheet status from 'Advance Information' to final. (Page 10)

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