

RZ/G SERIES EMBEDDED MICROPROCESSORS

High-End Graphics, Video, Embedded Vision and More





RZ/G SERIES MULTI-CORE MPUs FOR **HIGH-END GRAPHICS, VIDEO, EMBEDDED VISION, AND MORE**

RZ/G Series microprocessors (MPUs) enable rapid development of game-changing Linux, Android, and QNX-based embedded systems. Designed for use in industrial, home appliance, office, and medical equipment, the RZ/G Series extends the capabilities of the successful Renesas RZ/A Series MPUs to deliver high-end performance in applications such as graphics, multistream video, and embedded vision.

The RZ/G Series features up to 1.5 GHz CPUs, 3D graphics acceleration, 1080p@60fps H.264 video processing, and DDR3 memory interfaces, providing ample performance margin for highly differntiated Human-Machine Interface (HMI)-type applications. Other features include USB, PCIe, SATA, Fast Ethernet, and Gigabit Ethernet interfaces.

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KEY FEATURES	KEY BENEFITS
World-class Imagination Technologies 3D graphics engine for image rendering, plus 1080p 60fps video codec	Enhance GUI expressiveness and add value to your end application with 3D graphics and real-time video
Arm [®] Cortex [®] -A7 and A15 CPU cores for power or performance optimization	Tune your design to minimize power dissipation or maximize performance to suit your specific requirements
Shared IP and memory map over entire RZ/G Series MPU lineup	Build scalable software with common device drivers
Rich ecosystem of design and system integration partners	Compress your development cycle by tapping into a growing network of RZ/G MPU design experts
RZ/G Linux Platform with super-long-term supported Civil Infrastructure Platform (CIP) Linux	Dramatically reduce risk and cost of implementing and maintaining industrial-grade Linux systems





RZ/G LINUX PLATFORM WITH INDUSTRIAL-GRADE CIP LINUX REDUCES RISK AND COST OF OWNERSHIP

The Renesas RZ/G Linux Platform makes it easy to implement and maintain Industrial-Grade Linux in electronic equipment thanks to a unique software-design framework with Renesas' Verified Linux Package, advanced code verification and analysis tools, and cloud-build from within Renesas' e² studio development environment. The Verified Linux Package is built with Civil Infrastructure Platform (CIP) Linux with super long-term (10+ year) support for a given Linux kernel, which eliminates the need for costly upgrades.

The RZ/G Linux Platform enables users of all types to excel with Linux – from those upgrading from microcontrollers to MPUs who don't want to deal with setting up a Linux server of their own, to experts who are already accustomed to a manual Yocto build process. https://www.cip-project.org/



High-Performance Processing with Support for 3D Graphics and Full HD Video

	RZ/G1C R8A77470	RZ/G1E R8A77450	RZ/G1N R8A77440	RZ/G1M R8A77430	RZ/G1H R8A77420		
Core	Dual Cortex-A7	Dual Cortex-A7	Dual Cortex-A15	Dual Cortex-A15	Quad Cortex-A15 Quad Cortex-A7		
Operating Frequency	1.0 GHz	1.0 GHz	1.5 GHz	1.5 GHz	1.4 GHz 780 MHz		
Processing Performance	3,800 DMIPS	3,800 DMIPS	10,500 DMIPS	10,500 DMIPS	25,528 DMIPS		
Cache Size	L1 cache I/32 KB D/32 KB L2 cache 512 KB	L1 cache I/32 KB D/32 KB L2 cache 512 KB	L1 cache I/32 KB D/32 KB L2 cache 1 MB	L1 cache I/32 KB D/32 KB L2 cache 1 MB	L1 cache I/32 KB D/32 KB L2 cache 2 MB (A15) 512 KB (A7) S3 cache 2 MB		
MMU	Yes						
NEON/VFP	SIMDv2/VFPv4						
3D Graphics	SGX531, 260 Mpx/s	SGX540, 520 Mpx/s	SGX544MP2, 1240 Mpx/s	SGX544MP2, 2080 Mpx/s	SG6400, 4160 Mpx/s		
	2 ch digital video inputs	2 ch digital video inputs	3 ch digital video inputs	4 ch digital video inputs			
	2 ch RGB video display interfaces	2 ch RGB video display	1 ch RGB video display inter				
Video Functions	1 ch analog input	interfaces	1 ch LVDS video output	2 ch LVDS video interfaces			
	H.264 – 1920x1080 @ 60 x 1	H.264 – 1920x1080 @ 60 x 2 ch					
	Video image processing functions, including color conversion and scaling						



RZ/G SERIES MULTI-CORE MPUs BIG IDEAS FOR EVERY SPACE WITH RZ/G MPUs

Human Machine Interface (HMI)

Create winning human machine interface solutions for building and office automation, healthcare, and industrial markets with RZ/G MPUs. Take advantage of the multitude of graphics and multimedia examples to get your prototypes up and running quickly and to get to market early.

EtherCAT Master with HMI for Industrial Automation

RZ/G1E with EtherCAT Master stack from partner, Acontis GmbH, is ideal for industrial PCs, HMI master devices, and motion controllers, especially when combined with Renesas RZ/T EtherCat slave devices.







Embedded Vision

Embedded vision and AI are areas of growing interest today as they hold huge promise to revolutionize human-to-machine and machine-to-machine interaction. Renesas is ideally positioned to enable this capability in products with a range of examples, created in conjunction with our software partners, for face recognition, object identification, optical flow, and more.



Renesas Marketplace for Verified Software Add-Ons

The online Renesas Marketplace connects product developers to value-adding Renesas partners, enabling developers to immediately bring to life RZ/G silicon with leading software solutions, including embedded vision, EtherCAT, and fast-boot, among others.

U.S.: https://mp.renesas.com/en-us/rzg/ Europe: https://mp.renesas.com/en-eu/rzg/ Singapore: https://mp.renesas.com/en-sg/rzg/ Japan: https://mp.renesas.com/ja-jp/rzg/





RZ/G SERIES BLOCK DIAGRAMS

RZ/G1E (R8A77450)

Bring huge value to your low-cost embedded design with over 3,800 Dhrystone (DMIPS) performance at a low power footprint via Arm's lowest power processor, the Cortex-A7, while enjoying dual Ethernet, 3D graphics, and a 1080p60 H.264 video codec.

RZ/G1C (R8A77470)

(block diagram not shown)

Keep BOM costs down with a 4-layer PCB and no need for a PMIC. With a dual-core, 3,800 DMIPS Arm Cortex-A7 processor, you can build brilliant HMI systems with accelerated graphics from a PowerVR SGX531 3D GPU and a 1080p60 H.264 video codec. Available in a FX-BGA2121-501 package.

RZ/G1M (R8A77430)

Take your high-end MPU design to the next level with over 10,000 DMIPS performance from two Arm Cortex-A15 cores; USB 3.0, SATA and PCle interfaces; 3D graphics; a 1080p60 H.264 video codec; and a 64-bit wide external DDR3 SDRAM bus to deliver immense memory bandwidth and an incredible end-user experience.

System		Interfaces			
Arm Debugger (CoreSight)	Package: FX-BGA2121-501				External Bus Interface
DMAC		C	PU		DDR3L-SDRAM (32-bit) (1333 Mbps)
MMU etc.	Arm Cortex			n Cortex-A7 1.0 GHz	1 ch USB 2.0 Host
Timers	L1 I\$ I	L1 D\$ 32 KB	L1 I\$	L1 D\$	1 ch USB 2.0 Host/Func
(various)	L2 Cache: 512	2 KB	N	EON/VFPv4	3 ch SDHI Support SDXC
Network		1 ch MMCIF			
2 ch CAN	Memory RAM0 RAM1 RAM2				6 ch 12C
Ethernet AVB 100 and 1000 Mbps	RAM0 72 KB	4 K		RAM2 256 KB	6 ch SCIF
Ethernet MAC 10 and 100 Mbps		Graph			3 ch MSIOF
Audio IPs	PowerVR SG		De-Interlacing		QSPI
10 ch SSI	3D Graphics Engine (260 MHz, 520 Mpx/s) H.264 Codec Module 1920 x 1080 @ 60		Video Format Converter		Single/Dual/Quad-SPI
Serial Sound Interface			2D-DMAC Image Extraction,		3 ch HSCIF
10 ch SRC Sampling Rate Converter			Rota	tion, Scaling, Inversion	GPIO
ADG Audio Clock Generator	Digital Video 2 ch Inputs		Digital RGB 2 ch Outputs		etc.

	System	Package:					Interfaces
	Arm Debugger (CoreSight)	FC-BGA2727-831				Ext	ernal Bus Interface
	DMAC		СРИ				
	MMU						l ch USB 2.0 Host
	etc.	Arm Cortex 1.5 GHz			Cortex-A15 1.5 GHz	1 ch	USB 2.0 Host/Func.
	Timers				L1 D\$ 32 KB		3 ch SDHI Support SDXC
	(various)	L2 Cache: 1	мв	NEON/VFPv4			1 ch MMCIF
	Network						6 ch 12C
	2 ch CAN	Memory			Sir	QSPI ngle/Dual/Quad-SPI	
	Ethernet AVB 100 and 1000 Mbps	RAMO RAM 72 KB 4 K				-	GPIO
f	Ethernet MAC 10 and 100 Mbps	Graphics IP				1	ch USB 3.0 Host
	· · · ·	PowerVR SGX544		De-Interlacing			Serial-ATA
	Audio IPs	3D Graphics E (520 MHz, 2080		Video F	ormat Converter	1	lane PCI Express
	Serial Sound Interface	H.264 Codec Module		2D-DMAC Image Extraction, Rotation, Scaling,		Do	LVDS t Clock ~ 148.5 MHz
	Sampling Rate Converter	1920 x 1080 @	1920 x 1080 @ 60		Inversion		THS/TSC
	ADG Audio Clock Generator	Digital Video 3 ch Inputs		Digital RGB 2 ch Outputs			Thermal Sensor etc.

USB 3.0 and SATA0 share same PHY.

2 PCIe and SATA1 share same PHY.

3 Ethernet AVB and 10/100 Ethernet share the same pins.



System		Interfaces				
Arm Debugger (CoreSight)	FC	External Bus Interface				
DMAC					DDR3L-SDRAM (32-bit)(1600 Mbps)	
		C	PU		1 ch USB 2.0 Host	
MMU	Arm Cortex 1.5 GH			Cortex-A15 1.5 GHz	1 ch USB 2.0 Host/Func.	
etc. Timers	L1 I\$	L1 I\$ L1 D\$ L1 IS			3 ch SDHI Support SDXC	
(various)	L2 Cache: 1 MB		N		1 ch MMCIF	
Network	Lz Gache. 1	L2 Cache: 1 MB NEON/VFPv4				
2 ch CAN		Memory			QSPI Single/Dual/Quad-SPI	
Ethernet AVB 100 and 1000 Mbps	RAMU 72 KB	RAMO RAN 72 KB 4 K		RAM2 256 KB	GPIO	
2 Ethernet MAC		Graphics IP				
10 and 100 Mbps	Power\/B Sf	PowerVR SGX544		-Interlacing	Serial-ATA	
Audio IPs	3D Graphics Engine (312 MHz, 1240 Mpx/s) H.264 Codec Module 1920 x 1080 @ 60		Video Format Converter		1 Iane PCI Express	
10 ch SSI Serial Sound Interface			-	2D-DMAC ge Extraction,	LVDS Dot Clock ~ 148.5 MHz	
10 ch SRC Sampling Rate Converter			Rota	ition, Scaling, Inversion	THS/TSC	
ADG Audio Clock Generator	Digital Video 3 ch Inputs		Digital RGB 2 ch Outputs		Thermal Sensor etc.	

RZ/G1N (R8A77440)

Leverage a rich set of high-end embedded capabilities with 10,500 DMIPS from two Arm Cortex-A15 CPUs; a USB 3.0, SATA or PCIe interface; a 3D graphics engine; and a 1080p60 H.264 video codec to propel your application beyond the reach of your competition.

1 USB 3.0, SATA and PCIe share same PHY. 2 Ethernet AVB and 10/100 Ethernet share the same pins.

	Sys	tem	Package:				Interfaces
	Arm De (Core)		FC	External Bus Interface			
	DMAC	MMU		CPU			
	S3 Cach	e: 2 MB	Arm Cort		Arm Cortex-A7		2 ch USB 2.0 Host
	etc.		4 X 1.4 GHz L1 I\$ L1 D\$ 32 KB 32 KB		4 780 MHz		1 ch USB 2.0 Host/Func.
	Timers (various)				32 KE		4 ch SDHI Support SDXC
			L2 Cache: 2 MB		L2 Cache: 512 KB		2 ch MMCIF
	Netv	vork	NEON/VF	Pv4	N	ON/VFPv4	4 ch I2C
	2 ch	CAN	Memory			QSPI Single/Dual/Quad-SPI	
	Ethern 100 and 1		RAMO RAM 72 KB 4 K				GPIO
6	Etherne		Graphics IP			1 ch USB 3.0 Host	
	10 and 1		PowerVR G6400		3 ch De-Interlacing		Serial-ATA
	Audi		3D Graphics (520 MHz, 4,16	Engine	Video F	ormat Converter	1 Iane PCI Express
	10 ch SSI Serial Sound Interface	H.264 Codec Module		2D-DMAC Image Extraction,		2 ch LVDS Dot Clock ~ 148.5 MHz	
	10 ch Sampling Rat		1920 x 1080 @ 60 (2 ch)		Rotation, Scaling, Inversion		THS/TSC Thermal Sensor
	AD Audio Clock	-	Digital Video 4 ch Inputs		Digital RGB 2 ch Outputs (plus 2 ch LVDS Outputs)		etc.

RZ/G1H (R8A77420)

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Harness the ultimate in embedded MPU performance with this 8-core "Big-Little" quad Arm Cortex-A15 and quad Arm Cortex-A7 based MPU that delivers over 25,000 DMIPS; USB 3.0, SATA and PCIe interfaces; ultra high-end 3D graphics with two-channel 1080p60 H.264 codecs; and a 64-bit wide DDR3 SDRAM memory bus.

1 USB 3.0 and SATA0 share same PHY. 2 PCIe and SATA1 share same PHY. 3 Ethernet AVB and 10/100 Ethernet share the same pins.

RENESAS

RZ/G EXTENDS RENESAS RZ MPU FAMILY WITH 3D GRAPHICS, H.264 VIDEO CODEC, AND HIGH-PERFORMANCE DDR3 MEMORY INTERFACES



Evaluation Kits



RZ/G1M Starter Kit Part No.: YR8A77430S000BE

www.renesas.com/en-us/products/microcontrollers-microprocessors/rz.html

Various partner kits and SoMs also available through iWave Systems, emtrion embedded systems, Mistral Solutions and others



RZ/G1E Starter Kit

Part No.: YR8A77450S000BE

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