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April 1st, 2010 Renesas Electronics Corporation

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Renesas MCUs H8S Family H8S/Tiny Series



The MCUs for everyone.



Renesas Tiny Series MCUs bring enhanced convenience, security, and richness to people's lives.

New advances in general purpose MCUs

Renesas offers a wide range of MCU products to match the demands of every aspect of today's modern lifestyle. Of these, the Tiny Series has attracted great attention. These single-chip MCUs each combine a high-performance CPU core with on-chip flash memory. A broad lineup of Tiny Series MCUs is available, allowing users to choose precisely the features and performance that their specific application requires. From specialized high-grade applications to general purpose products, the Tiny Series has your needs covered.

Four personalities, four types of usability

Tiny consists of four product series, each with a different high-performance CPU core. The SH/Tiny Series uses the SuperH™ CPU core. The M16C/Tiny Series uses the M16C CPU core. The R8C/Tiny Series uses the R8C CPU core, which is instruction compatible with the M16C. The H8/Tiny Series uses the H8/300H CPU core. Finally, H8S/Tiny Series uses the H8S/2000 core.

Low cost, low pin count, small package

The product concept of the Tiny Series couldn't be simpler. These Renesas MCUs each offer low cost, low pin count, and a small package.

- Low pin count and small package (most 20 to 100 pins), high-performance CPU (16-bit or more).
- Highly reliable on-chip flash memory (factory programming available).*1
- High-performance on-chip peripheral functions for reduced system cost.

Common features throughout the series



*1: Applies to R8C/Tiny Series only. *2: Renesas Starter Kit

Renesas MCU I INY Series

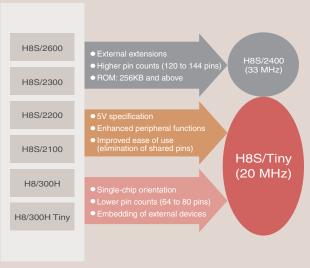


The H8S/Tiny Series adds improved performance to the many advantages of the acclaimed H8/300H Tiny Series.

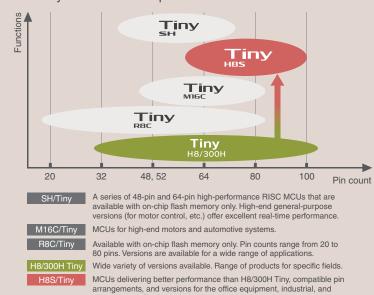
■ H8SX, H8S, H8 Lineup



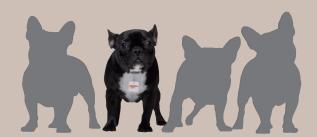
■ Advancing to the H8S/Tiny Series



■ Tiny Series Roadmap

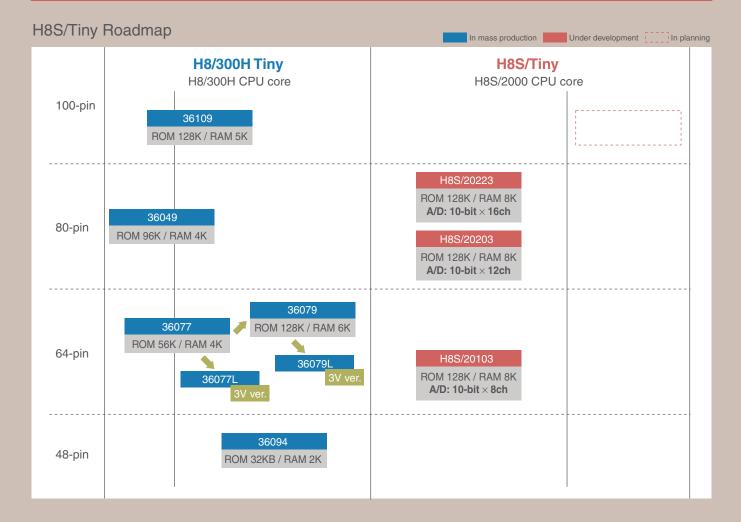


consumer applications.



The H8/Tiny Series of high-performance MCUs are built around the H8S/2000 CPU, which provides object code compatibility with the H8/300H CPU. It is also available in the same packages and retains pin compatibility with earlier MCUs.

 Further improvement in CPU performance New functions such as event link controller (ELC) and peripheral I/O mapping controller (PMC) **Enhanced performance>** ● More powerful interrupt controller for reduced software processing load (four priority levels, multiplex interrupts) H8S/Tiny On-chip data transfer controller (DTC) for better data transfer performance More A/D converter input channels (16 input channels on 80-pin version) Big increase in number of timers High-speed on-chip oscillator with improved accuracy Power-on reset (POR) circuit with improved functionality <Enhanced ease of use> Low-voltage detection (LVD) circuit **H8/300H Tiny** Watchdog timer (WDT) • Serial communication interface (SCI) with built-in noise elimination function





Peripheral Functions

New function 1 Event link controller (ELC)

The event link controller (ELC) is a circuit that assigns interrupt signals generated by on-chip functions such as the timers, A/D converters, and the data transfer controller (DTC) as startup sources for other on-chip functions.

Reduced interrupt processing load

When on-chip modules are used in combination, event link controller settings enable a reduction in the interrupt processing load.

Improved realtime performance

Eliminates interrupt transition time, module startup processing within the interrupt handler, and interrupt save time.

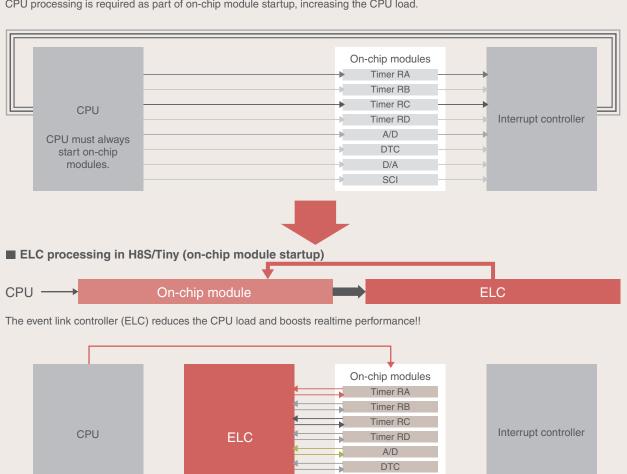
Reduced program size

The overall number of interrupts is reduced, making it possible to reduce the size of interrupt handling routines.

■ Conventional processing (on-chip module startup)



CPU processing is required as part of on-chip module startup, increasing the CPU load.



D/A





Newfunction 2 Peripheral I/O mapping controller (PMC)

The peripheral I/O mapping controller (PMC) enables changing of the I/O pin assignments of on-chip functions.

On 64-pin versions 41 pins, and on 80-pin versions 51 pins, can be reassigned to different functions.

Pins other than system control pins and analog pins can be reassigned to different functions.

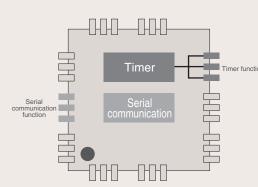
Selectable among up to six functions per pin.

Pins can be reassigned to different functions, such as timers and serial communication, by changing register settings.

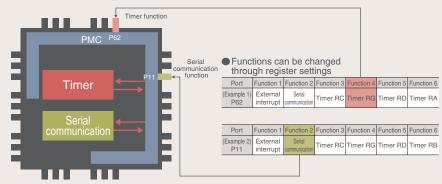
More flexibility when designing boards.

MCU pin functions can be assigned to specific pins to match the layout of the system board, shortening the time needed for board development.

■ Conventional MCU



■ Using PMC settings on H8S/Tiny



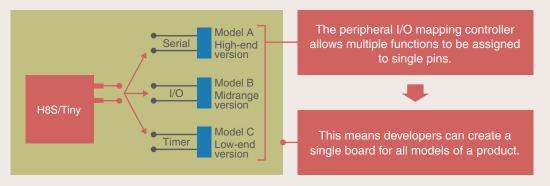
- 64-pin version (H8/20103): 41 pins can be reassigned to different functions.
- 80-pin versions (H8/20203, H8/20223): 51 pins can be reassigned to different functions.

Functions assigned to pins are fixed.



Selectable among up to six functions per pin.

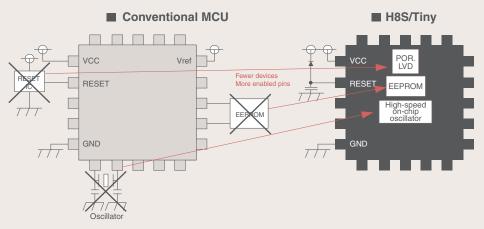
■ Usage example



Big reduction in external devices

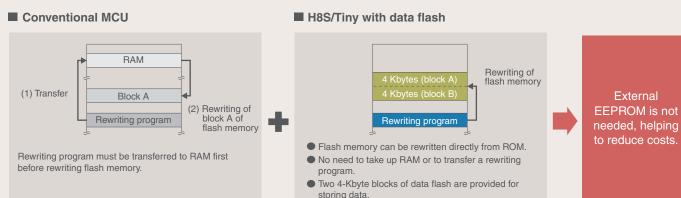
In addition to a high-performance CPU and low power consumption, the H8S/Tiny Series provides superior functionality that reduces the number of external devices needed.

- Fewer external devices: On-chip modules include on-chip oscillator, power-on reset, low-voltage detection, and EEPROM.
- Safe design: Oscillation stop detection, highly functional watchdog timer, ROM protect function, etc.



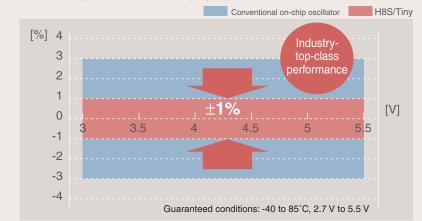
Data flash

H8S/Tiny MCUs feature on-chip data flash (flash memory for storing data).



Highly accurate high-speed on-chip oscillator

The H8S/Tiny Series is equipped with a highly accurate high-speed on-chip oscillator that is at the top of its class in the industry. This eliminates the need for an external oscillator.



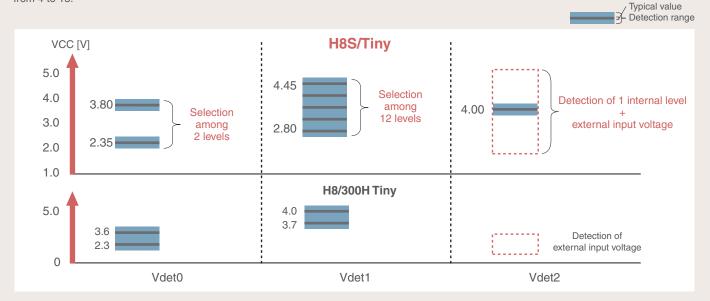


RENESAS Tiny HBS

Peripheral Functions

More advanced low-voltage detection (LVD) function

This function monitors the power supply voltage level and generates an internal reset signal or an interrupt when the voltage drops below a designated value. The H8S/Tiny Series has an enhanced low-voltage detection function that increases the available detection voltage levels from 4 to 15.



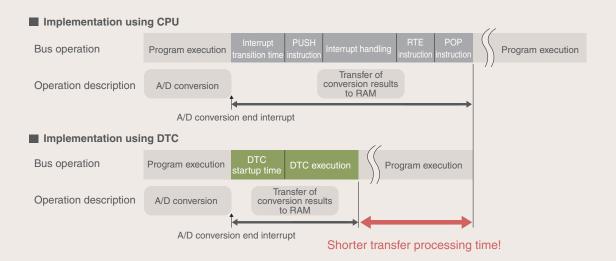
Data transfer controller (DTC) function

- The data transfer controller (DTC) is a function that transfers data between the memory and registers, bypassing the CPU.
- It is started by interrupts from peripheral functions.
- There are a total of 40 start sources for the 20103 Group, 44 for the 20203 Group, and 46 for the 20223 Group.



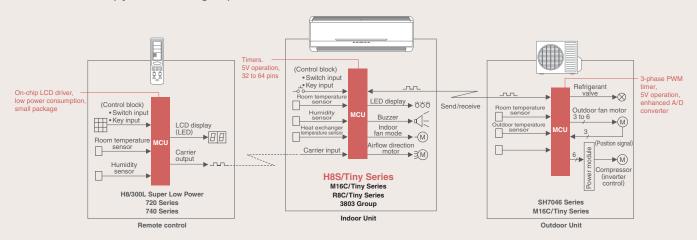
DTC operation example

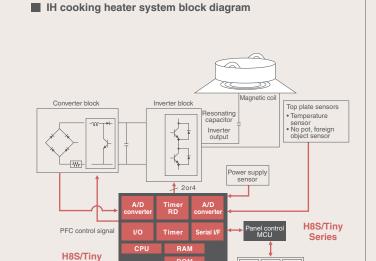
Transferring A/D conversion results to RAM

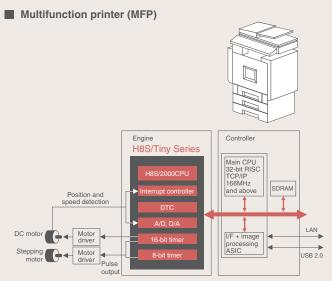


Application Examples

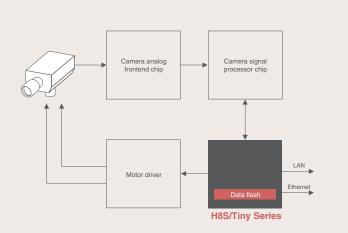
■ Air conditioner (system block diagram)



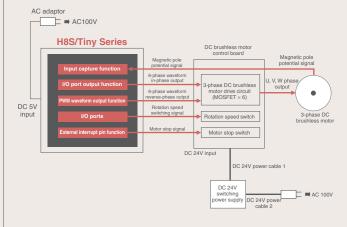




■ Home security camera module



■ DC brushless motor (control block diagram)



7





Specification Table

■ H8S/Tiny

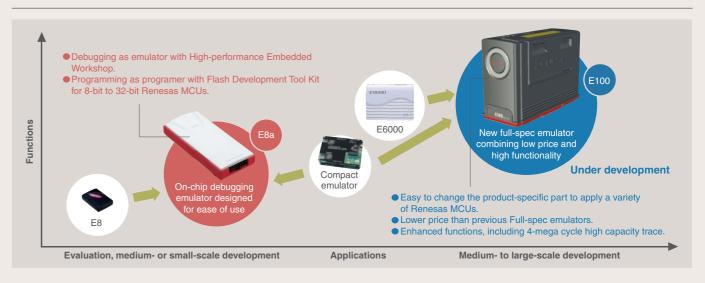
P	Pin count	64	nin		80	pin			
Group		H8S/20103		H8S/20203 H8S/20223					
	duct name	H8S/20102	H8S/20103	H8S/20202	H8S/20203	H8S/20222	H8S/20223		
110	ROM (bytes)	96K	128K	96K	128K	96K	128K		
	RAM (bytes)	8K	8K	8K	8K	8K	8K		
Memory	EEPROM (bytes)	4K × 2 blocks	4K × 2 blocks	4K × 2 blocks	4K × 2 blocks	4K × 2 blocks	4K × 2 blocks		
,	ROM type	F	F	F	F	F	F		
	Program security	•	•	•	•	•	•		
	CPU core	H8S/2000 CPU	H8S/2000 CPU	H8S/2000 CPU	H8S/2000 CPU	H8S/2000 CPU	H8S/2000 CPU		
CPU	Min. instruction execution time (ns)	50	50	50	50	50	50		
	Clock oscillator circuits 4 circuits (system clock, subclock, on-chip oscillators (low speed, high speed))				ubclock, on-chip oscillators high speed))	4 circuits (system clock, subclock, on-chip oscillators (low speed, high speed))			
	Subclock	•	•	•	•	•	•		
Clock	On-chip oscillator	(High speed, low speed)	(High speed, low speed)	(High speed, low speed)	(High speed, low speed)	(High speed, low speed)	(High speed, low speed)		
	Oscillation stop detect	•	•	•	•	•	•		
	Low-power-consumption configuration	•	•	•	•	•	•		
	Power-on reset	•	•	•	•	•	•		
Voltage detection	Low voltage detect	•	•	•	•	•	•		
A/D converter	Resolution × channels	10-bit × 8ch (On-chip comparator mode)	10-bit × 8ch (On-chip comparator mode)	10-bit × 12ch (On-chip comparator mode)	10-bit × 12ch (On-chip comparator mode)	10-bit × 16ch (On-chip comparator mode)	10-bit × 16ch (On-chip comparator mod		
D/A converter	Resolution × channels	8-bit × 2ch	8-bit × 2ch	8-bit × 2ch	8-bit × 2ch	8-bit × 2ch	8-bit × 2ch		
	RTC	1	1	1	1	1	1		
	Timer RA	1	1	1	1	1	1		
	Timer RB	1	1	1	1	1	1		
Timers	Timer RC	1	1	_	_	_	_		
	Timer RD (16-bit, 2 channels)	1	1	2	2	2	2		
	Timer RE	1	1	1	1	1	1		
	Timer RG	1	1	1	1	1	1		
Wate	chdog timer	Selectable internal oscillator	Selectable internal oscillator	Selectable internal oscillator	Selectable internal oscillator	Selectable internal oscillator	Selectable internal oscillator		
Event link controller		•	•	•	•	•	•		
Peripheral I/0	O mapping controller	•	•	•	•	•	•		
Serial interface	Clock synchronous/asynchronous	3	3	3	3	3	3		
	I ² C bus	1	1	1	1	1	1		
	SSU	1	1	1	1	1	1		
	CAN	_	_	_	_	_	_		
I/O ports	COMS I/O	55	55	69	69	69	69		
	Large-current drive port	47	47	57	57	53	53		
	Pull-up resistors	55	55	69	69	69	69		
Interrupts (sources)	External	7	7	9	9	9	9		
Debug functions	On-chip debug	•	•	•	•	•	•		
Debug fullelions	Onboard flash programming	•	•	•	•	•	•		
Packages		10mm × 10mm (0.5mm pitch)				14mm × 14mm (0.65mm pitch)			
	ncy/power supply voltage		2.7V to 5.5V/4MHz to 20MHz			2.7V to 5.5V/4MHz to 20MHz			
Operating temperature (°C)		-20 to 75°C, -40 to 85°C	-20 to 75°C, -40 to 85°C	-20 to 75°C, -40 to 85°C	-20 to 75°C, -40 to 85°C	-20 to 75°C40 to 85°C	-20 to 75°C, -40 to 85°C		

■ H8/300H Tiny

Pin count		100pin	80pin			64pin								
Group		H8/36109	H8/36049				H8/36077				H8/36079			
Pro	duct name	H8/ 36109F	H8/ 36047	H8/ 36048	H8/ 36049	H8/ 36049F	H8/ 36077F	H8/ 36074F	H8/ 36077L	H8/ 36074L	L 36079F 36078F 3607		H8/ 36079L	H8 3607
	ROM (bytes)	128K	64K	80K	96K	96K	56K	32K	56K	32K	128K	96K	128K	96K
Memory	RAM (bytes)	5K	3K	3K	3K	4K	4K	4K	4K	4K	6K	6K	6K	6K
	EEPROM (bytes)	_		-				_	_			-	_	
	ROM type	F	M	M	M	F	F	F	F	F	F	F	F	F
	Program security	•	•			•			•					
CPU	CPU core	16-bit H8/300H CPU core	16-bit H8/300H CPU core			Э	16-bit H8/300H CPU core			16-bit H8/300H CPU core			е	
CPU	Min. instruction execution time (ns)	100 (@20MHz)	100 (@20MHz)			100 (@20MHz)			100 (@20MHz)					
	Clock oscillator circuits	3 circuits (system clock, subclock, on-chip oscillators)	2 circuits (system clock, subclock)			3 circuits (system clock, subclock, on-chip oscillators)			3 circuits (system clock, subclock, on-chip oscillato					
	Subclock	•	•			•			•					
	On-chip oscillator	•					•							
Clock	Oscillation stop detect	•								•				
	Low-power-consumption configuration	Supports four low-power-consumption modes, mo Standby/sub-active (when usin Sleep/sub-sleep (when using 3					sing 32kHz subclock)							
Voltage detection	Power-on reset	• (option)	(option)			• (option)			• (option)					
Low voltage detect		• (option)		(op	tion)			(op	tion)		(option)			
A/D converter	Resolution × channels	10-bit × 16ch	10-bit × 8ch 10-bit × 8ch					10-bit × 8ch						
	RTC	1	1			1			1					
	Timer A		_			_			_					
	Timer B1	1	1			1			1					
	Timer V	1	1			1			1					
Timers	Timer W		1			_			_					
Timers	Timer Z (16-bit, 2 channels)	_	1			1			1					
	Timer RC	1	_			_			_					
	Timer RD (16-bit, 2 channels)	2	_			_			_					
	Sub-system timer		_			_			_					
	14-bit PWM	1	1			1			1					
Wate	chdog timer	Selectable internal oscillator	Selectable interr		ernal oscillator		Selectable internal oscillator		Selectable internal oscillator		ior			
Serial interface	Clock synchronous/asynchronous	3			3		2		2					
	I ² C bus	1			1		1		1					
	CAN			-				_				-	_	
	Input only	8	8			8			8					
I/O ports	COMS I/O	79	59			47			47					
I/O ports	Large-current drive port	20	13			8			8					
	Pull-up resistors	15	13			15		15						
Interrupts (sources)	External	11	11		11		11							
Debug functions	On-chip debug	•	•			•			•					
Debug functions	Onboard flash programming	•	•			•			•					
Packages		14mm × 20mm (0.65mm pitch), 14mm × 14mm (0.5mm pitch)	14mm × 14mm (0.65mm pitch)		ch)	10mm × 10mm (0.5mm pitch			n), 14mm × 14mm (0.8mm pitch)					
Operating frequency/power supply voltage		[Without POR/LVD] 4MHz to 10MHz/ 3.0V to 5.5V, 4MHz to 20MHz/4.5V to 5.5V [With POR/LVD] 4MHz to 16MHz/4.5V to 5.5V		tz/2.7V to 5.5V tz/4.0V to 5.5V	1MHz to 10MHz/ 2.7V to 5.5V 1MHz to 20MHz/ 4.0V to 5.5V	3.0V to 5.5V	4MHz to 4.5V to	20MHz/ o 5.5V	4MHz to 1 3.0V to		4MHz to 4.5V t	20MHz/ o 5.5V	4MHz to 3.0V t	16MH to 3.6V
	temperature (°C)	-20 to 75°C,-40 to 85°C	-20 to 75°C40 to 85°C					40 to 85°C			-20 to 75°C	10. 05.0		

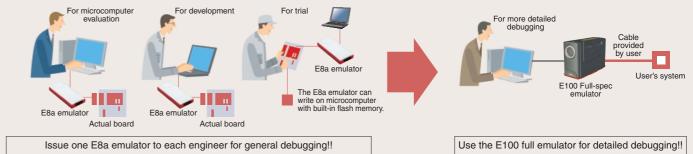
Development Environment

New emulator lineup

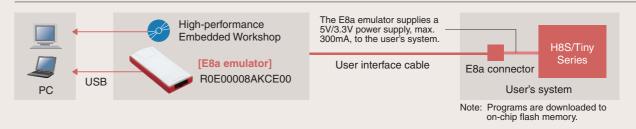


The E8a on-chip debugging emulator is compatible with all products in the H8S/Tiny Series and delivers an excellent performance-to-cost ratio. As it debuggs MCUs on the actual boards, it is also optimum to evaluate the analog specific characteristics of MCUs.

The E8a emulator also can function as a flash programmer. Each engineer can be issued an E8a for general debugging tasks, while the development team use two or three E100 full emulators for more detailed debugging in the final stages.



On-chip debugging emulator



Full-spec emulator

The E100 full emulator performs at the CPU's maximum operating frequency. When paired with the High-performance Embedded Workshop software package, it provides an easy-to-use environment for debugging programs using a few mouse clicks. The E100 can also be used to quickly download load module files.

 Powerful debugging functions (trace, RAM monitor, conditional breakpoint trace, performance analysis).

Source level debugging of C/C++ code.



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