

# INTRODUCING HVPAK™

## THE ULTIMATE MOTOR DRIVER SOLUTION FOR MODERN SMART LOCKS

**HVPAK (up to 26.4 V and 3 A per OUT)** takes its roots from the **GreenPAK family** featuring its configurability and free GUI-based **software** to design the circuit without any programming language needed. It combines mixed-signal logic and **high-voltage H-/Half-bridge** functionality in **a tiny 2 mm x 3 mm QFN package**.

The HVPAK incorporates advanced PWM macrocells, enabling the simultaneous control of multiple motors with varying PWM frequencies and duty cycles. Its compact size and low idle current consumption make it highly versatile, expanding its range of potential applications. This tiny and thermally-efficient IC provides an ideal platform for designing mixed-signal functions alongside its high-voltage capabilities.



### Secure system stability

by reducing motor peak current  
(soft start)

- Increased operating voltage range



### Flexible/Programmable motor control

- Programmable Reset Timing

- Programmable Motion Profile

- Smaller board space



### Low current consumption

– 30 nA sleep current

- Built-in MOSFETs
- Constant motor speed feature
- Current limit control feature

Datasheet  
SLG47105

Datasheet  
SLG47115

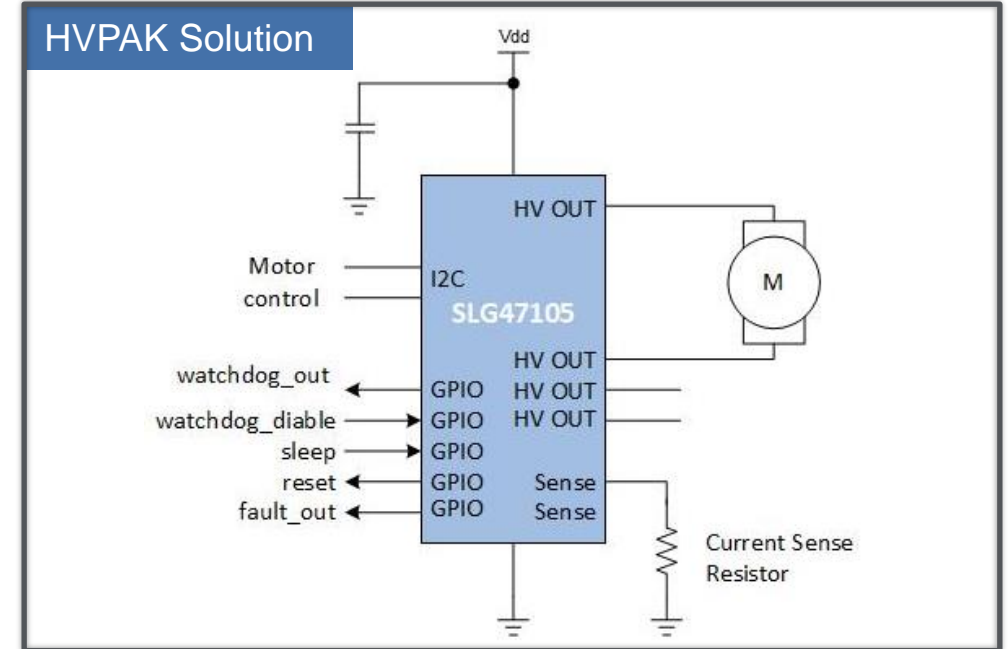
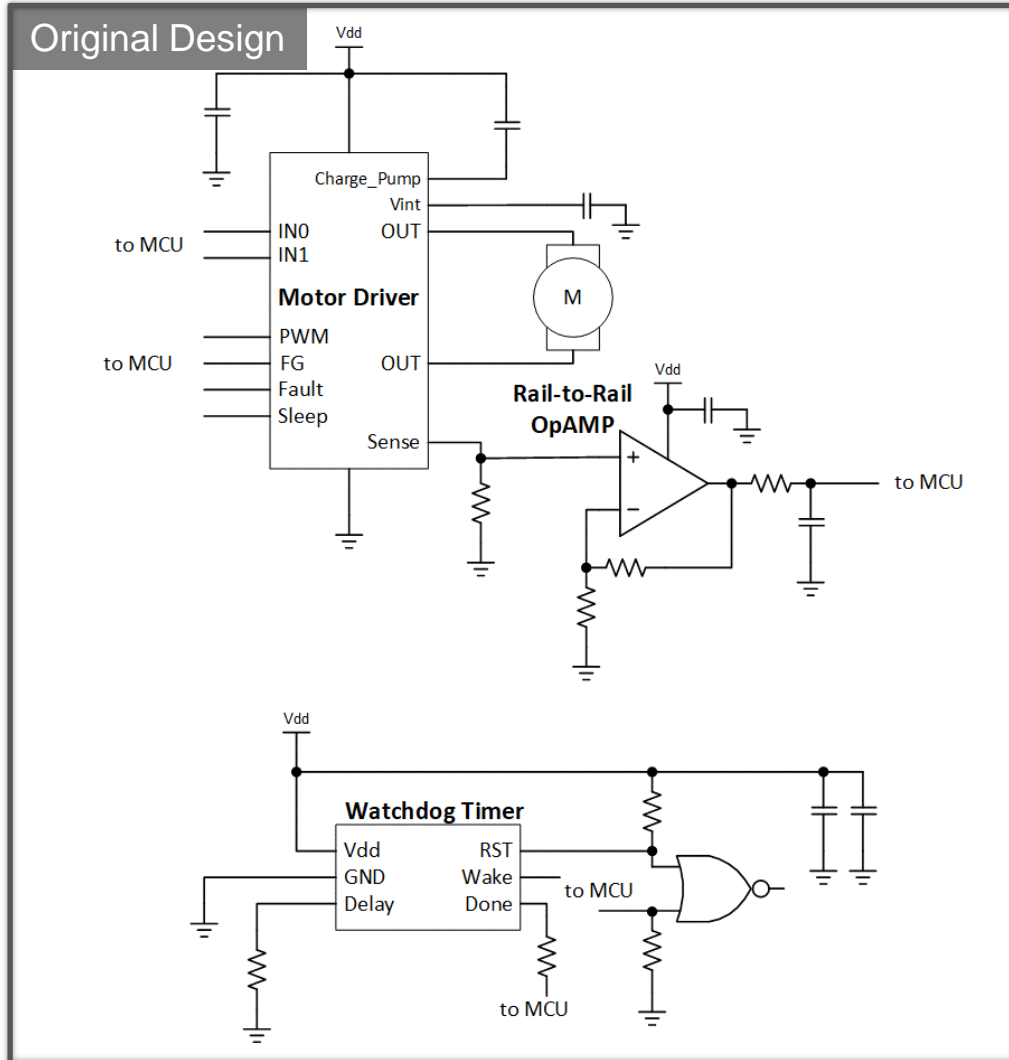
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Software Hub

HVPAK  
Application Notes

Winning Combo:  
Wireless Smart Door  
Lock

Winning Combo:  
Smart Lock with  
Super-Low Power  
Wi-Fi and Bluetooth  
Low Energy

# HVPAK APPROACH: SMART LOCK DESIGN



## Design reduced by:

- 4 ICs
- 14 passive components
- reduced current consumption by four times (idle mode)

Value	Approx. savings with HVPK
Layout Size	17.8 mm <sup>2</sup>
Cost Savings	\$ 1.33