To our customers,

---

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)
Send any inquiries to http://www.renesas.com/inquiry.
Notice

1. All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any Renesas Electronics products listed herein, please confirm the latest product information with a Renesas Electronics sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by Renesas Electronics such as that disclosed through our website.

2. Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.

3. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part.

4. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.

5. When exporting the products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations. You should not use Renesas Electronics products or the technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations.

6. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.

7. Renesas Electronics products are classified according to the following three quality grades: “Standard”, “High Quality”, and “Specific”. The recommended applications for each Renesas Electronics product depend on the product’s quality grade, as indicated below. You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application categorized as “Specific” without the prior written consent of Renesas Electronics. Further, you may not use any Renesas Electronics product for any application for which it is not intended without the prior written consent of Renesas Electronics. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for an application categorized as “Specific” or for which the product is not intended where you have failed to obtain the prior written consent of Renesas Electronics. The quality grade of each Renesas Electronics product is “Standard” unless otherwise expressly specified in a Renesas Electronics data sheets or data books, etc.

“Standard”: Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots.

“High Quality”: Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; safety equipment; and medical equipment not specifically designed for life support.

“Specific”: Aircraft; aerospace equipment; submersible repeaters; nuclear reactor control systems; medical equipment or systems for life support (e.g. artificial life support devices or systems), surgical implantations, or healthcare intervention (e.g. excision, etc.), and any other applications or purposes that pose a direct threat to human life.

8. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.

9. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.

10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.

11. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of Renesas Electronics.

12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

(Note 1) “Renesas Electronics” as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.
1. Abstract

This document describes how to set up and use the timer RB in programmable one-shot generation mode on the R8C/25 Group device.

2. Introduction

The application example described in this document is applied to the following:

- MCU: R8C/25 Group
- XIN clock: 20 MHz

This program can be used with other R8C/Tiny Series which have the same SFR (special function register) as the R8C/25 Group. Check the manual for any additions and modifications to functions. Careful evaluation is recommended before using this application note.
3. Applications

3.1 Timer RB

Timer RB is an 8-bit timer with an 8-bit prescaler. The prescaler and timer each consist of a reload register and counter. The reload register and counter are allocated at the same address (refer to Table 3.1 Programmable One-shot Generation Mode Specifications for access to the reload register and counter).

Timer RB has timer RB primary and timer RB secondary as reload registers. The count source for timer RB is the operating clock that regulates the timing of timer operations such as counting and reloading.

Figure 3.1 shows a Block Diagram of Timer RB.

Timer RB has four operation modes listed as follows:

- **Timer mode:** The timer counts an internal count source (peripheral function clock or timer RA underflows).
- **Programmable waveform generation mode:** The timer outputs pulses of a given width successively.
- **Programmable one-shot generation mode:** The timer outputs a one-shot pulse.
- **Programmable wait one-shot generation mode:** The timer outputs a delayed one-shot pulse.

![Figure 3.1 Block Diagram of Timer RB](image-url)
3.2 Programmable One-shot Generation Mode

In programmable one-shot generation mode, a one-shot pulse is output from the TRBO pin by a program or an external trigger input (input to the INT0 pin) (refer to Table 3.1). When a trigger is generated, the timer starts operating from that point only once for a given period equal to the set value in the TRBPR register. The TRBSC register is not used in this mode.

Figure 3.2 shows Registers TRBCR and TRBOCR in Programmable One-shot Generation Mode, and Figure 3.3 shows Registers TRBIOC and TRBMR in Programmable One-shot Generation Mode, Figure 3.4 shows Registers TRBPRE and TRBPR in Programmable One-shot Generation Mode. Figure 3.5 shows an Operating Example of Timer RB in Programmable One-shot Generation Mode.

### Table 3.1 Programmable One-shot Generation Mode Specifications

<table>
<thead>
<tr>
<th>Item</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Count sources</td>
<td>f1, f2, f8, timer RA underflow</td>
</tr>
<tr>
<td>Count operations</td>
<td>• Decrement the setting value in the TRBPR register</td>
</tr>
<tr>
<td></td>
<td>• When the timer underflows, it reloads the contents of the reload register</td>
</tr>
<tr>
<td></td>
<td>before the count completes and the TOSSTF bit is set to 0 (one-shot stops).</td>
</tr>
<tr>
<td></td>
<td>• When the count stops, the timer reloads the contents of the reload register</td>
</tr>
<tr>
<td></td>
<td>before it stops.</td>
</tr>
<tr>
<td>One-shot pulse output</td>
<td>[(n+1)(m+1)/fi]</td>
</tr>
<tr>
<td>time</td>
<td>fi: count source frequency,</td>
</tr>
<tr>
<td></td>
<td>n: Setting value in TRBPRE register, m: Setting value in TRBPR register</td>
</tr>
<tr>
<td>Count start conditions</td>
<td>• The TSTART bit in the TRBCR register is set to 1 (count starts) and the</td>
</tr>
<tr>
<td></td>
<td>next trigger is generated.</td>
</tr>
<tr>
<td></td>
<td>• Set the TOSST bit in the TRBOCR register to 1 (one-shot starts)</td>
</tr>
<tr>
<td></td>
<td>• Input trigger to the INT0 pin</td>
</tr>
<tr>
<td>Count stop conditions</td>
<td>• When reloading completes after timer RB underflows during primary period.</td>
</tr>
<tr>
<td></td>
<td>• When the TOSSP bit in the TRBOCR register is set to 1 (one-shot stops).</td>
</tr>
<tr>
<td></td>
<td>• When the TSTART bit in the TRBCR register is set to 0 (count stops).</td>
</tr>
<tr>
<td></td>
<td>• When the TSTOP bit in the TRBCR register is set to 1 (count forcibly stops).</td>
</tr>
<tr>
<td>Interrupt request</td>
<td>In half a cycle of the count source, after the timer underflows (at the same</td>
</tr>
<tr>
<td>generation timing</td>
<td>time as the TRBO output ends) [timer RB interrupt]</td>
</tr>
<tr>
<td>TRBO pin function</td>
<td>Pulse output</td>
</tr>
<tr>
<td>INT0 pin functions</td>
<td>• When the INOSTG bit in the TRBIOC register is set to 0 (INT0 one-shot trigger</td>
</tr>
<tr>
<td></td>
<td>disabled): programmable I/O port or INT0 interrupt input</td>
</tr>
<tr>
<td></td>
<td>• When the INOSTG bit in the TRBIOC register is set to 1 (INT0 one-shot trigger</td>
</tr>
<tr>
<td></td>
<td>enabled): external trigger (INT0 interrupt input)</td>
</tr>
<tr>
<td>Read from timer</td>
<td>The count value can be read out by reading registers TRBPR and TRBPRE.</td>
</tr>
<tr>
<td>Write to timer</td>
<td>• When registers TRBPRE and TRBPR are written while the count is stopped,</td>
</tr>
<tr>
<td></td>
<td>values are written to both the reload register and counter.</td>
</tr>
<tr>
<td></td>
<td>• When registers TRBPRE and TRBPR are written during the count, values are</td>
</tr>
<tr>
<td></td>
<td>written to the reload register only (the data is transferred to the counter</td>
</tr>
<tr>
<td></td>
<td>at the following reload)(^1).</td>
</tr>
<tr>
<td>Select functions</td>
<td>• Output level select function</td>
</tr>
<tr>
<td></td>
<td>The TOPL bit in the TRBIOC register can select the output level of the one-</td>
</tr>
<tr>
<td></td>
<td>shot pulse waveform.</td>
</tr>
<tr>
<td></td>
<td>• One-shot trigger select function</td>
</tr>
<tr>
<td></td>
<td>Refer to 3.3 One-Shot Trigger Selection.</td>
</tr>
</tbody>
</table>

NOTES:
1. The set value is reflected at the following one-shot pulse after writing to the TRBPR register.
Timer RB in Programmable One-shot Generation Mode

**Timer RB Control Register**

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Address</th>
<th>After Reset</th>
</tr>
</thead>
<tbody>
<tr>
<td>TRBCR</td>
<td>0108h</td>
<td>00h</td>
</tr>
</tbody>
</table>

- **TSTART**: Timer RB count start bit
- **TCSTF**: Timer RB count status flag
- **TSTOP**: Timer RB count forcible stop bit

**Timer RB One-Shot Control Register**

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Address</th>
<th>After Reset</th>
</tr>
</thead>
<tbody>
<tr>
<td>TRBOCR</td>
<td>0109h</td>
<td>00h</td>
</tr>
</tbody>
</table>

- **TOSST**: Timer RB one-shot start bit
- **TOSSP**: Timer RB one-shot stop bit
- **TOSSTF**: Timer RB one-shot status flag

**NOTES:**
1. Refer to 3.4 Notes on Timer RB.
2. When the TSTOP bit is set to 1, registers TRBPRE, TRBSC, TRBPR, and bits TSTART and TCSTF, and the TOSSTF bit in the TRBOCR register are set to values after a reset.
3. Indicates that count operation is in progress in timer mode or programmable waveform mode. In programmable one-shot generation mode or programmable wait one-shot generation mode, indicates that a one-shot pulse trigger has been acknowledged.

**Figure 3.2** Registers TRBCR and TRBOCR in Programmable One-shot Generation Mode
## Timer RB in Programmable One-shot Generation Mode

### Timer RB I/O Control Register

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Address</th>
<th>After Reset</th>
<th>Bit Symbol</th>
<th>Bit Name</th>
<th>Function</th>
<th>RW</th>
</tr>
</thead>
</table>
| TRBIOC | 010Ah    | 00h         | TRBIOC     | Timer RB Output Level Select Bit  | 0: Outputs one-shot pulse "H" Outputs "L" when the timer is stopped  
                                          1: Outputs one-shot pulse "L"  
                                          Outputs "H" when the timer is stopped | RW |
|        |          |             | TOPL       | Timer RB Output Level Select Bit  | 0: Outputs one-shot pulse "H" Outputs "L" when the timer is stopped  
                                          1: Outputs one-shot pulse "L"  
                                          Outputs "H" when the timer is stopped | RW |
|        |          |             | TOCNT      | Timer RB Output Switch Bit        | Set to 0 in programmable one-shot generation mode. | RW |
|        |          |             | INOSTG     | One-Shot Trigger Control Bit(1)   | 0: INT0 pin one-shot trigger disabled  
                                          1: INT0 pin one-shot trigger enabled | RW |
|        |          |             | INOSEG     | One-Shot Trigger Polarity Select Bit(1) | 0: Falling edge trigger  
                                          1: Rising edge trigger | RW |
|        |          |             |           | Nothing is assigned. When write, set to 0.  
                                          When read, its content is 0. | — |

**NOTE:**
1. Refer to 3.3 One-Shot Trigger Selection.

### Timer RB Mode Register

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Address</th>
<th>After Reset</th>
<th>Bit Symbol</th>
<th>Bit Name</th>
<th>Function</th>
<th>RW</th>
</tr>
</thead>
</table>
| TRBMR  | 010Bh    | 00h         | TMOD0      | Timer RB operating mode select bits(1) | b1 b0  
                                          1 1: Programmable one-shot generation mode | RW |
|        |          |             | TMOD1      | Nothing is assigned. If necessary, set to 0.  
                                          When read, the content is 0. | — |
|        |          |             | TWRC       | Timer RB write control bit         | Set to 1 in programmable one-shot generation mode. | RW |
|        |          |             | TCK0       | Timer RB count source select bits(1) | b0 b4  
                                          0 0: f1  
                                          0 1: f8  
                                          1 0: Timer RA underflow  
                                          1 1: f2 | RW |
|        |          |             | TCK1       | Nothing is assigned. If necessary, set to 0.  
                                          When read, the content is 0. | — |
|        |          |             | TCKCUT     | Timer RB count source cutoff bit(1) | 0: Provides count source  
                                          1: Cuts off count source | RW |

**NOTES:**
1. Change bits TMOD1 and TMOD0; TCK1 and TCK0; and TCKCUT when both the TSTART and TCSTF bits in the TRBCR register set to 0 (count stops).

---

Figure 3.3  Registers TRBIOC and TRBMR in Programmable One-shot Generation Mode
Timer RB Prescaler Register

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Address</th>
<th>After Reset</th>
<th>Function</th>
<th>Setting Range</th>
<th>Setting Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>TRBPRE</td>
<td>010Ch</td>
<td>FFh</td>
<td>Counts an internal count source or timer RA underflow</td>
<td>00h to FFh</td>
<td>RW</td>
</tr>
</tbody>
</table>

NOTE:
1. When the TSTOP bit in the TRBCR register is set to 1, the TRBPRE register is set to FFh.

Timer RB Primary Register

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Address</th>
<th>After Reset</th>
<th>Function</th>
<th>Setting Range</th>
<th>Setting Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>TRBPR</td>
<td>010Eh</td>
<td>FFh</td>
<td>Counts timer RB prescaler underflow (one-shot width is counted)</td>
<td>00h to FFh</td>
<td>RW</td>
</tr>
</tbody>
</table>

NOTES:
1. When the TSTOP bit in the TRBCR register is set to 1, the TRBPRE register is set to FFh.

Figure 3.4 Registers TRBPRE and TRBPR in Programable One-shot Generation Mode
Figure 3.5 Operating Example of Timer RB in Programmable One-shot Generation Mode
3.3 One-Shot Trigger Selection

In programmable one-shot generation mode and programmable wait one-shot generation mode, operation starts when a one-shot trigger is generated while the TCSTF bit in the TRBCR register is set to 1 (count starts).

A one-shot trigger can be generated by either of the following causes:

- 1 is written to the TOSST bit in the TRBOCR register by a program.
- Trigger input from the INT0 pin.

When a one-shot trigger occurs, the TOSSTF bit in the TRBOCR register is set to 1 (one-shot operation in progress) after one or two cycles of the count source have elapsed. Then, in programmable one-shot generation mode, count operation begins and one-shot waveform output starts. (In programmable wait one-shot generation mode, count operation starts for the wait period.) If a one-shot trigger occurs while the TOSSTF bit is set to 1, no retriggering occurs.

To use trigger input from the INT0 pin, input the trigger after making the following settings:

- Set the PD4_5 bit in the PD4 register to 0 (input port).
- Select the INT0 digital filter with bits INT0F1 and INT0F0 in the INTF register.
- Select both edges or one edge with the INT0PL bit in INTEN register. If one edge is selected, further select falling or rising edge with the INOSEG bit in TRBIOC register.
- Set the INT0EN bit in the INTEN register to 0 (enabled).
- After completing the above, set the INOSTG bit in the TRBIOC register to 1 (INT0 pin one-shot trigger enabled).

Note the following points with regard to generating interrupt requests by trigger input from the INT0 pin.

- Processing to handle the interrupts is required. Refer to 12. Interrupts in “R8C/24, R8C/25 Group Hardware Manual” (Document No. REJ09B0244), for details.
- If one edge is selected, use the POL bit in the INT0IC register to select falling or rising edge. (The INOSEG bit in the TRBIOC register does not affect INT0 interrupts).
- If a one-shot trigger occurs while the TOSSTF bit is set to 1, timer RB operation is not affected, but the value of the IR bit in the INT0IC register changes.
3.4 Notes on Timer RB

- Timer RB stops counting after a reset. Set the values in the timer RB and timer RB prescalers before the count starts.
- Even if the prescaler and timer RB is read out in 16-bit units, these registers are read 1 byte at a time by the MCU. Consequently, the timer value may be updated during the period when these two registers are being read.
- In programmable one-shot generation mode and programmable wait one-shot generation mode, when setting the TSTART bit in the TRBCR register to 0 (count stops) or setting the TOSSP bit in the TRBOCR register to 1 (one-shot stops), the timer reloads the value of reload register and stops. Therefore, in programmable one-shot generation mode and programmable wait one-shot generation mode, read the timer count value before the timer stops.
- The TCSTF bit remains 0 (count stops) for 1 to 2 cycles of the count source after setting the TSTART bit to 1 (count starts) while the count is stopped.
  
  During this time, do not access registers associated with timer RB(1) other than the TCSTF bit.
  
  The TCSTF bit remains 1 for 1 to 2 cycles of the count source after setting the TSTART bit to 0 (count stops) while the count is in progress.
  
  During this time, do not access registers associated with timer RB(1) other than the TCSTF bit.
  
  Timer RB counting is stopped when the TCSTF bit is set to 0.

NOTE:

1. Registers associated with timer RB: TRBCR, TRBOCR, TRBMR, TRBPRE, TRBSC, and TRRBPR.

- If the TSTOP bit in the TRBCR register is set to 1 during timer operation, timer RB stops immediately.
- If 1 is written to the TOSST or TOSSP bit in the TRBOCR register, the value of the TOSSTF bit changes after one or two cycles of the count source have elapsed. If the TOSSP bit is written to 1 during the period between when the TOSST bit is written to 1 and when the TOSSTF bit is set to 1, the TOSSTF bit may be set to either 0 or 1 depending on the content state. Likewise, if the TOSST bit is written to 1 during the period between when the TOSSP bit is written to 1 and when the TOSSTF bit is set to 0, the TOSSTF bit may be set to either 0 or 1.
4. Program Overview

By starting one-shot operation, a signal will be output from the TRBO pin as follows.

- One-shot pulse output time: 1 ms
  \[1 \text{ ms} = 20 \text{ MHz} \times f_2 \times (\text{TRBPRE} + 1) \times (\text{TRBPR} + 1)\]
  \[= 50 \text{ ns} \times 2 \times (199 + 1) \times (49 + 1)\]

- “H” output during one-shot and “L” output when the timer stops.
- INTO pin one-shot trigger disabled.

Figure 4.1 shows the Pin Used.

4.1 Faction Table

Table 4.1

<table>
<thead>
<tr>
<th>Declaration</th>
<th>void timer_rb_init(void)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Overview</td>
<td>SFR initial setting associated with timer RB</td>
</tr>
<tr>
<td>Argument</td>
<td>Argument name</td>
</tr>
<tr>
<td></td>
<td>None</td>
</tr>
<tr>
<td>Variable used</td>
<td>Variable name</td>
</tr>
<tr>
<td>(global)</td>
<td>None</td>
</tr>
<tr>
<td>Return value</td>
<td>Type</td>
</tr>
<tr>
<td></td>
<td>None</td>
</tr>
<tr>
<td>Function</td>
<td>Initialize the SFR registers associated with time RB</td>
</tr>
</tbody>
</table>
4.2 Flow Chart
4.2.1 Main functions

main()

asm("FCLR I")  
prc0 ← 1  

System control register protect cancelled

cm13 ← 1  

XIN-XOUT pin

cm15 ← 1  

Select XIN-XOUT drive capacity: HIGH

cm05 ← 0  

Main clock oscillation starts

while(i <= 255) i++

Wait until oscillation stabilizes

ocd2 ← 0  

Select main clock

cm16 ← 0  

Main clock frequency: no divide

cm17 ← 0  

CM16 and CM17 enabled

cm06 ← 0  

System control register protect

prc0 ← 0  

Interrupt disabled

Timer RB SFR initial setting

Initialize timer RB SFR setting
(Set to programmable one-shot generation mode)

timer_rb_init()

asm("FSET I")

ir_trbic ≠ 0

No

Timer RB interrupt request?

Yes

trbic ← 0x00

Set timer RB interrupt request bit to 0
(Set the IR bit to 0 using MOV instruction)
4.2.2 Timer RB SFR Initial Setting

```
timer_rb_init()
tstart_trbcr ← 0

Yes
tcstf_trbcr = 0 ?
   trbic ← 0x00
   tstop_trbcr ← 1
   timer_rb_init()

No
   trbpre ← 200 - 1
   trbpr ← 50 - 1
   topl_trbioc ← 0
   tocnt_trbioc ← 0
   inostg_trbioc ← 0
   tmod0_trbmr ← 0
   tmod1_trbmr ← 1
   twrc_trbmr ← 1
   tck0_trbmr ← 1
   tck1_trbmr ← 1
   tckcut_trbmr ← 0
   tstart_trbcr ← 1

Yes
tcstf_trbcr = 1 ?
   No
tossf_trbocr = 0 ?
      Yes
   Yes
   return

No
   tossf_trbocr = 0 ?
      Yes
   Yes
   timer_rb_one_shot_starts
```

- **Timer RB operation stops**
- **Timer RB interrupt disabled**
- Initialize registers TRBPRE, TRBSC, and TRBPR, and bits TSTART and TCSTF in TRBCR register
- One-shot pulse output time: set to 1 ms
  \[(20 \text{ MHz} \times f2 \times 200 \times 50 = 1 \text{ ms})\]
- "H" output during one-shot pulse period
- "L" output when timer stops
- Set to 0 in programmable one-shot generation mode
- INT0 pin one-shot trigger disabled
- Set to 10 in programmable one-shot generation mode
- Set to 1 in programmable one-shot generation mode
- Timer RB count source: f2
- Provide count source
- Timer RB operation starts
- Confirm timer RB one-shot is stopped
- Timer RB one-shot starts
5. Sample Programming Code
   Download a sample program on the Renesas Technology website.
   For download, click Application Notes in the left-hand side menu from the top page of the R8C/Tiny Series.

6. Reference Document
   Hardware Manual
   R8C/25 Group Hardware Manual
   (Download the latest version from the Renesas Technology website.)

   Technical News/Technical Update
   (Download the latest information from the Renesas Technology website.)
Website and Support

Renesas Technology website
http://www.renesas.com/

Inquiries
http://www.renesas.com/inquiry
csc@renesas.com

<table>
<thead>
<tr>
<th>REVISION HISTORY</th>
<th>R8C/25 Group Timer RB in Programmable One-shot Generation Mode</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rev.</td>
<td>Date</td>
</tr>
<tr>
<td>------</td>
<td>------</td>
</tr>
<tr>
<td>1.00</td>
<td>Sep 15, 2006</td>
</tr>
</tbody>
</table>

Page Summary
Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.
2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein. The information described here may contain technical inaccuracies or typographical errors. Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors. Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).
4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
5. Renesas Technology Corp. semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.
7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination. Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.

© 2006. Renesas Technology Corp., All rights reserved. Printed in Japan.