

SH7216 Series

RET05B0012-0100

Rev.1.00

Digital Power Control: Battery Charger

Sep 30, 2010

Introduction

This application note describes the design of a battery charger based on the Renesas SH7216 microcontrollers.

This battery charger provide a reference design using Renesas SH7216 MCU, which has the fast processing core and I/O port for the controller needs. It has a fast charging system, which can shorten charging time for large capacity battery.

Target Device

SuperH RISC engine Family SH7216 Series

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1. System Architecture

The Battery Charger consists of three main sections. These are:

- CPU Board
- Three Batteries in series
- Battery Charger Board

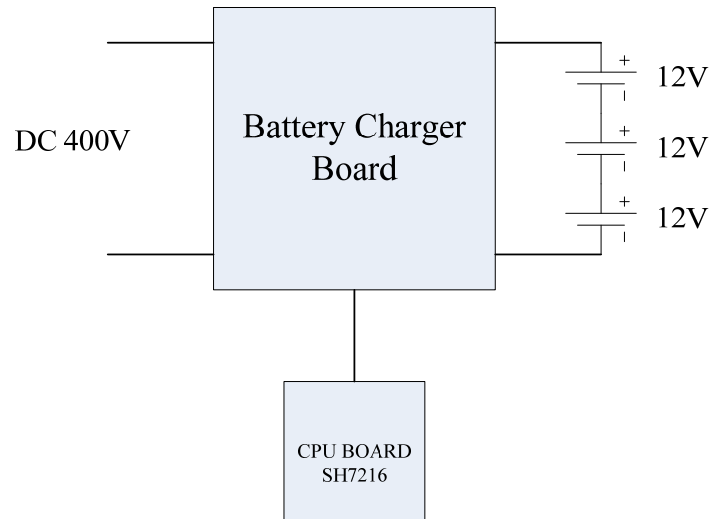


Figure 1 Block diagram

2. Specifications

Battery Charger Board:

The detail designed specifications of the battery charger are shown in Table 1.

Table 1 Specifications

	Charger
Input Voltage	DC 400 V
Output Voltage	DC 31.5~40.5V
Output Waveform	DC
Type/Topology	Flyback
Max. Output Current	DC 1.7 A
Max. Output Power	68.85 W
Battery	12V * 3

CPU Board:

- Renesas Microcontroller: SH7216

Battery:

- Three 12V 7.2Ah Batteries in series

NOTE:

The use of high voltage power supplies is extremely dangerous. Only authorized personnel working in a controlled environment are allowed to change the system configuration, at their own risk.

The voltage supply in the standard configuration is 400Vdc.

Renesas does not assume any responsibility for any voluntary modification of the standard demonstration hardware as well as over any misuse of the proposed system.

2.1 CPU Board

The CPU board can be used to control the battery charger. The microcontroller responsible for the following tasks:

- 1) Battery charging
- 2) Regulation PI algorithm
- 3) MOSFETs management.

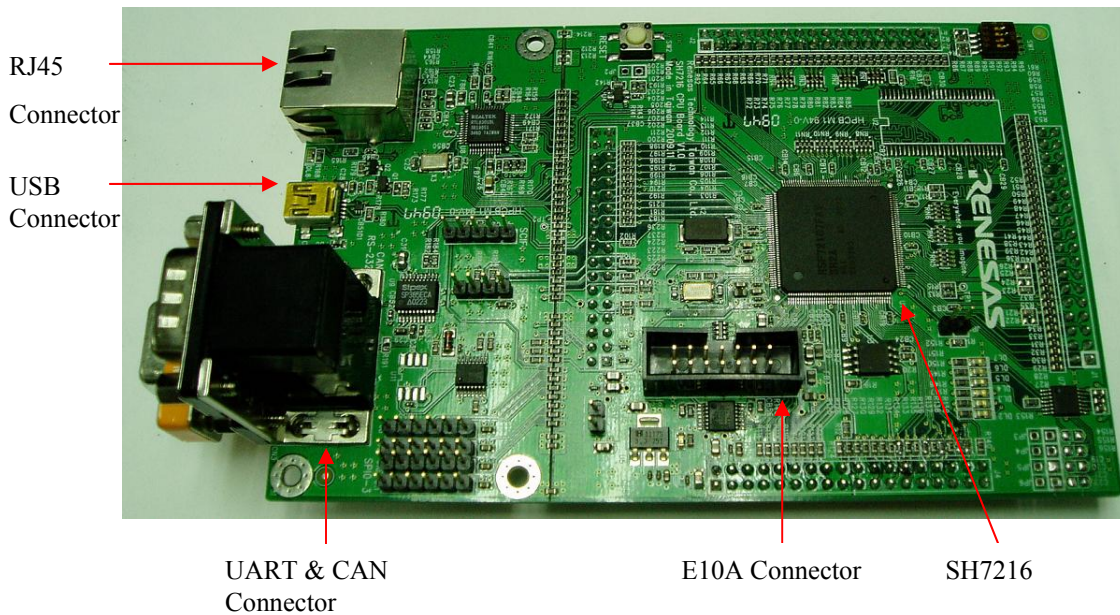


Figure 5 CPU Board

3. Control Block Diagram

The PI algorithm method is shown in Figure 9. The output voltage of charger is regulated at 36 V with 1.7 A current limit to prevent damaging of battery.

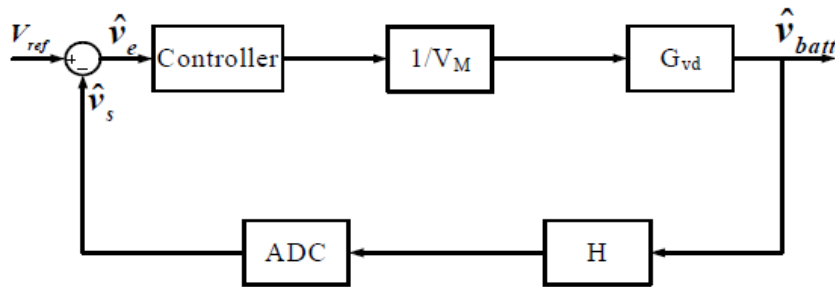


Figure 9 PI algorithm for charger

4. Hardware Block Diagram Analysis

The charger circuit is shown in figure 12. The type of charger circuit is flyback. Since the output power is quite low, the basic topology is a flyback converter with input bridge rectifier.

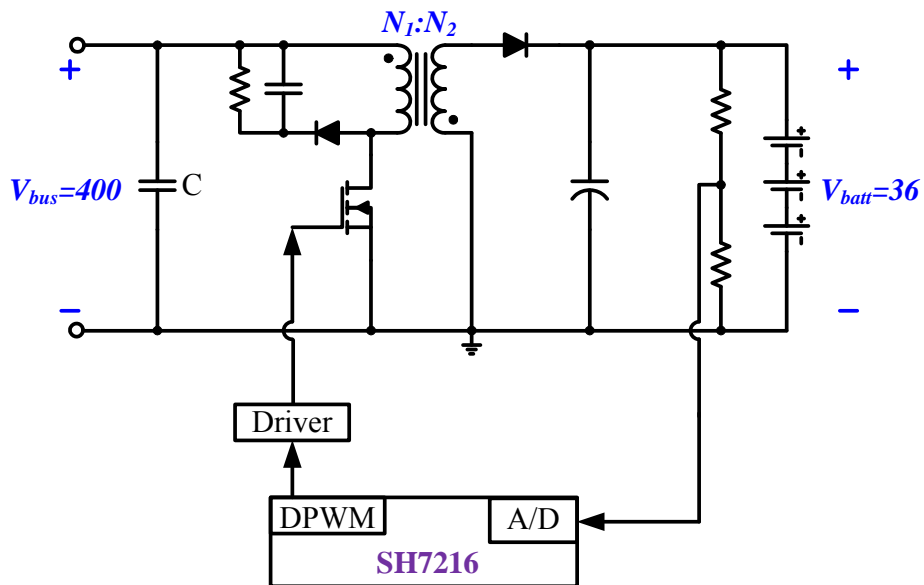


Figure 12 charger circuit

5. Software description

List and description of the battery charger software modules and functions

The main settings are:

io_set_cpg: performs the PLL initialization

WDT.WRITE.WTCSR = 0xa51e;

=> WDT stop, WDT count clock setting
=> 1/4096 x P-clock (50MHz; 20.97msec)

WDT.WRITE.WTCNT = 0x5a85;

=> Counter initial setting 10msec

CPG.FRQCR.WORD = 0x0303;

=> Clockin = 12.5MHz

=> I Clock = 200MHz, B Clock = 50MHz

=> P Clock = 50MHz

CPG.MCLKCR.BIT.MSDIVS = 1;

=> MTU2S = 100MHz

CPG.ACLKCR.BIT.ASDIVS = 3;

=> AD = 50MHz

STB.CR3.BYTE = 0x02;

=> Module Standby Clear

=> HIZ,MTU2S,MTU2,POE2,IIC3,ADC0,Reserve(1),FLASH

STB.CR4.BYTE = 0xE2;

=> Module Standby Clear

=> SCIF3,Reserve(0),CMT,Reserve(1),EtherC

STB.CR5.BYTE = 0x12;

=> Module Standby Clear

=> SCI0,SCI1,SCI2,SCI4,ADC1

charger_init: performs the MTU2 initialization

MTU2S3.TCR.BYTE = 0x20;

=> CCLR = 0x1;

=> TCNT cleared by TGRA compare match/input capture

=> CKEG = 0x0; Count at rising edge

=> TPSC = 0x0; Internal clock: counts on Pφ/1

MTU2S3.TMDR.BIT.MD = 0x2;

=> Operate as PWM mode 1

MTU2S3.TMDR.BIT.BFB = 1;

MTU2S3.TMDR.BIT.BFA = 1;

MTU2S3.TIOR.BIT.IOA = 0x02;

MTU2S3.TIOR.BIT.IOB = 0x01;

=> Initial output is 1. 0 output at compare match

MTU2S3.TGRA = 0x3E8;

MTU2S3.TGRB = 0x001;

MTU2S3.TGRC = 0x3E8;

MTU2S3.TGRD = 0X001;

MTU2S3.TBTM.BIT.TTSB = 1;

MTU2S3.TBTM.BIT.TTSA = 1;

MTU2S3.TCNT = 0x0000;

ADC0.ADCR.BIT.ADCS=0x0;

=> 0: Single-cycle scan

ADC0.ADANSR.BIT.ANS3=0x1;

=> AD3 is used as analog input channel

ADC0.ADBYPSCR.BIT.SH=0x1;

=> Turn on the sample and hold function

INTC.IPR12.WORD = 0xE000;

MTU2S3.TIER.BIT.TGIEA = 0x01;

PFC.PDCRL3.BIT.PD9MD = 0x4;

PFC.PDCRL3.BIT.PD8MD = 0x4;

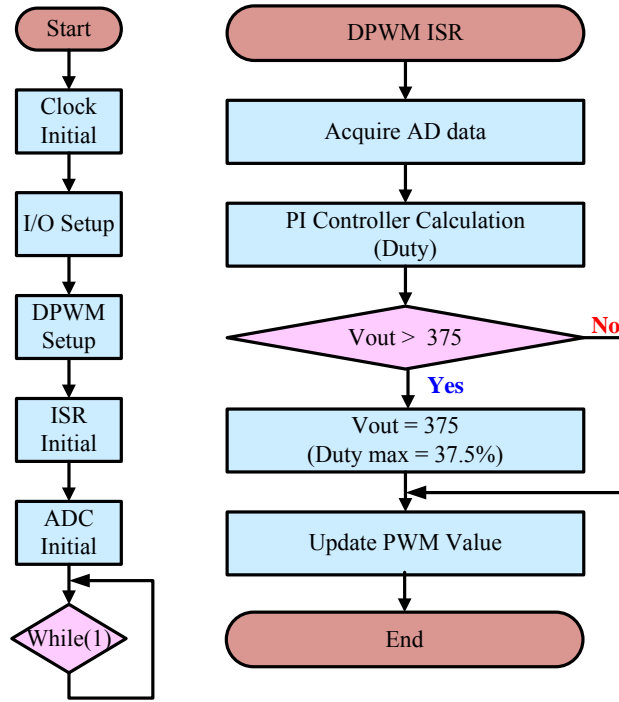


Figure 15 Flow chart for charger

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Revision Record

Rev.	Date	Description	
		Page	Summary
1.00	Sep.30.10	—	First edition issued

General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this manual, refer to the relevant sections of the manual. If the descriptions under General Precautions in the Handling of MPU/MCU Products and in the body of the manual differ from each other, the description in the body of the manual takes precedence.

1. Handling of Unused Pins

Handle unused pins in accord with the directions given under Handling of Unused Pins in the manual.

- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.

In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.

In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable.

When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to one with a different type number, confirm that the change will not lead to problems.

- The characteristics of MPU/MCU in the same group but having different type numbers may differ because of the differences in internal memory capacity and layout pattern. When changing to products of different type numbers, implement a system-evaluation test for each of the products.

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