
RX62T

R01AN0253ET0100

SPWM Motor Control

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Introduction

This document shows how to use RX62T to drive PMSM by Sinusoidal PWM method (SPWM).The system is starting up by 120 degree trapezoidal waveform .Then, changes to use sinusoidal waveform if the conditions which means commutation position counting for six-hundred times in one phase is satisfied.

Target Device

RX62T

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1. System Configuration

The system uses RX62T RSK and inverter circuit to drive a PMSM (Permanent Magnet Synchronous Motors, PMSM) which is 750 Watt. We used 120 degree trapezoidal waveform control firstly and changed from 120 degree trapezoidal control to sinusoidal waveform control if the commutation position counts for six hundred times in one phase.

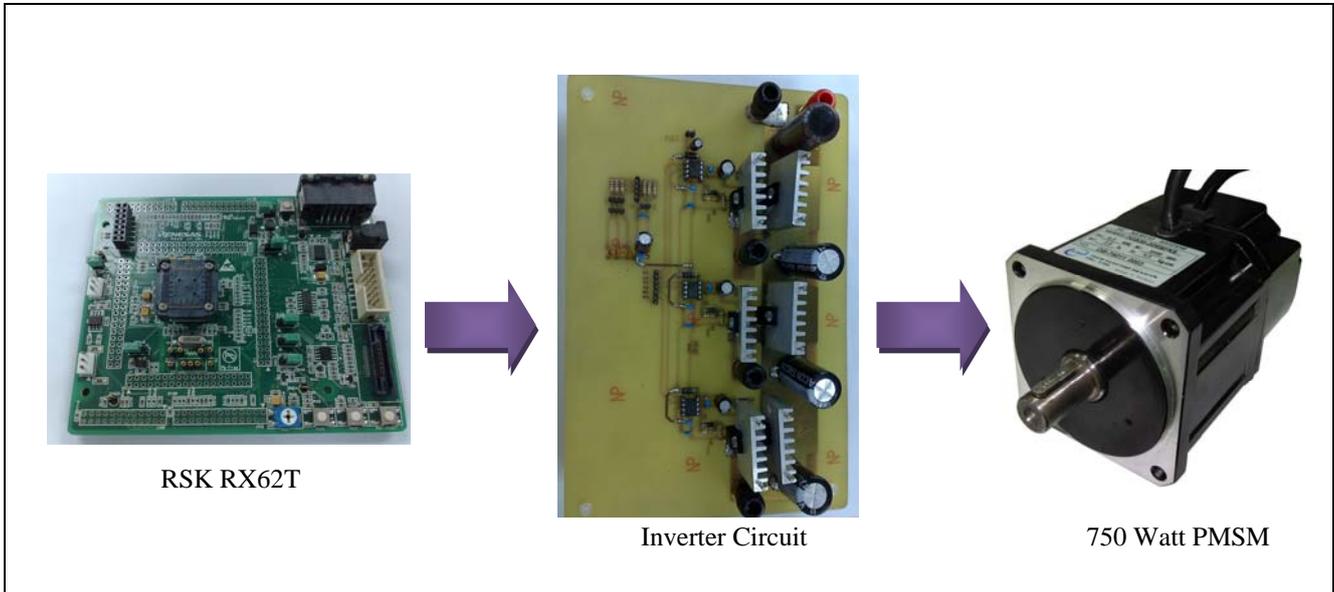


Figure 1-1 System Configuration

Fig. 1-2 shows the driver stage of inverter circuit and Fig. 1.3 shows the inverter circuit. In Fig. 1-2 left part, CHOP_iP and Hin_i2 (i= A, B, C) stand for upper stage of three phase inverter driver; CHOP_iN and Lin_i2 (i= A, B, C) stand for lower stage of three phase inverter driver; in Fig. 1-2 right part, the buffer stage of inverter driver is shown as below. You can find the full inverter circuit in sample code file.

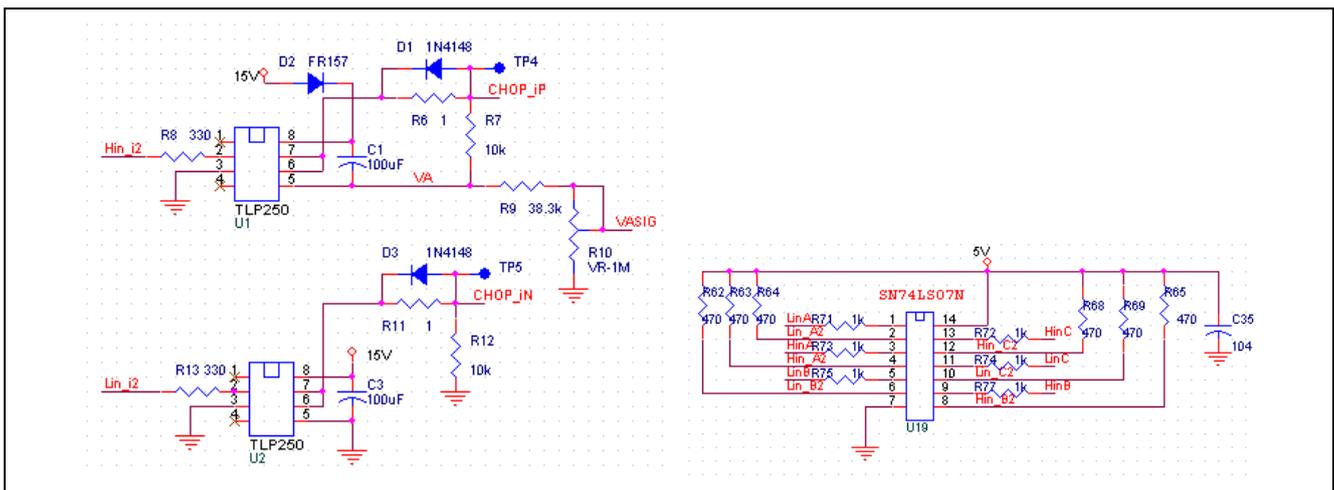


Figure 1-2 Inverter Driver (Left) and buffer stage (Right)

Fig. 1-3 shows the inverter circuit.

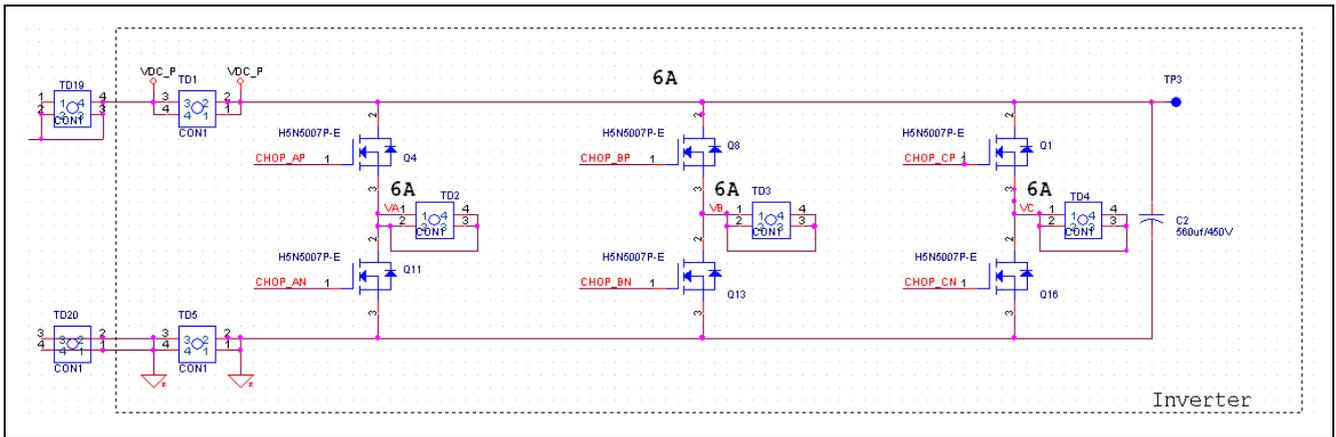


Figure 1-3 Inverter Circuit

Please find the attached file to see the whole inverter circuit.

2. Block Diagram

MTU3 module is used in this application. MTU3 channel 3 and channel 4 are used to drive the motor in complementary mode. Channel 0 is used for calculating the period of the 60electrical degree by detecting the transition of the hall sensor signal. Channel 1 is the timer for one electrical degree. Channel 2 is the timer for thirty degrees. Channel 6 is connected with a low-pass filter to show the current degree of the phase A.

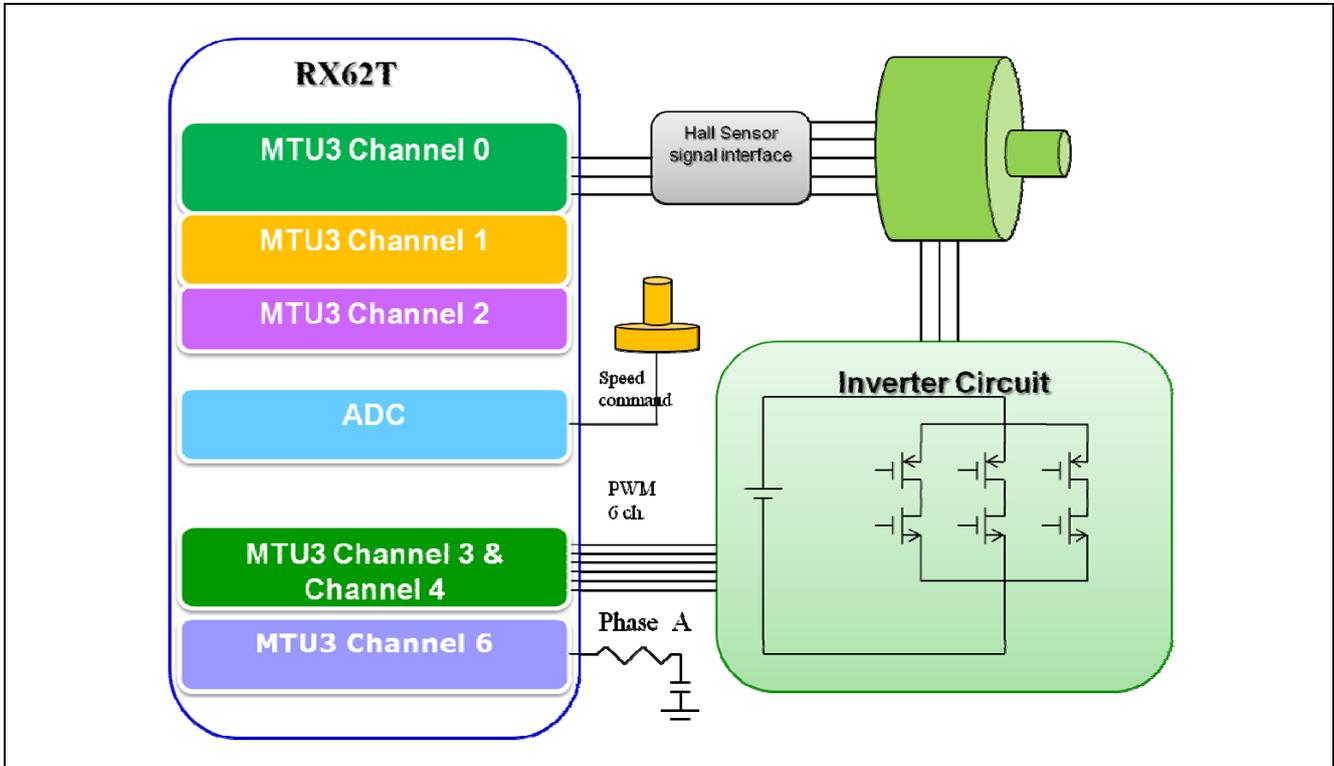


Figure 2-1 Block Diagram

3. Program Flow Chart

The setting procedure is shown at following flowcharts.

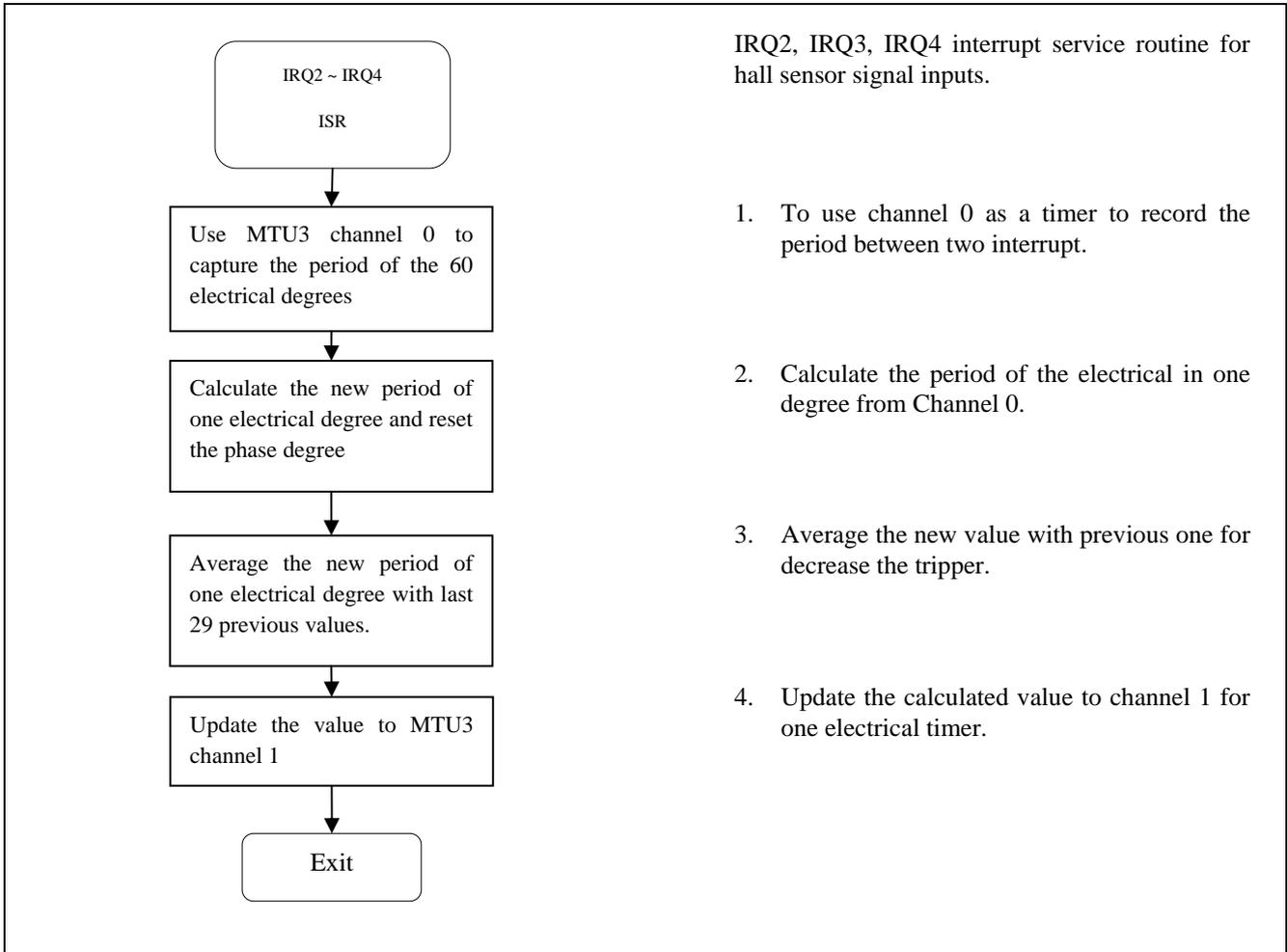


Figure 3-1 ISR of Hall Sensor Signal Input

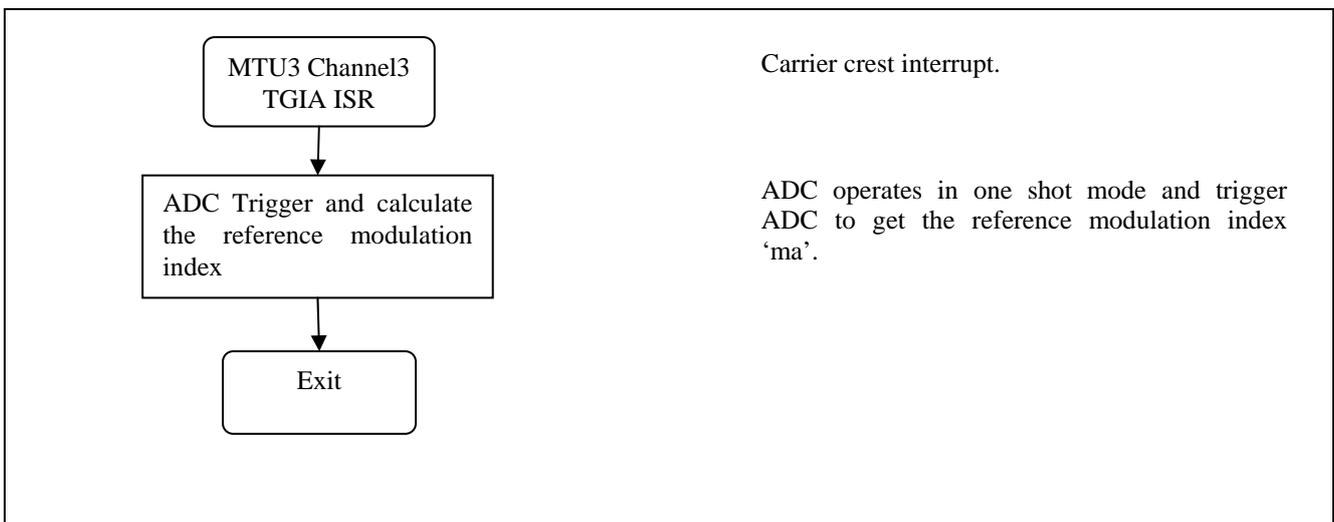


Figure 3-2 Carrier Interrupts (MTU3 channel3 TGIA)

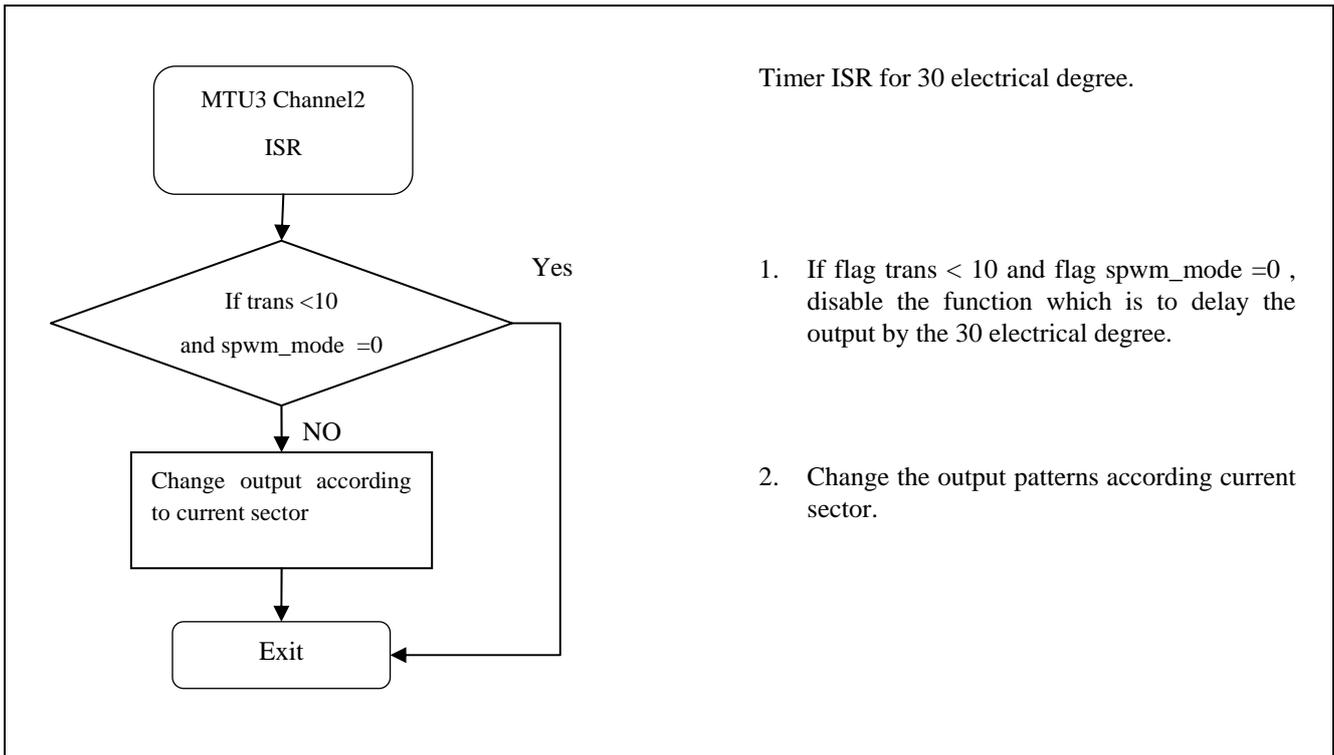


Figure 3-3 ISR for 30 Electrical Degrees in MTU3 Channel 2

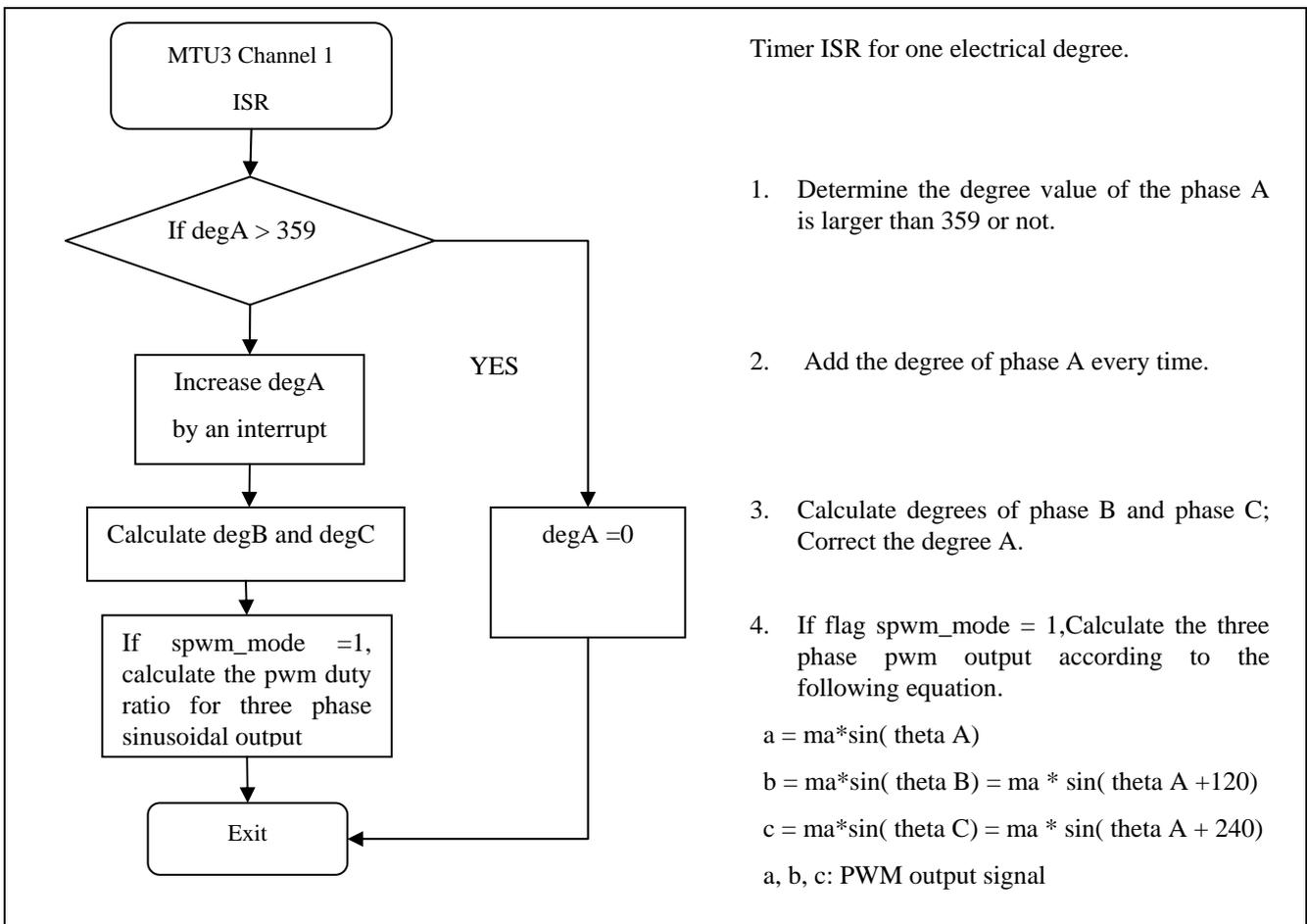


Figure 3-4 ISR in MTU3 Channel 1

4. Experimental Result

The experimental results are shown as Fig. 4.1 and Fig. 4.2. The system is starting up by 120 degree trapezoidal control and changes to sinusoidal waveform control after the commutation position in phase A counts for six hundred times.

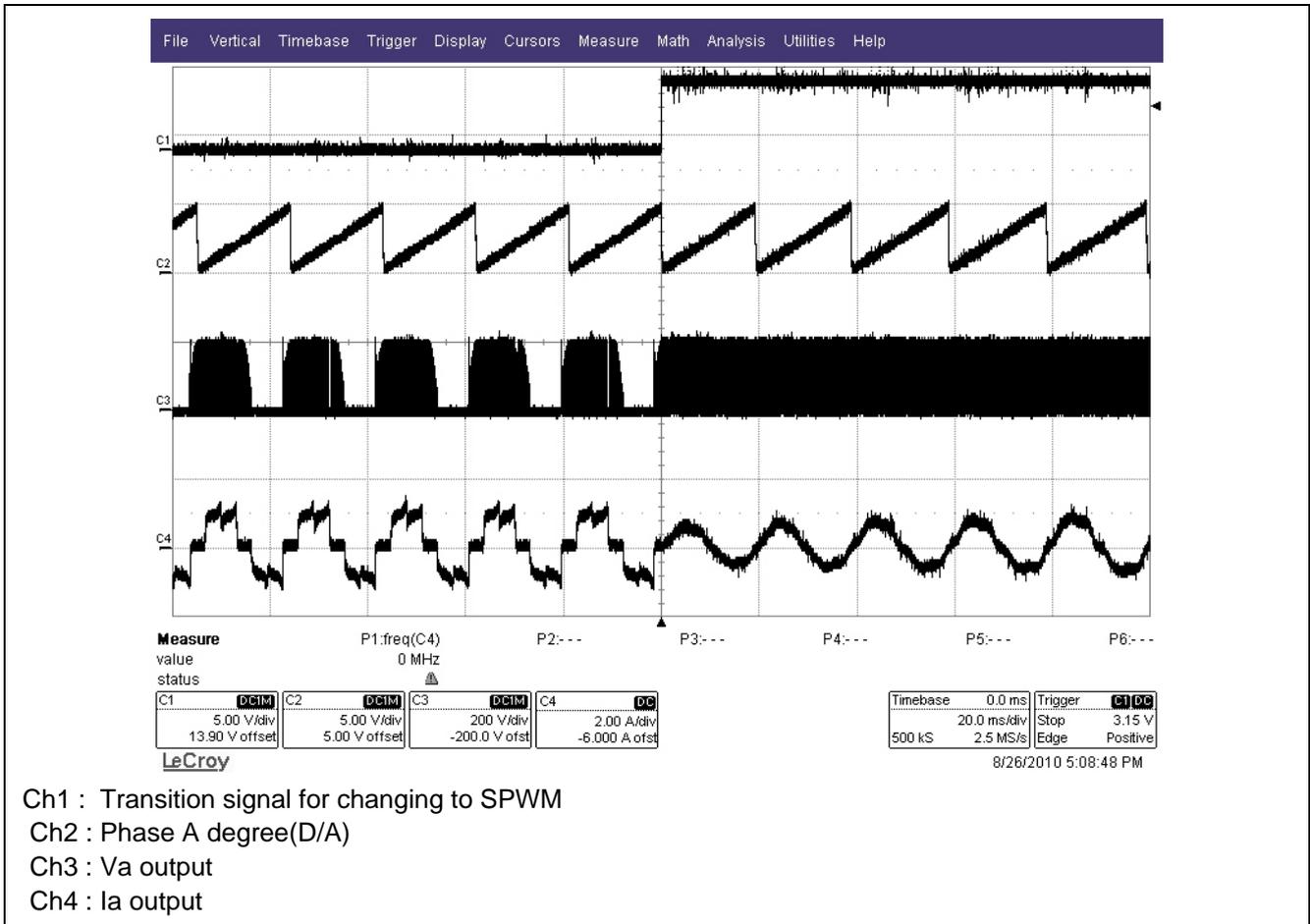
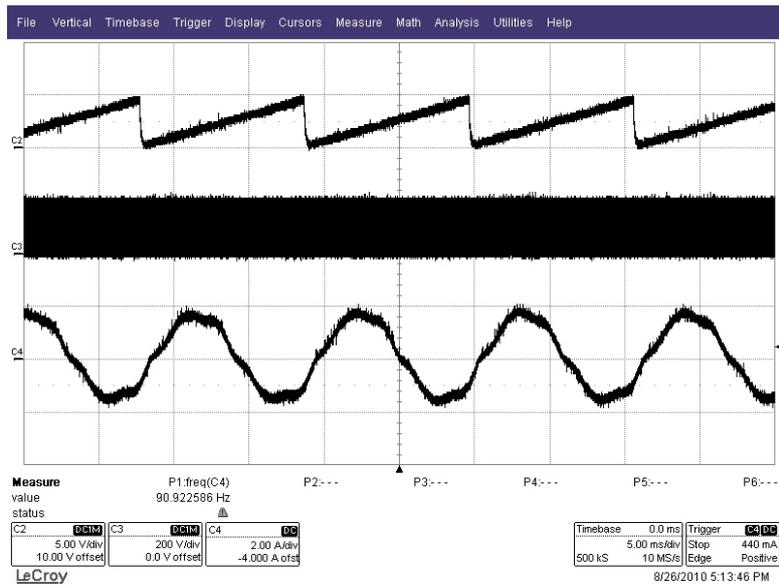
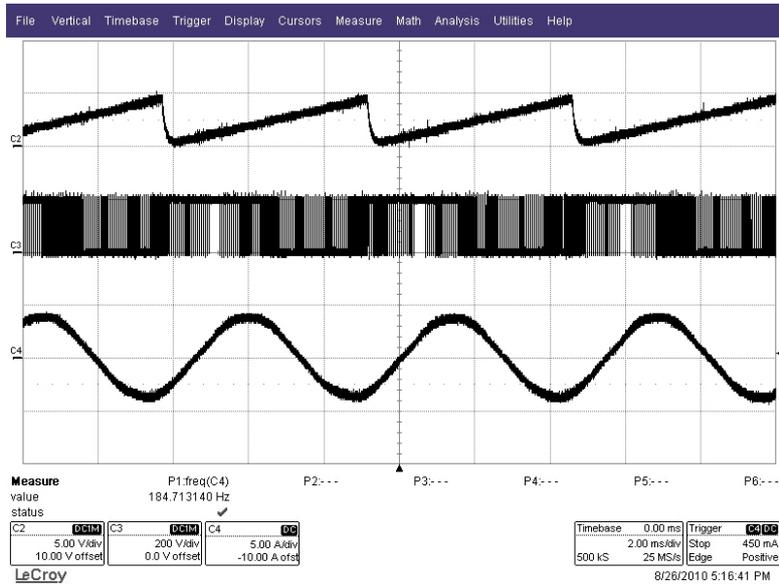


Figure 4-1 SPWM Transition

The current waveform will be more similar to pear sinusoidal waveform when the modulation rate is added. See the result at Fig. 4-2.



(a) $V_{DC} = 200 \text{ V}$, $f_{sw} = 20 \text{ kHz}$, $M = 0.4 \text{ p.u.}$ for SPWM



(b) $V_{DC} = 200 \text{ V}$, $f_{sw} = 20 \text{ kHz}$, $M = 0.9 \text{ p.u.}$ for SPWM

Ch2 : 360 degree(D/A)
 Ch3 : Va
 Ch4 : Ia

Figure 4-2 Driving Current of the SPWM

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Revision Record

Rev.	Date	Description	
		Page	Summary
1.00	Jun.07.11	—	First edition issued

General Precautions in the Handling of MPU/MCU Products

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1. Handling of Unused Pins

Handle unused pins in accord with the directions given under Handling of Unused Pins in the manual.

- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.

In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

- The characteristics of an MPU or MCU in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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