RX Family
Specification Differences Between the RSPI and SCI (Simple SPI Mode) and Selection Guide

Abstract
This document describes the specification differences between the serial peripheral interface (RSPI) and simple SPI mode (simple SPI) of the serial communications interface (SCI) and also describes the procedure to select whether to use the RSPI or simple SPI.

Products
RX Family *1

Note 1. The RX610, RX62N, RX621, RX62T, and RX62G Groups do not have the simple SPI.
Contents

1. Specifications .......................................................................................................................... 3
2. Reference Application Note .................................................................................................. 3
3. Flowchart for Selecting the SPI Module .............................................................................. 4
4. Reference Documents ......................................................................................................... 5
1. Specifications

Table 1.1 lists the Specification Differences Between the RSPI and Simple SPI. Some specifications vary depending on the product.

Table 1.1 Specification Differences Between the RSPI and Simple SPI

<table>
<thead>
<tr>
<th>Item</th>
<th>RSPI</th>
<th>Simple SPI</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bit rate (^*)</td>
<td>Master Maximum 25 Mbps</td>
<td>Maximum 12.5 Mbps</td>
</tr>
<tr>
<td></td>
<td>Slave Maximum 6.25 Mbps</td>
<td>Maximum 6.25 Mbps</td>
</tr>
<tr>
<td>Transfer function</td>
<td>Clock polarity Selectable</td>
<td>Selectable</td>
</tr>
<tr>
<td></td>
<td>Clock phase Selectable</td>
<td>Selectable</td>
</tr>
<tr>
<td>Data format</td>
<td>MSB first/LSB first Selectable</td>
<td>Selectable</td>
</tr>
<tr>
<td></td>
<td>Transfer bit length Selectable</td>
<td>8 bits fixed</td>
</tr>
<tr>
<td></td>
<td>Transmit/receive buffer 32 bits × 4-stage buffer</td>
<td>8 bits × 1-stage buffer</td>
</tr>
<tr>
<td></td>
<td>Shift register 32 bits</td>
<td>8 bits</td>
</tr>
<tr>
<td></td>
<td>Maximum frames 4 frames</td>
<td>1 frame</td>
</tr>
<tr>
<td></td>
<td>Parity Whether or not to add a parity bit can be selected</td>
<td>Parity bit cannot be added.</td>
</tr>
<tr>
<td>Slave select pin output control</td>
<td>Hardware control with the dedicated pin</td>
<td>Software control with the general port</td>
</tr>
<tr>
<td>Pin control when a mode fault occurs</td>
<td>Place pins PSPCK and MOSI in high-impedance to release the SPI bus</td>
<td>Place pins SCK and SMOSI in high-impedance to release the SPI bus</td>
</tr>
<tr>
<td>Error detection</td>
<td>Overrun Detectable</td>
<td>Detectable</td>
</tr>
<tr>
<td></td>
<td>Mode fault Detectable</td>
<td>Detectable</td>
</tr>
<tr>
<td></td>
<td>Parity Detectable</td>
<td>Not detectable</td>
</tr>
<tr>
<td>Interrupt sources</td>
<td>Receive buffer full, transmit buffer empty, mode fault, overrun, parity error, RSPI idle</td>
<td>Receive data full, transmit data empty, receive error, transmit end</td>
</tr>
</tbody>
</table>


2. Reference Application Note

For additional information associated with this document, refer to the following application note.

- RX Family RSPI Sequence Control and Interrupt Generation Timing (R01AN1105EJ)
3. Flowchart for Selecting the SPI Module

Figure 3.1 shows a flowchart for selecting the recommended SPI module based on the differences between the RSPI and simple SPI in the RX63N Group. Some specifications vary with the product.

![Flowchart for Selecting the SPI Module](image)

Note 1. The communication speed varies depending on the product.

Figure 3.1 Selecting the SPI Module
4. Reference Documents

User’s Manual: Hardware
RX63N Group, RX631 Group User’s Manual: Hardware Rev.1.80 (R01UH0041EJ)
The latest version can be downloaded from the Renesas Electronics website.

Technical Update/Technical News
The latest information can be downloaded from the Renesas Electronics website.

User’s Manual: Development Tools
The latest version can be downloaded from the Renesas Electronics website.

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## REVISION HISTORY

<table>
<thead>
<tr>
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<th>Date</th>
<th>Description</th>
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<td>Aug. 18, 2014</td>
<td>First edition issued</td>
</tr>
</tbody>
</table>

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The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins
   Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.
   - The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on
   The state of the product is undefined at the moment when power is supplied.
   - The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
   - In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.
   - In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

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   Access to reserved addresses is prohibited.
   - The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals
   After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.
   - When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal.
   - Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products
   Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.
   - The characteristics of an MPU or MCU in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.
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