

RL78/L1C

R01AN2307EG0100

Rev.1.00

Low Power Mode for e² studio

Sep 11, 2014

Introduction

The Low Power sample code demonstrates the low power modes HALT, SNOOZE and STOP for reducing current consumption.

Target Device

RL78/L1C

Development environment

IDE: e² studio

Compiler: GNURL78 v13.02 -ELF

Hardware: Renesas Starter Kit for RL78/L1C

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1. Installation

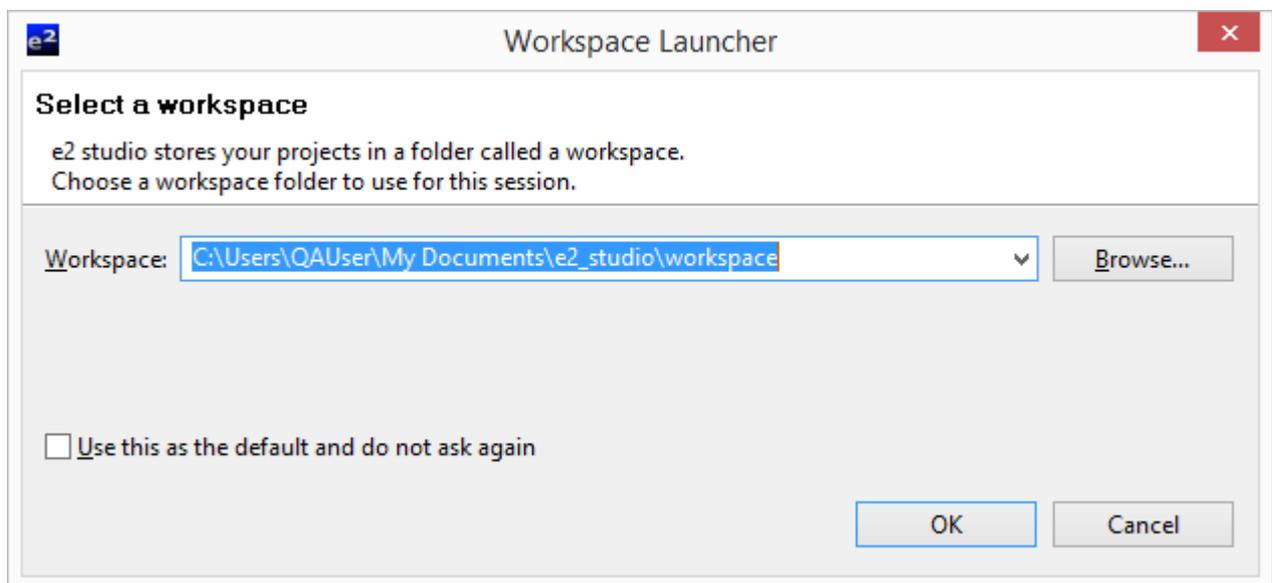
This section assumes e² studio IDE is already installed on the user's personal computer (PC). It is also assumed that the following software and versions are installed:

- Renesas e² studio Version 2.02.00.13 or later
- Application Leading Tool for RL78 Version 1.01.00.02
- GNURL78 Version 13.02-ELF

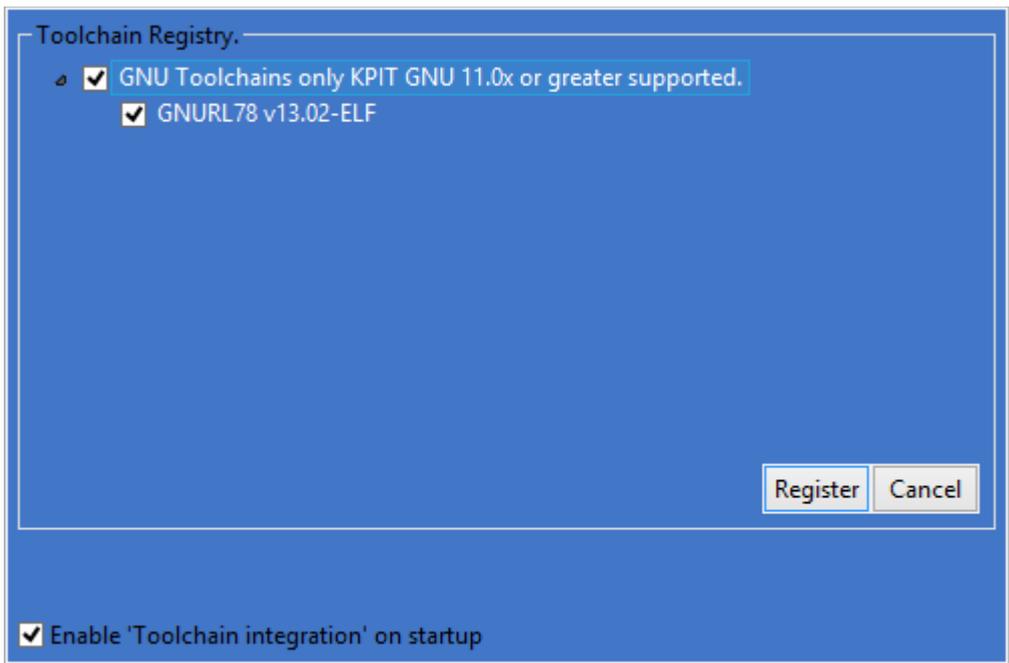
Create a new folder and name it as 'RSKRL78L1C_Workspace'. Copy the zipped file 'an_r01an2125eg0100_rl78l1c_power_save.zip', available in the Application Note package downloaded from the website, to this folder. Extract the 'an_r01an2125eg0100_rl78l1c_power_save.zip' file to the 'RSKRL78L1C_Workspace' folder.

2. Creating the Project Workspace

Open e² studio IDE by clicking the Windows Start button, select All Programs > Renesas Electronics e2 studio > Renesas e2 studio.



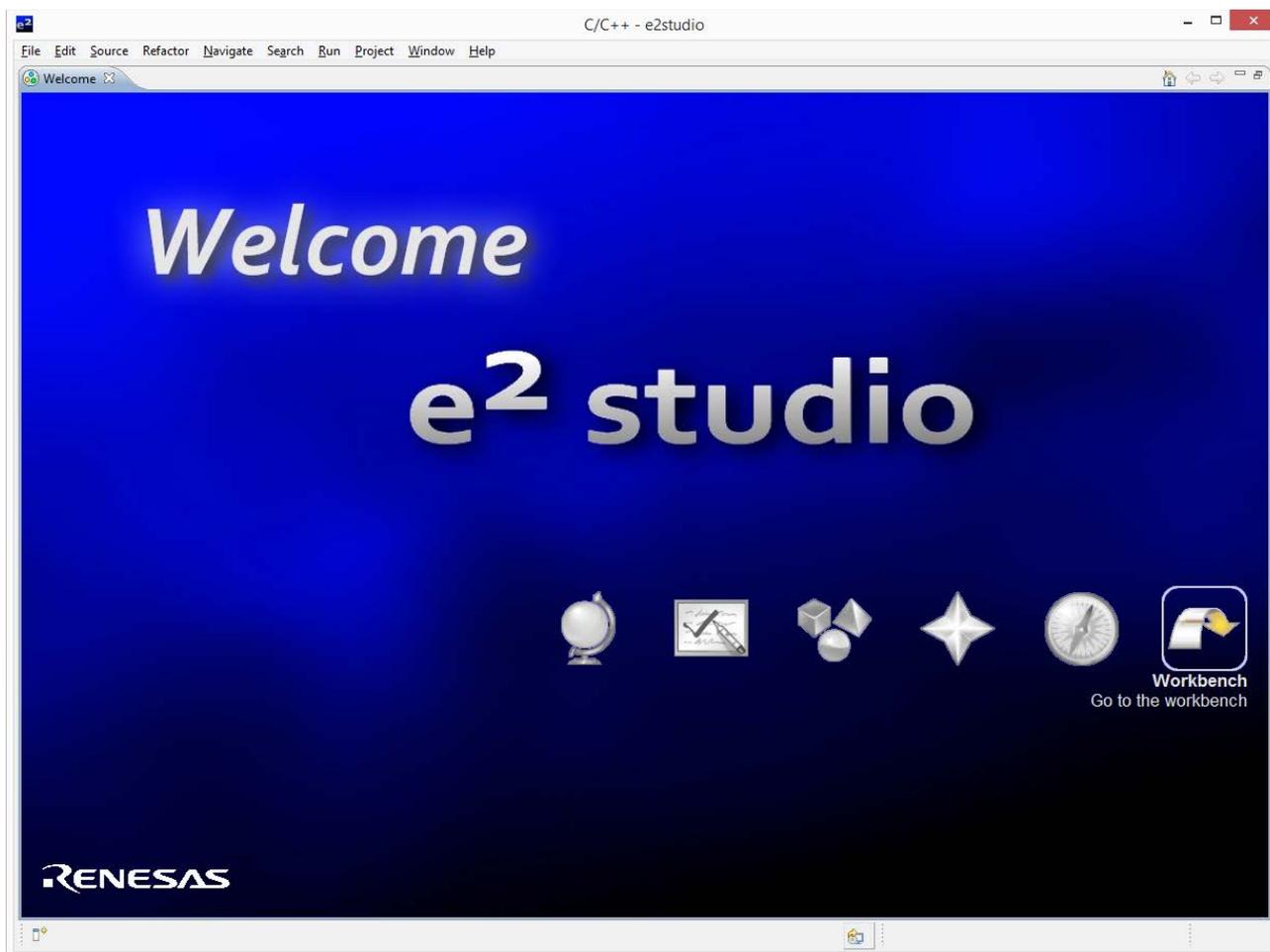
Select <OK>



Select 'GNU Toolchains only...' and 'GNURL78 v13.02-ELF' checkboxes. Click 'Register'. A dialog will appear. Click <OK>.

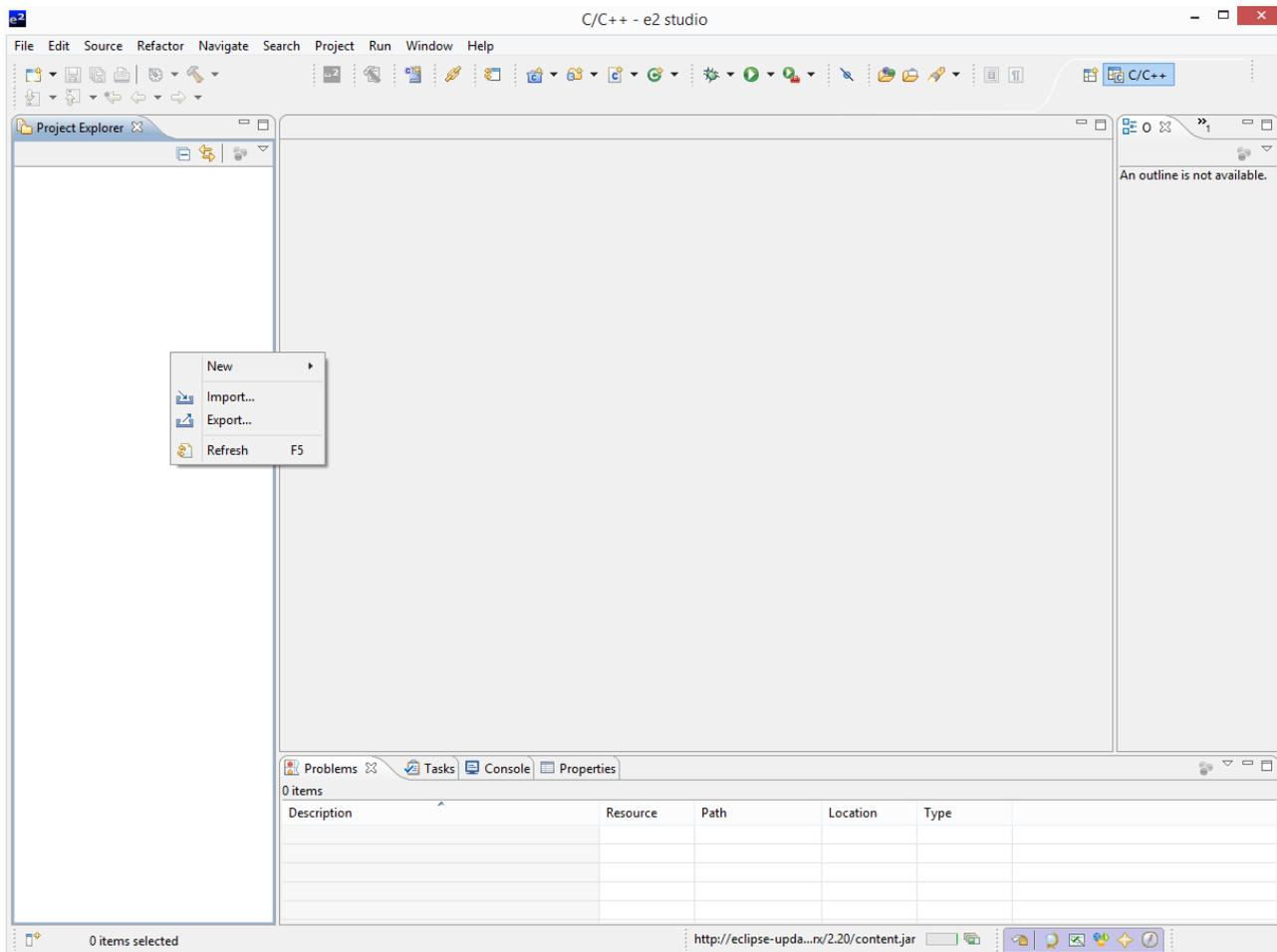
"Selected Toolchains were successfully integrated with e2studio". Click <OK>.



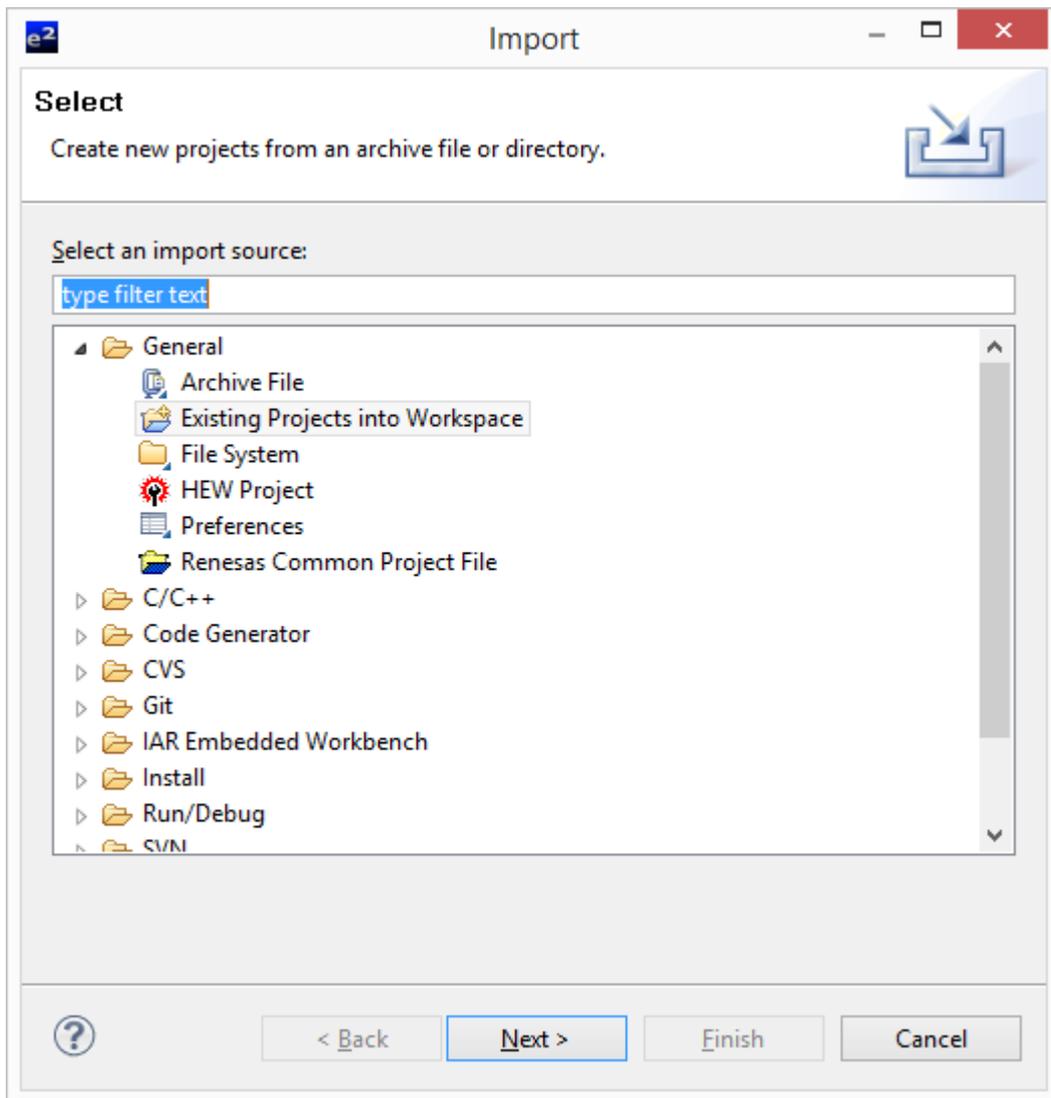


On the welcome screen, select 'Go to the Workbench' icon as shown above.

1. Once the e² studio environment has initialised, right click in the project explorer window and click <Import...>



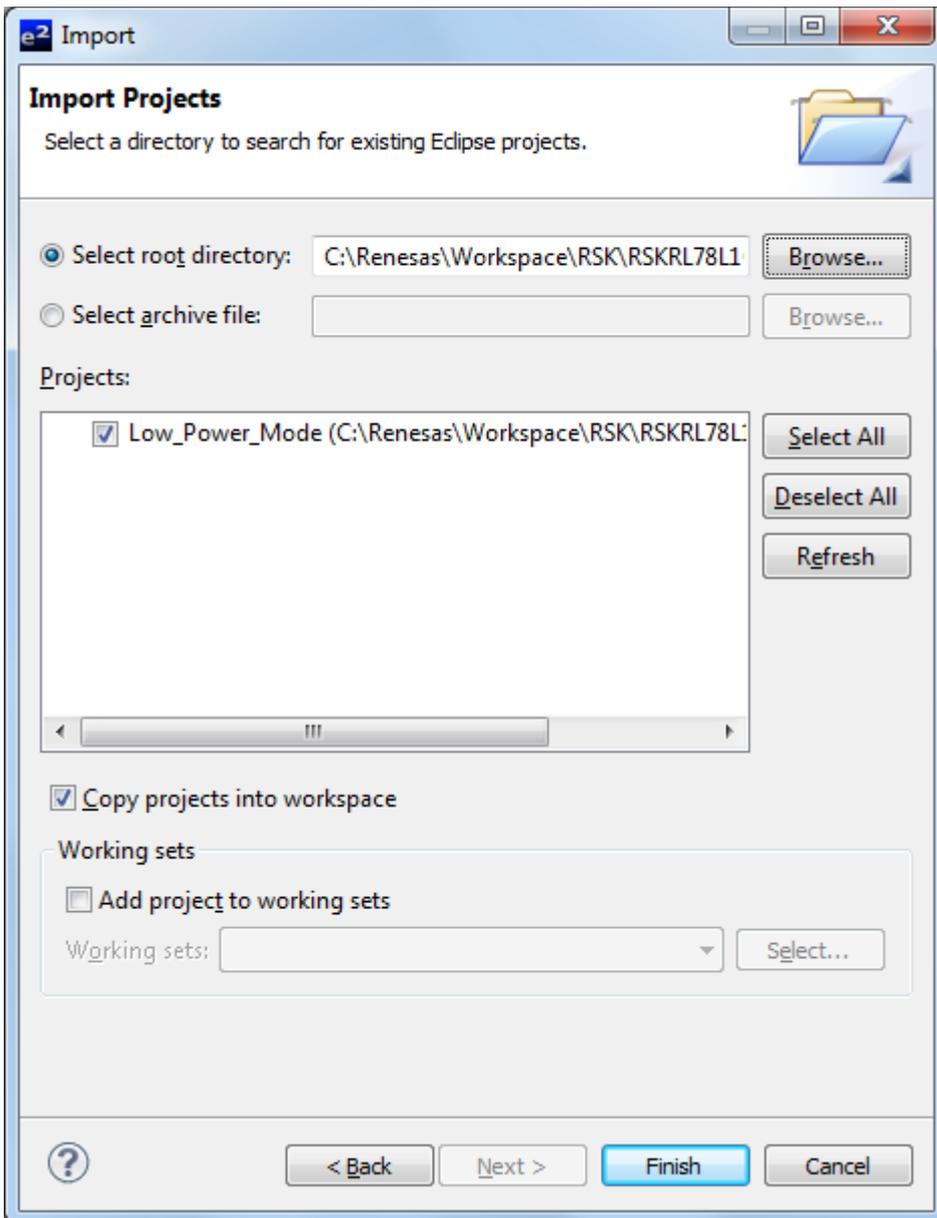
2. The Import dialog will now appear. Expand the “General” folder icon, and select “Existing Projects into Workspace”, then click ‘Next’.



3. The Import Dialog will now appear and specify the project to import. Click the “Browse” button and locate the directory created in section 1: ‘RSKRL78L1C_Workspace’.

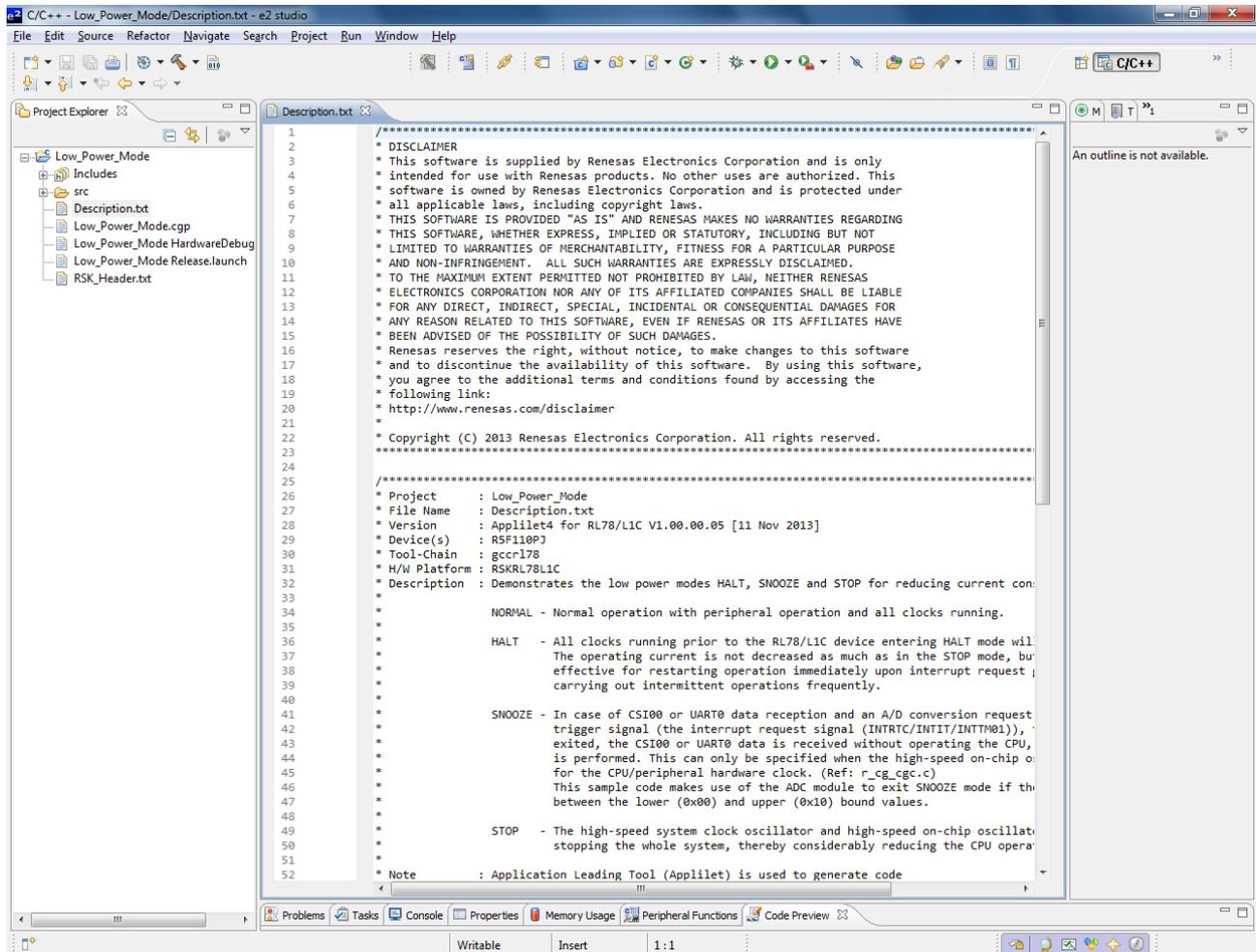
Also ensure that the ‘Copy projects into workspace’ option is ticked, and then click <Finish>

The IDE e² studio will load the project.



3. Opening Sample Code and Source

Once the project has been opened, the source code and all dependent files can be opened in the editor by expanding the folders in the Project Tree window and double clicking the files listed. All files have been grouped according to their file type.



4. Source Code Functionality

The source code project is specifically written to run on the appropriate RSK. However, this source code can be useful as an example even without the RSK.

The project was written using source files containing API functions generated using Code Generator. The project will contain a C source file 'r_cg_main.c'. This source file includes the C function main(). All source files and dependent files whose filenames are prefixed with 'r_cg_' were generated using Applilet4 (Application Leading Tool). For more information, refer to Description.txt.

5. Appendix

Example of comment block with code functionality.

```
/*
*****
* Project    : Low_Power_Mode
* File Name  : Description.txt
* Version    : Applilet4 for RL78/L1C V1.00.00.05 [11 Nov 2013]
* Device(s)  : R5F110PJ
* Tool-Chain : gccrl78
* H/W Platform : RSKRL78L1C
* Description : Demonstrates the low power modes HALT, SNOOZE and STOP for reducing current consumption.
*
*           NORMAL - Normal operation with peripheral operation and all clocks running.
*
*           HALT - All clocks running prior to the RL78/L1C device entering HALT mode will continue to run.
*                The operating current is not decreased as much as in the STOP mode, but the HALT mode is
*                effective for restarting operation immediately upon interrupt request generation and
*                carrying out intermittent operations frequently.
*
*           SNOOZE - In case of CSI00 or UART0 data reception and an A/D conversion request by the timer
*                trigger signal (the interrupt request signal (INTRTC/INTIT/INTTM01)), the STOP mode is
*                exited, the CSI00 or UART0 data is received without operating the CPU, and A/D conversion
*                is performed. This can only be specified when the high-speed on-chip oscillator is selected
*                for the CPU/peripheral hardware clock. (Ref: r_cg_cgc.c)
*                This sample code makes use of the ADC module to exit SNOOZE mode if the conversion result is
*                between the lower (0x00) and upper (0x10) bound values.
*
*           STOP - The high-speed system clock oscillator and high-speed on-chip oscillator stop,
*                stopping the whole system, thereby considerably reducing the CPU operating current.
*
* Note      : Application Leading Tool (Applilet) is used to generate code
*            for this sample program. The name Applilet is used hereafter.
*            The Applilet details and version number are given in the file
*            header for each relevant module.
*
* Modifications:
*
*           External Device/Component
*           -----
*
*           Remove R125 and connect an ammeter across J5, configure the ammeter for current measurement.
*
*
*
*/
```

* Operation : Fit J6, pins 2-3 and J7 pins 2-3.

* Power the RSK from a +5V centre positive PSU via the PWR connector.

*

* Instructions :

* 1. Compile the sample code and download to the RSK. Click the 'Resume'

* button to start program execution. Click again if the program stops at main().

* Press 'Stop' and disconnect the debugger from the RSK.

*

* 2. A real time clock will be displayed in the format:

* HH:MM

* The : will flash every second; the seconds will be displayed in the main display.

* The clock is updated when the device is in normal operating mode.

*

* 3. Turn the potentiometer (RV1) fully clockwise. This sets the ADC input

* to the highest value.

*

* 4. The sample code will cycle through the four different operating modes on each

* switch press starting with NORMAL, HALT, SNOOZE then STOP.

*

* 5. Press SW1 to prepare for HALT mode, HALT will be displayed on the LCD.

* Press SW1 again to enter HALT mode and observe the current for HALT mode.

*

* 6. Press SW1 to wake the MCU and prepare for SNOOZE mode. SNZ will be displayed on the LCD.

* Press SW1 again to enter SNOOZE mode and observe the current. Note that the LCD display will be

* turned off in this mode. For SNOOZE mode the MCU is configured to apply the high speed

* system clock to the ADC at 1 minute intervals and perform an A/D conversion. If the result of this

* conversion is in the range 0x00 to 0x10, then SNOOZE mode is exited, otherwise the MCU stays

* in SNOOZE mode and wakes after another 1 minute interval to perform the next A/D conversion.

*

* 7. Turn the potentiometer (RV1) fully anti-clockwise to wake the MCU and to prepare for STOP mode.

* Note that this may not cause an immediate wake up of the MCU as the RTC is configured to perform

* an A/D conversion at one minute intervals. Once the MCU is awake STOP will be displayed on the LCD.

* Press SW1 again to enter STOP mode and observe the current. Note that the LCD display will be turned

* off in this mode.

*

* 8. Press SW1 to exit STOP mode and go to step 2 to repeat the test.

*

*****/

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Revision History

Rev.	Date	Description	
		Page	Summary
1.0	Sep 11, 2014	-	First edition issued

General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.

- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

- The characteristics of an MPU or MCU in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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