RL78/G23
Low-speed On-chip Oscillator (LOCO) Clock Frequency Correction

Introduction
This application note explains how to correct the oscillation clock frequency of the low-speed on-chip oscillator (LOCO) by using the low-speed on-chip oscillator trimming register (LIOTRM) incorporated in RL78/G23.

An error in the oscillation frequency of the low-speed on-chip oscillator (LOCO) is detected using a hi-speed on-chip oscillator and the low-speed on-chip oscillator trimming register (LIOTRM) is adjusted to set the oscillation frequency of the LOCO close to 32.768 kHz.

Target Device
RL78/G23

When applying the sample program covered in this application note to another microcomputer, modify the program according to the specifications for the target microcomputer and conduct an extensive evaluation of the modified program.
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1. Specifications

1.1 Overview of Specifications

In this application note, an error in the clock oscillation frequency of the LOCO is detected using a HOCO. The HIOTRM register is adjusted to set the oscillation frequency of the LOCO close to 32.768 kHz.

When the start switch is pressed, the 32-bit interval timer counts the period of LOCO. The HOCO is used for the count clock of the 32-bit interval timer. If the count value measured by the 32-bit interval timer is beyond the target range, the LIOTRM register is adjusted to set the oscillation frequency of the LOCO close to 32.768 kHz. The target range of the LOCO oscillation frequency is 4 MHz ± 1.3% (3.948 MHz to 4.052 MHz) under the condition of HOCO frequency ±1.0%.

32-bit interval timer measures period of the low-speed on-chip oscillator. To enhance accuracy, the pulse interval is measured four times to detect an error in the oscillation frequency of the LOCO.

Caution: Specified times and calibration methods in this sample code are used as examples. In this sample code, input from the start switch is used to start calibration, to simplify processing flows and to provide clear understanding. Adjust the timing of starting calibration and the intervals between start timings according to the system. This application note describes two calibration methods. Select the method most appropriate to the system for use.

Table 1-1 lists the peripheral functions to be used and their uses.

Table 1-1 Peripheral Function and Use

<table>
<thead>
<tr>
<th>Peripheral Function</th>
<th>Use</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pin input edge detection interrupt</td>
<td>Used for the correction start switch.</td>
</tr>
<tr>
<td>32-bit interval timer channel 0, 1</td>
<td>Used for calibration of LOCO.</td>
</tr>
<tr>
<td>Timer Array Unit channel 0</td>
<td>Used to prevent chattering on the correction start switch.</td>
</tr>
</tbody>
</table>
1.2 Outline of Operation

The following describes the settings of peripheral functions.

(1) Initialization of external interrupt (INTP0)

The settings are shown in Table 1-2.

<table>
<thead>
<tr>
<th>Item</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Valid edge</td>
<td>Falling edge</td>
</tr>
</tbody>
</table>

(2) Initialization of 32-bit interval timer

Use in 16-bit capture mode.

The settings are shown in Table 1-3.

<table>
<thead>
<tr>
<th>Item</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating mode</td>
<td>16-bit capture mode</td>
</tr>
<tr>
<td>Operating clock (fITL0)</td>
<td>fITL0</td>
</tr>
<tr>
<td>Count source division ratio</td>
<td>fITL0 (non-divided)</td>
</tr>
<tr>
<td>Capture trigger</td>
<td>Interrupt on compare match with ITLCMP01</td>
</tr>
<tr>
<td>Capture clock</td>
<td>fSXp</td>
</tr>
<tr>
<td>Interval of capture trigger</td>
<td>128 counts</td>
</tr>
</tbody>
</table>

(3) Initialization of Timer Array Unit

The settings are shown in Table 1-4.

<table>
<thead>
<tr>
<th>Item</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating mode</td>
<td>Interval timer</td>
</tr>
<tr>
<td>Operating clock</td>
<td>CK00</td>
</tr>
<tr>
<td>Count source division ratio</td>
<td>fCLK/2^8</td>
</tr>
<tr>
<td>Interval period</td>
<td>10 ms</td>
</tr>
</tbody>
</table>
1.3 Description of Calibration Methods

This section describes the two calibration methods to be used in this application note.

Use the 32-bit interval timer in 16-bit capture mode to measure the period of the low-speed on-chip oscillator. Select the interrupt on compare match with ITLCMP01 for the capture trigger and generate a trigger every 2 counts with the clock of the low-speed on-chip oscillator (32.768 kHz).

Select a high-speed on-chip oscillator clock (32 MHz) for the count clock.

To enhance accuracy, the subsystem clock cycle is measured four times, and the four captured values are added up to calculate an error in the LOCO oscillation clock frequency.

The target frequency range should be within ±2LSB (±0.6%) of the trimming register (LIOTRM).

The calculated value of the count for four captures is the value in the following table.

### Table 1-5 Range of Count Values

<table>
<thead>
<tr>
<th>LOCO Clock Frequency (f_{IL})</th>
<th>Count Value Obtained through Four Times of Capture (Calculated Value)</th>
</tr>
</thead>
<tbody>
<tr>
<td>32.768 kHz</td>
<td>7812.5</td>
</tr>
<tr>
<td>32.768 kHz – 0.6%</td>
<td>32.571kHz</td>
</tr>
<tr>
<td>32.768 kHz + 0.6%</td>
<td>32.965kHz</td>
</tr>
</tbody>
</table>

Shown in Table 1-5, the target range of the count value obtained through four times of capture is set to 7766 to 7859.

When the frequency accuracy of the high-speed on-chip oscillator is within ±1.0%, the frequency accuracy of the medium-speed on-chip oscillator will be within ±1.6%.

If the obtained count value is 7765 or less, this means that the LOCO clock is slower than the target frequency. If it is 7860 or more, this means that the LOCO clock is faster than the target frequency. Determine the direction of correction of the LIOTRM register value (speeding up/slowing down), according to the count value, and perform calibration by incrementing the LIOTRM register value by ±1. When the count value is within the target range, the calibration is completed.

Figure 1-1 gives an example of calibration.
**Figure 1-1 Example of Calibration**

- **INTITL3**
  - Period: 2 clocks of $f_L$
  - 32MHz

- **32-bit interval timer channel 0, 1**
  - Get count value
  - Slow down LIOTRM ← -1

- **Target frequency**
  - Slow down LIOTRM ← -1

- **Obtain count value**
  - Get frequency
2. Operation Confirmation Conditions

The sample code of this application note has been tested under the following conditions.

Table 2-1 Operation Confirmation Conditions

<table>
<thead>
<tr>
<th>Item</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>MCU used</td>
<td>RL78/G23 (R7F100GLG)</td>
</tr>
<tr>
<td>Board used</td>
<td>RL78/G23 Fast Prototyping Board (RTK7RLG230CLG000BJ)</td>
</tr>
<tr>
<td>Operating frequency</td>
<td>High-speed on-chip oscillator clock: 32 MHz</td>
</tr>
<tr>
<td></td>
<td>CPU/peripheral hardware clock: 32 MHz</td>
</tr>
<tr>
<td>Operating voltage</td>
<td>3.3 V (can be operated at 1.8 V to 5.5 V)</td>
</tr>
<tr>
<td></td>
<td>LVD0 detection voltage: Reset mode</td>
</tr>
<tr>
<td></td>
<td>At rising edge TYP. 1.90 V (1.84 V to 1.95 V)</td>
</tr>
<tr>
<td></td>
<td>At falling edge TYP. 1.86 V (1.80 V to 1.91 V)</td>
</tr>
<tr>
<td>Integrated development environment (CS+)</td>
<td>CS+ V8.05.00 from Renesas Electronics Corp.</td>
</tr>
<tr>
<td>C compiler (CS+)</td>
<td>CC-RL V1.10.00 from Renesas Electronics Corp.</td>
</tr>
<tr>
<td>Integrated development environment (e2studio)</td>
<td>e2 studio V2021-04 (21.4.0) from Renesas Electronics Corp.</td>
</tr>
<tr>
<td>C compiler (e2studio)</td>
<td>CC-RL V1.10.00 from Renesas Electronics Corp.</td>
</tr>
<tr>
<td>Integrated development environment (IAR)</td>
<td>IAR Embedded Workbench for Renesas RL78 V4.21.1 from IAR Systems Corp.</td>
</tr>
<tr>
<td>C compiler (IAR)</td>
<td>IAR C/C++ Compiler for Renesas RL78 V4.21.1 from IAR Systems Corp.</td>
</tr>
<tr>
<td>Smart configurator (SC)</td>
<td>V1.0.1 from Renesas Electronics Corp.</td>
</tr>
<tr>
<td>Board support package (BSP)</td>
<td>V1.00 from Renesas Electronics Corp.</td>
</tr>
</tbody>
</table>
3. Hardware Descriptions

3.1 Example of hardware configuration

Figure 3-1 shows an example of the hardware configuration used in the application note.

Figure 3-1  Hardware Configuration

Caution: 1. The purpose of this circuit is only to provide the connection outline and the circuit is simplified accordingly. When designing and implementing an actual circuit, provide proper pin treatment and make sure that the hardware's electrical specifications are met (connect the input-only ports separately to VDD or VSS via a resistor).
2. Connect any pins whose name begins with EVSS to VSS and any pins whose name begins with EVDD to VDD, respectively.
3. VDD must be held at not lower than the reset release voltage (VLVD) that is specified as LVD.
3.2 List of Pins Used

Table 3-1 lists the pins used and their functions.

<table>
<thead>
<tr>
<th>Pin Name</th>
<th>I/O</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>P137/INTP0</td>
<td>Input</td>
<td>Start switch: Connects the switch for starting calibration.</td>
</tr>
<tr>
<td>P53</td>
<td>Output</td>
<td>Correction completion LED: Connects the LED that indicates correction completion.</td>
</tr>
</tbody>
</table>

Caution: In this application note, only the used pins are processed. When actually designing your circuit, make sure the design includes sufficient pin processing and meets electrical characteristic requirements.
4. Software Explanation

4.1 Setting of Option Byte

Table 4-1 shows the option byte settings.

Table 4-1 Option Byte Settings

<table>
<thead>
<tr>
<th>Address</th>
<th>Setting Value</th>
<th>Contents</th>
</tr>
</thead>
<tbody>
<tr>
<td>000C0H/010C0H</td>
<td>11101111B</td>
<td>Disables the watchdog timer. (Counting stopped after reset)</td>
</tr>
<tr>
<td>000C1H/010C1H</td>
<td>11111110B</td>
<td>LVD0 detection voltage: Reset mode</td>
</tr>
<tr>
<td></td>
<td></td>
<td>At rising edge TYP. 1.90 V (1.84 V to 1.95 V)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>At falling edge TYP. 1.86 V (1.80 V to 1.91 V)</td>
</tr>
<tr>
<td>000C2H/010C2H</td>
<td>11101000B</td>
<td>HS mode, High-speed on-chip oscillator clock (f_H): 32 MHz</td>
</tr>
<tr>
<td>000C3H/010C3H</td>
<td>10000101B</td>
<td>Enables on-chip debugging</td>
</tr>
</tbody>
</table>

4.2 List of Constants

Table 4-2 lists the constants that are used in the sample code.

Table 4-2 Constants

<table>
<thead>
<tr>
<th>Constant Name</th>
<th>Setting Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LIOTRM_MAX</td>
<td>11111111B</td>
<td>Maximum value of the LIOTRM register</td>
</tr>
<tr>
<td>LIOTRM_MIN</td>
<td>00000000B</td>
<td>Minimum value of the LIOTRM register</td>
</tr>
<tr>
<td>CCNT_MAX</td>
<td>7860</td>
<td>Upper threshold of clock count</td>
</tr>
<tr>
<td>CCNT_MIN</td>
<td>7765</td>
<td>Lower threshold of clock count</td>
</tr>
</tbody>
</table>
4.3 List of Variables
Table 4-3 lists the global variables.

<table>
<thead>
<tr>
<th>Type</th>
<th>Variable Name</th>
<th>Contents</th>
<th>Function Used</th>
</tr>
</thead>
<tbody>
<tr>
<td>uint8_t</td>
<td>calibration_count</td>
<td>Calibration count value</td>
<td>R_Main_Calibrate_LOCO()</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>R_Trimming_OCO()</td>
</tr>
<tr>
<td>uint8_t</td>
<td>calibrate_history</td>
<td>Calibration history</td>
<td>R_Main_Calibrate_LOCO()</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>R_Trimming_OCO()</td>
</tr>
<tr>
<td>uint16_t</td>
<td>count_value</td>
<td>Count value (Used as an argument of R_Trimming_OCO)</td>
<td>R_Main_Calibrate_LOCO()</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>R_Trimming_OCO()</td>
</tr>
<tr>
<td>uint16_t</td>
<td>max</td>
<td>Upper threshold of count</td>
<td>R_Main_Calibrate_LOCO()</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>R_Trimming_OCO()</td>
</tr>
<tr>
<td>uint16_t</td>
<td>min</td>
<td>Lower threshold of count</td>
<td>R_Main_Calibrate_LOCO()</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>R_Trimming_OCO()</td>
</tr>
</tbody>
</table>

4.4 List of Functions
Table 4-4 shows a list of functions.

<table>
<thead>
<tr>
<th>Function Name</th>
<th>Outline</th>
</tr>
</thead>
<tbody>
<tr>
<td>R_Main_Calibrate_LOCO</td>
<td>Calibration process</td>
</tr>
<tr>
<td>R_Trimming_OCO</td>
<td>LOCO clock correction</td>
</tr>
</tbody>
</table>
4.5 Specification of Functions

The function specifications of the sample code are shown below.

[Function Name] R_Main_Calibrate_LOCO

Outline: Calibration process
Header: None
Declaration: void R_Main_Calibrate(void)
Description: Frequency measurement and correction process for the low-speed on-chip oscillator.
Argument: None
Return Value: None
Outline: None

[Function Name] R_Trimming_OCO

Outline: LOCO clock correction
Header: None
Declaration: uint8_t R_Trimming_OCO(uint16_t count)
Description: This function sets the LIOTRM according to the argument and then determines whether to continue calibration.
Argument: count: [Target clock count value]
Return Value: [0]: Calibration ends.
[1]: Calibration continues.
Outline: None
4.6 Flowcharts

4.6.1 Main Processing

Figure 4-1 shows the flowchart of the main processing.

Figure 4-1 Main Processing

```plaintext
main()

Disable interrupts
DI()

Enable INTP0 interrupt
R_Config_INTC_INTP0_Start()

HALT instruction
HALT()

INTP0 interrupt?

yes

no

Disable INTP0 interrupt

Clear the TMIF00 interrupt request flag

Start timer array unit channel 0
R_Config_TAU0_0_Start()

INTTM00 interrupt?

yes

no

Start timer array unit channel 0
R_Config_TAU0_0_Stop()

Level of the start switch is low?

yes

no

Turn off the correction completion LED

Turn on the correction completion LED

P5.bit3 = 1

P5.bit3 = 0

Calibration process
R_Main_Calibrate_LOCO()

P5.bit3 = 0
```

4.6.2 System Initialization

R_Config.getID()
4.6.2 Calibration process
Figure 4-2 show the flowchart for calibration process.

Figure 4-2 Calibration process

- **R_Main_Calibrate_LOCO()**
  - Initialize the number of times of captures to 0
  - Set the correction continuation flag to 1
  - Initialize the number of times of correction to 0
  - Initialize the correction history to 0
  - Specify the range of correction
  - Measure cycle (The number of captures = 0; <4; +1)
  - INTITLC interrupt?
    - no
    - Accumulate the capture value
      - CAPF0CR = 1
      - ITFC0 = 0
      - ITLIF = 0
      - ITLEN00 = 1
      - ITLEN02 = 1
  - yes
    - Period measurement
      - Stop capture operation
      - Increment the number of times of correction by 1
      - LOCO correction function **R_Trimming_OCO()**
      - CORR (Correction flag = 0)
      - return
4.6.3 LOCO Correction Function

Figure 4-3 and Figure 4-4 show the flowcharts for the LOCO correction function.

Figure 4-3 LOCO Correction Function (1/2)

- **R_Trimming_OCO()**
  - Clear the correction continuation flag
  - Measured value <= Lower limit?
    - no
    - Specified value < Upper limit?
      - yes
      - Decrement the LIOTRM register value by 1
      - Update the correction history
      - The number of times of correction is less than 3?
        - yes
        - Set the correction continuation flag
        - The correction history shows no repetition?
          - yes
          - Set the correction continuation flag
          - return
        - no
        - no
        - no
    - yes
  - no
  - yes
  - yes
  - yes
  - yes
  - return
Figure 4-4 LOCO Correction Function (2/2)

1. Increment the LIOTRM register value by 1
2. Update the correction history
3. Check if the number of times of correction is less than 3?
   - If yes: Continue to step 4.
   - If no: Return to step 1.
4. Check if the correction history shows no repetition?
   - If yes: Set the correction continuation flag.
   - If no: Return to step 1.
5. **Sample Code**

Sample code can be downloaded from the Renesas Electronics website.

6. **Reference Documents**

- RL78/G23 User’s Manual: Hardware (R01UH0896)
- RL78 family user's manual software (R01US0015)

   The latest versions can be downloaded from the Renesas Electronics website.

   Technical update
   The latest versions can be downloaded from the Renesas Electronics website.

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Inquiries
http://www.renesas.com/contact/

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<table>
<thead>
<tr>
<th>Rev.</th>
<th>Date</th>
<th>Page</th>
<th>Summary</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.00</td>
<td>Apr.01.21</td>
<td>—</td>
<td>First Edition</td>
</tr>
<tr>
<td>1.01</td>
<td>May.18.21</td>
<td>7</td>
<td>Updated the Operation Confirmation Conditions</td>
</tr>
</tbody>
</table>
General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)
   A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on
   The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state
   Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins
   Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals
   After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin
   Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between \( V_{IL} \) (Max.) and \( V_{IH} \) (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between \( V_{IL} \) (Max.) and \( V_{IH} \) (Min.).

7. Prohibition of access to reserved addresses
   Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products
   Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.
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