Introduction

This document explains the specification and usage of RM-110-RFB-2 Bluetooth® Low Energy module. The document describes hardware platform information such as module interface, peripheral connection, its Bluetooth® connectivity, Radio performance, and its schematics. This RM-110-RFB-2 Bluetooth® Low Energy module is product of NAITO DENSEI MACHIDA MFG.CO., LTD. [1]

Target Device

RL78/G1D device (R5F11AGJ)

This RM-110-RFB-2 module includes Renesas’ Intelligent Bluetooth® Low Energy device, part number starting with R5F11A (256 KB program flash memory, 20 KB RAM and 8 KB data flash memory).
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1. Overview

RM-110-RFB-2 module uses Renesas Bluetooth® Low Energy module, RY7011A0000DZ00. The RM-110-RFB-2 module, therefore, has extended usability for evaluation to Renesas module, RY7011A0000DZ00 [2] with additional 32.768 kHz real time clock as well as external terminals for easy connection.

![Image of RM-110-RFB-2 module from top and bottom views]

Figure 1-1 RM-110-RFB-2 module

1.1 Specification Outline

The specification of RM-110-RFB-2 module is described as below Table 1.

Table 1 RM-110-RFB-2 Module Specification

<table>
<thead>
<tr>
<th>Item</th>
<th>Content</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dimension</td>
<td>40 mm x 19 mm x 12mm (1.57 in x 0.75 in x 0.47 in)</td>
</tr>
<tr>
<td>Operation Power Supply Voltage</td>
<td>1.6 V to 3.6 V Note1</td>
</tr>
<tr>
<td>Maximum Power Supply Voltage</td>
<td>3.6 V</td>
</tr>
<tr>
<td>Average Operation Current</td>
<td>10 µA Note2</td>
</tr>
<tr>
<td>Maximum Total Output Current</td>
<td>150 mA Note1</td>
</tr>
<tr>
<td>Operating Ambient Temperature/Humidity</td>
<td>-25°C to +75°C, 20% to 80% RH (non condensing)</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-25°C to +85°C, 20% to 80% RH (non condensing)</td>
</tr>
</tbody>
</table>

Note 2: One-second interval with keeping Bluetooth® connection

![Image of RM-110-RFB-2 module dimensions]

Figure 1-2 RM-110-RFB-2 module dimension (Top View)
2. **RM-110-RFB-2 module connection**

2.1 **Development interface (CN1)**

At bottom layer, the module has 2x12 pin connector CN1: part number 14 5602 024 000 829H+ by Kyocera connector. This CN1 connector can be attached to custom design board or extension board known as Bluetooth® Low Energy evaluation board, R0K3ZBBBDBN00BR through its mating connector part numbers, 14 5602 024 000 829S+. Thus, you can program or debug to the RM-110-RFB-2 module via extension board and for detail, refer to RL78/G1D User’s Manual: Evaluation Board [3]. To secure connection to extension board, use screws and nuts as shown in Figure 2-1.

![Figure 2-1 RM-110-RFB-2 module mounting on R0K3ZBBBDBN00BR board](image)

Table 2 shows the connector CN1 pin assignment between RM-110-RFB-2 module and the custom design board or extension board, R0K3ZBBBDBN00BR.

**Table 2 Connector pin assignment for CN1**

<table>
<thead>
<tr>
<th>Pin Number</th>
<th>Signal Name</th>
<th>Pin Number</th>
<th>Signal Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>VSS_1</td>
<td>2</td>
<td>VDD</td>
</tr>
<tr>
<td>3</td>
<td>P40/TOOL0</td>
<td>4</td>
<td>No Connection</td>
</tr>
<tr>
<td>5</td>
<td>RESET</td>
<td>6</td>
<td>No Connection</td>
</tr>
<tr>
<td>7</td>
<td>P60/SCLA0</td>
<td>8</td>
<td>P61/SDA0</td>
</tr>
<tr>
<td>9</td>
<td>P03/ANI16/RXD1</td>
<td>10</td>
<td>P11/SI00/RXD0/TOOLRXD/SDA00</td>
</tr>
<tr>
<td>11</td>
<td>P147/ANI18</td>
<td>12</td>
<td>P12/SO00/TXD0/TOOLTXD</td>
</tr>
<tr>
<td>13</td>
<td>P02/ANI17/SIO/TXD1</td>
<td>14</td>
<td>P10/SCK00/SCL00</td>
</tr>
<tr>
<td>15</td>
<td>TXSELH_RF/GPIO0</td>
<td>16</td>
<td>P22/ANI2</td>
</tr>
<tr>
<td>17</td>
<td>XSELL_RF/GPIO1</td>
<td>18</td>
<td>P23/ANI3</td>
</tr>
<tr>
<td>19</td>
<td>No Connection</td>
<td>20</td>
<td>P16/T10/TO01/INTP5</td>
</tr>
<tr>
<td>21</td>
<td>VSS_21</td>
<td>22</td>
<td>P30/INTP3/RTC1HZ</td>
</tr>
<tr>
<td>23</td>
<td>P120/ANI19</td>
<td>24</td>
<td>No Connection</td>
</tr>
</tbody>
</table>
2.2 External expansion terminal (TH)

The RM-110-RFB-2 module provides the through-hole (TH) terminals for the expansion of the module. There are three unpopulated through-hole connectors and they are assigned number from 1 to 25. The assigned through-hole pin from 1 to 9 and from 18 to 25 are single in line 0.1 inch connector for 9 pins and 8 pins respectively. The assigned through-hole pin from 10 to 17 is dual in line 0.1 inch connector. Pin 18 and 19 has two-pin connector for selecting UART communication modes. When insert the jumper at this connector, the interface UART setting will be UART 2-wire branch connection. Removing the jumper will set Simple 2-wire UART mode.

In addition, the module has two ground terminals, one Vdd terminal, and one 2-pin jumper (JP1) for power measurement.

Figure 2-2 RM-110-RFB-2 Module Pin Configuration

The pin assignment of the through-hold terminals are as listed in Table 3.

Table 3 Pin Assignment of Expansion Terminal

<table>
<thead>
<tr>
<th>No.</th>
<th>TH</th>
<th>Signal name</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>P61</td>
<td>P61/SDAA0</td>
</tr>
<tr>
<td>2</td>
<td>P60</td>
<td>P60/SCLA0</td>
</tr>
<tr>
<td>3</td>
<td>P123</td>
<td>P123/XT1</td>
</tr>
<tr>
<td>4</td>
<td>P124</td>
<td>P124/XT2/EXCLKS</td>
</tr>
<tr>
<td>5</td>
<td>P137</td>
<td>P137/INTP0</td>
</tr>
<tr>
<td>6</td>
<td></td>
<td>RESET</td>
</tr>
<tr>
<td>7</td>
<td>P40</td>
<td>P40/TOOLO</td>
</tr>
<tr>
<td>8</td>
<td>P120</td>
<td>P120/ANI19</td>
</tr>
<tr>
<td>9</td>
<td>P00</td>
<td>P00/TI00</td>
</tr>
<tr>
<td>10</td>
<td>P01</td>
<td>P01/TO00</td>
</tr>
<tr>
<td>11</td>
<td>P03</td>
<td>P03/ANI16/RxD1</td>
</tr>
<tr>
<td>12</td>
<td>P02</td>
<td>P02/ANI17/TxD1</td>
</tr>
<tr>
<td>13</td>
<td>P20</td>
<td>P20/ANI0/AVREFP</td>
</tr>
<tr>
<td>14</td>
<td>P21</td>
<td>P21/ANI1/AVREFM</td>
</tr>
<tr>
<td>15</td>
<td>P23</td>
<td>P23/ANI3</td>
</tr>
<tr>
<td>16</td>
<td>P22</td>
<td>P22/ANI2</td>
</tr>
<tr>
<td>17</td>
<td>P147</td>
<td>P147/ANI18</td>
</tr>
<tr>
<td>18</td>
<td>P30</td>
<td>P30/INTP3</td>
</tr>
<tr>
<td>19</td>
<td>P11</td>
<td>P11/SI00/RxD0/TOOLLx0D/SDA00/(TI06)/(TO06)</td>
</tr>
<tr>
<td>20</td>
<td>P15</td>
<td>P15/SCK20/SCL20/(TI02)/(TO02)</td>
</tr>
<tr>
<td>21</td>
<td>P14</td>
<td>P14/SI20/(SDAA0)/(SCLA0)/(TI03)/(TO03)</td>
</tr>
<tr>
<td>22</td>
<td>P13</td>
<td>P13/SO20/(SDAA0)/(TI04)/(TO04)</td>
</tr>
<tr>
<td>23</td>
<td>P12</td>
<td>P12/S000/TxD0/TOOLTx0D/(TI05)/(TO05)</td>
</tr>
<tr>
<td>24</td>
<td>P16</td>
<td>P16/TI01/TO01/INTP5</td>
</tr>
<tr>
<td>25</td>
<td>P10</td>
<td>P10/SCK00/SCL00/(TI07)/(TO07)</td>
</tr>
</tbody>
</table>

Note: The function in parentheses can be assigned by setting of the peripheral I/O redirection register (PIOR). For such a signal function, refer to the RY7011A0000DZ00 module manual [2] and RL78/G1D hardware user manual [4].
2.3 Programming and Debugging

Using ground (GND), power (VDD), 7 (TOOL0), and 6 (RESET) terminals, this module can be programmed or debugged by using Renesas E1 programmer/debugger. Table 4 shows the wiring connection for programming and debugging. For detail about how to program or debug, refer to RL78/G1D Module (RY7011), User’s Manual: Hardware [2]. You can also use with extension board for programming or debugging and refer RL78/G1D User’s Manual: Evaluation Board [3].

Table 4 Wiring between RL78/G1D Module and Dedicated Flash Memory Programmer

<table>
<thead>
<tr>
<th>Pin Configuration of Dedicated Flash Memory Programmer</th>
<th>Signal Name</th>
<th>Pin No. / Pin Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>PG-FP5, FL-PR5</td>
<td>TOOL0</td>
<td>I/O</td>
</tr>
<tr>
<td>E1 On-Chip debugging emulator</td>
<td>Transmit/receive signal</td>
<td>TOOL0/P40</td>
</tr>
<tr>
<td>SI/RxD</td>
<td>/RESET</td>
<td>Output</td>
</tr>
<tr>
<td></td>
<td>Transmit/receive signal</td>
<td>RESET</td>
</tr>
<tr>
<td>VDD</td>
<td>I/O</td>
<td>VDD voltage generation/Power monitoring</td>
</tr>
<tr>
<td>GND</td>
<td>Ground</td>
<td>GND</td>
</tr>
<tr>
<td>FLMD1 EMVDD</td>
<td>–</td>
<td>Driving power for TOOL0 pin</td>
</tr>
</tbody>
</table>

2.4 Battery holder

To be operated by itself, the battery holder is needed to solder on this RM-110-RFB-2 module. The Figure 2-3 shows this module with battery holder installed. For typical application, if the module is interfaced with a sensor, it can demonstrate a sensor network.

Figure 2-3 RM-110-RFB-2 Module with Installed Coin Cell Battery

The suitable battery holder for this module is compact 20 mm type battery holder, part number 1066 manufactured by KEYSTONE. The CR2032 Coin cell battery can be used with this battery holder, which the Figure 2-4 shows the positive (+) terminal and negative (-) terminal location on the module to be soldered.

Figure 2-4 Battery Connection Terminals
3. RY7011A0000DZ00 Module Antenna Radiation

3.1 Antenna radiation pattern (directional)

This RM-110-RFB-2 module is mounted on the extension board (RTK0EN0001D01001BZ) for evaluation. The module sends the transmission packet at frequency 2440MHz as of below figures show the antenna radiation pattern (directional).

Note that the below antenna radiation patterns are approximate. Thus, the measurement value will vary depending on the extension board that to be used and surrounding environment.
Figure 3-2 Antenna radiation pattern (y-axis rotation)

Figure 3-3 Antenna radiation pattern (z-axis rotation)
3.2 Communication distance characteristics

For evaluation, the RM-110-RFB-2 module is attached with the extension board (RTK0EN0001D01001BZ) and the below measurement results show the characteristics of signal strength with respect to distance and of the transmission packet with respect to distance.

![Figure 3-4 Communication measurement (RSSI value)](image)

![Figure 3-5 Communication measurement (number of packets)](image)

Noted: The characteristic of measurement is reference only. It depends on the extension board to be used and the surrounding environment.
4. Schematics of RM-110-RFB-2 Module
Appendix A - RY7011A0000DZ00 Bluetooth module info

1. Pin Configuration (Top View)

![Pin Configuration Diagram]

IC: Internally connected

2. Block Diagram

![Block Diagram]

RL78/G1D

LC filter for
DC-DC converter
Pattern
antenna
RESET
GPIO
GND
TxD0
RxD0
UART
Exposed
GNDs
XTAL
32 MHz
RL78/G1D
22
11
TxD1
RxD1
4
P130
RFCTLEN
PCLBUZ0
EXSLK_RF
XT2XT1
3. Module Drawings

<table>
<thead>
<tr>
<th>JEITA Package Code</th>
<th>RENESAS Code</th>
<th>Previous Code</th>
<th>MASS [Typ.]</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>MLZZ0042ZA-A</td>
<td></td>
<td>0.357 g</td>
</tr>
</tbody>
</table>

**Detail F**

Reference Symbol | Dimension in Millimeters |
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>D</td>
<td>13.20 13.35 13.50</td>
</tr>
<tr>
<td>E</td>
<td>8.80 8.95 9.10</td>
</tr>
<tr>
<td>A</td>
<td>— 1.7</td>
</tr>
<tr>
<td>b</td>
<td>— 0.85</td>
</tr>
<tr>
<td>Lp</td>
<td>0.57 0.65 0.73</td>
</tr>
<tr>
<td>b</td>
<td>0.52 0.6 0.68</td>
</tr>
<tr>
<td>y</td>
<td>— 0.1</td>
</tr>
</tbody>
</table>
Appendix B - References

Appendix C - Conformity Assessment

FCC/IC Regulatory
Since this module is not sold to general end users directly, there is no user manual of module.
For the details about this module, please refer to the specification sheet of module.
This module should be installed in the host device according to the interface specification (installation procedure).
The following information must be indicated on the host device of this module;

Contains FCC ID: 2AEMXY7011A00000
This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.
Contains IC: 20194-Y7011A00000
The following statements must be described on the user manual of the host device of this module;

[for FCC]
FCC CAUTION
Changes or modifications not expressly approved by the party responsible for compliance could void the user’s authority to operate the equipment.
This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment and meets the FCC radio frequency (RF) Exposure Guidelines. This equipment has very low levels of RF energy that it deemed to comply without maximum permissive exposure evaluation (MPE). But it is desirable that it should be installed and operated keeping the radiator at least 20cm or more away from person's body.

[for IC]
This device complies with Industry Canada's licence-exempt RSSs. Operation is subject to the following two conditions: (1) This device may not cause interference; and (2) This device must accept any interference, including interference that may cause undesired operation of the device.
Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : 1) l'appareil ne doit pas produire de brouillage; 2) l'utilisateur de l’appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d’en compromettre le fonctionnement.

This equipment complies with IC radiation exposure limits set forth for an uncontrolled environment and meets RSS-102 of the IC radio frequency (RF) Exposure rules. This equipment has very low levels of RF energy that it deemed to comply without maximum permissible exposure evaluation (MPE). But it is desirable that it should be installed and operated keeping the radiator at least 20cm or more away from person’s body.
Cet équipement est conforme aux limites d’exposition aux rayonnements énoncées pour un environnement non contrôlé et respecte les règles d’exposition aux fréquences radioélectriques (RF) CNR-102 de l’IC. Cet équipement émet une énergie RF très faible qui est considérée conforme sans évaluation de l’exposition maximale autorisée. Cependant, il est souhaitable qu’il devrait être installé et utilisé en gardant une distance de 20 cm ou plus entre le radiateur et le corps humain.
**Japan Radio Law**

Contains MIC ID: 007-AE0104

This device complies with the Japan Radio Law (Law No. 131, 1950) and Amendments.

**R&TTE Directive**

Hereby declare that this product is required the final certification tests and CE marking in order to compliance with the essential requirements and other EC relevant provisions.
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http://www.renesas.com/

Inquiries
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## Revision History

<table>
<thead>
<tr>
<th>Rev.</th>
<th>Date</th>
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<th>Summary</th>
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</thead>
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<tr>
<td>1.00</td>
<td>Sep. 30, 2016</td>
<td>—</td>
<td>First edition issued</td>
</tr>
</tbody>
</table>
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The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

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   — The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

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   The state of the product is undefined at the moment when power is supplied.
   — The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
   In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.
   In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

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   Access to reserved addresses is prohibited.
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4. Clock Signals
   After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.
   — When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

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